

CSE 251

Electronic Devices and CIRCUITS

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Group: 1

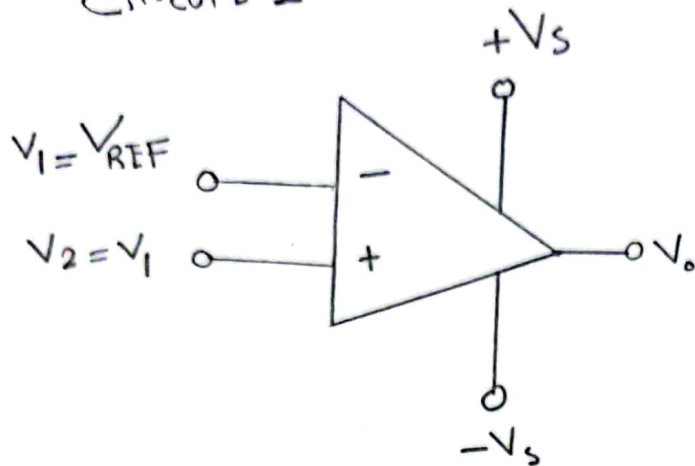
Semester: Fall '22

Date of performance: 25.11.22

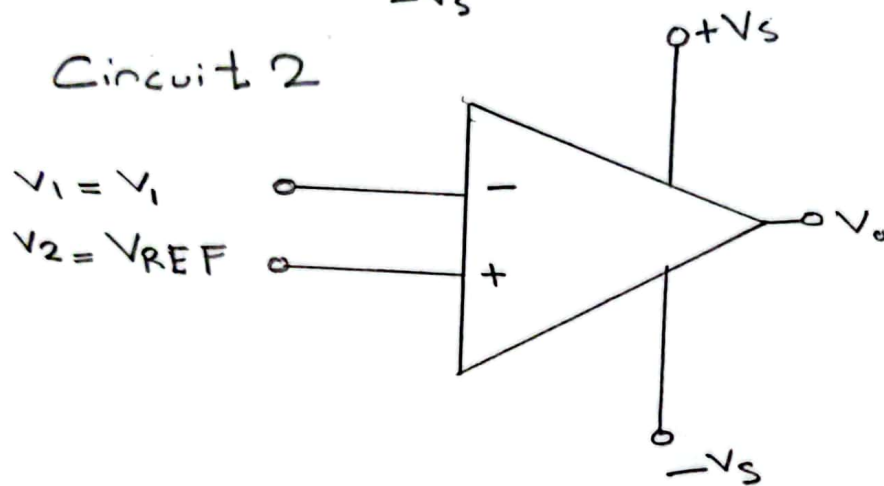
Date of submission: 26.11.22

Task 1: Op-Amp Comparator Circuits

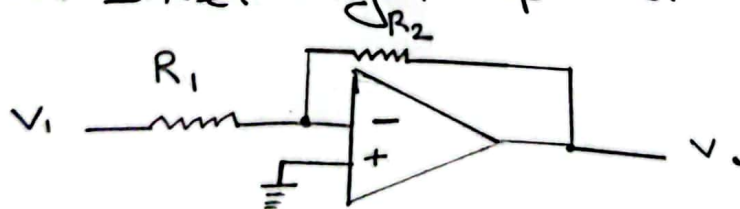
Circuit 1



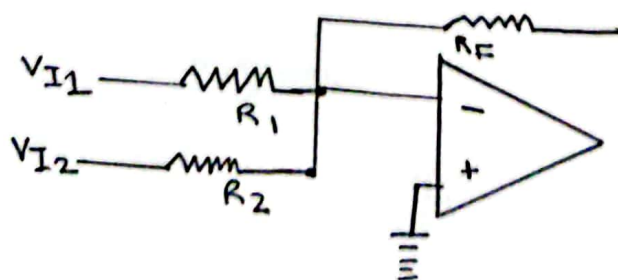
Circuit 2



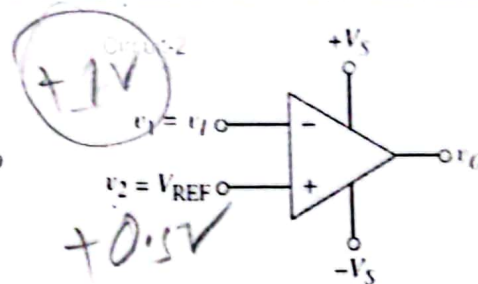
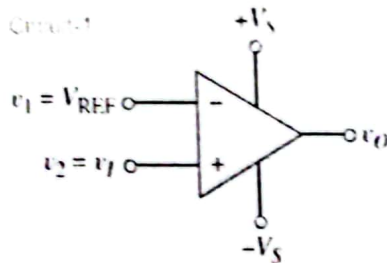
Task 2: Inverting Amplifier Circuits



Task 3: Inverting Summing Amplifier



Task-01: Op-Amp Comparator



Handwritten calculations:

$$V_O = A(V_+ - V_-)$$

$$= A(0.5 - 1)$$

$$= A(-0.5)$$

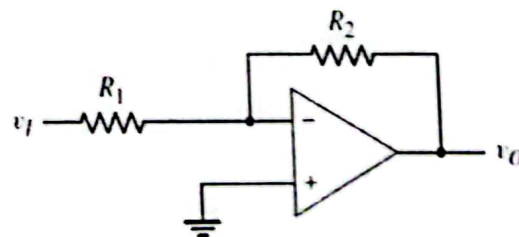
$$= -\text{large}$$

$$= -V_{\text{big}}$$

Procedure

1. Construct Circuit-1 with $v_I = 2 \text{ V}$ (p-p), 1 kHz sine wave and $V_{REF} = 0.5 \text{ V}$. The supply voltage $+V_S$ and $-V_S$ should be $+10 \text{ V}$ and -10 V respectively which can be taken from the trainer board. Use this supply voltage throughout the experiment. The input voltage v_I can be taken from the oscilloscope.
2. Connect the Ch1 and Ch2 of the oscilloscope to v_I and v_O respectively. Observe the input and output waveform and capture them using a camera.
3. Now, construct Circuit-2 and repeat the experiment with same values given above. Observe the input and output waveform and capture them using a camera.

Task-02: Inverting Amplifier



Procedure

1. Construct the circuit with $v_I = 2 \text{ V}$ (p-p), 1 kHz sine wave. Use $R_1 = 1 \text{ k}\Omega$, $R_2 = 2.7 \text{ k}\Omega$.
2. Connect the Ch1 and Ch2 of the oscilloscope to v_I and v_O respectively. Observe the input and output waveform and capture them using a camera.

Observation and Calculation

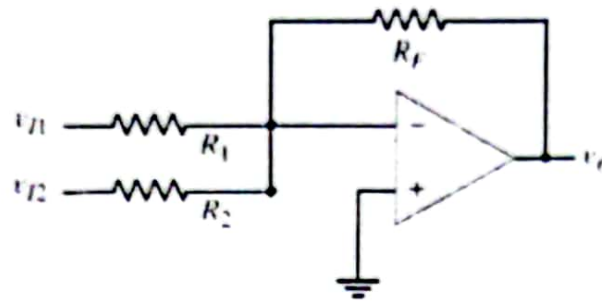
The output waveform should be amplified and inverted compared to the input waveform.

Input Amplitude from oscilloscope, $v_I = 1.88$

Output Amplitude from equation, $v_O = -\left(\frac{R_2}{R_1}\right) \times v_I = -\left(\frac{2.7}{1}\right) \times 1.88 = -5.154$

Output Amplitude from oscilloscope, $v_O = -5.20 \text{ V}$

Task-03: Inverting Summing Amplifier



Procedure

Part-01

1. Construct the circuit with $v_{I1} = 2\text{ V}$, $v_{I2} = 1\text{ V}$. Use $R_1 = 1\text{ k}\Omega$, $R_2 = 2.7\text{ k}\Omega$, $R_F = 2.7\text{ k}\Omega$.
2. Use the digital multimeter to measure the output voltage.

Part-02

1. Construct the circuit with $v_{I1} = 2\text{ V}$ (from the DC Supply) and $v_{I2} = 2\text{ V}$ (p-p), 1 kHz sine wave. Use $R_1 = 1\text{ k}\Omega$, $R_2 = 2.7\text{ k}\Omega$, $R_F = 2.7\text{ k}\Omega$.
2. Connect the Ch1 and Ch2 of the oscilloscope to v_I and v_O respectively and observe the waveforms.

Observation and Calculation

For Part-01,

from multimeter, $v_{I1} = 1.98\text{ V}$

from multimeter, $v_{I2} = 5.02\text{ V}$

Output Amplitude from equation, $v_O = -\left(\frac{R_F}{R_1} \times v_{I1} + \frac{R_F}{R_2} \times v_{I2}\right) = \frac{2.7}{0.982} \times 1.98 + \frac{2.7}{2.692} \times 5.02 = -10.51\text{ V}$

Output Amplitude from multimeter, $v_O = -9.11\text{ V}$

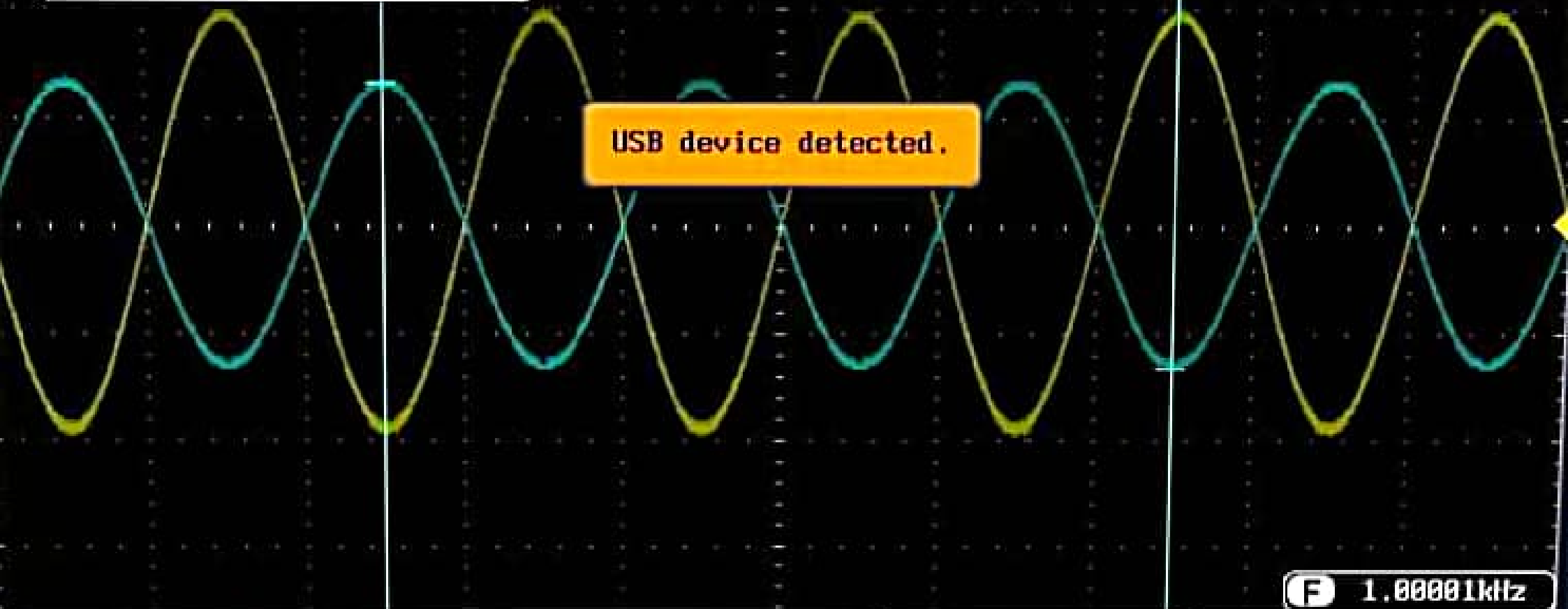
For Part-02,

The output waveform should be amplified and inverted compared to the input waveform. Observe the input and output waveform and capture them using a camera.



①	-1.25ms	2.64V
②	1.25ms	-2.64V
	$\Delta 2.50\text{ms}$	$\Delta 5.28\text{V}$
	dV/dt	-2.11kV/s

USB device detected.



F 1.00001kHz

① = 500mV ② = 2.00V

500us 0.00000s

① f 20nV DC

Coupling
DC AC GND

Impedance
1M Ω

Invert
On Off

Bandwidth
Full

Expand
By Ground

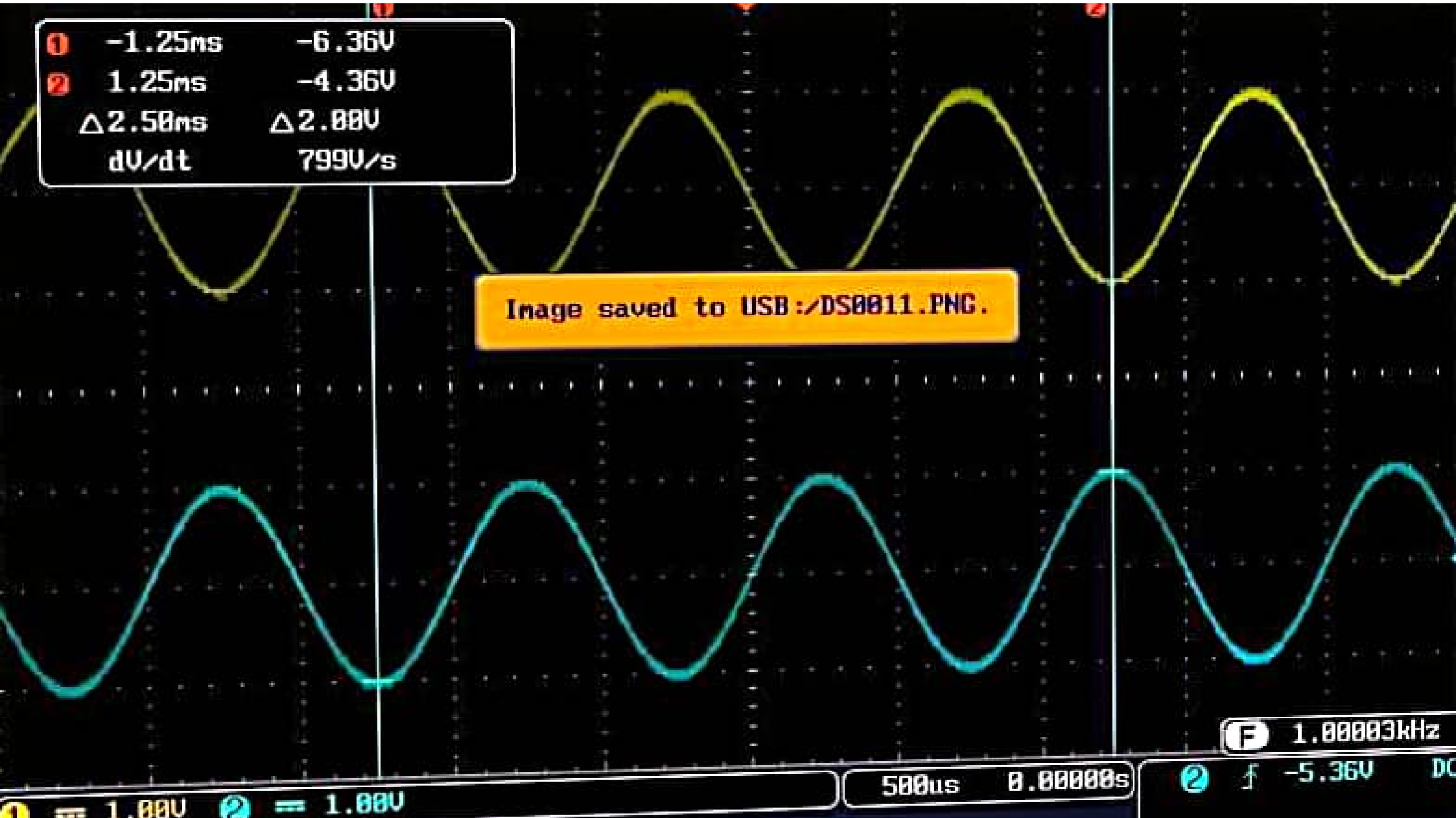
Position /
Set to 0
0.000V

Probe
Voltage
1X

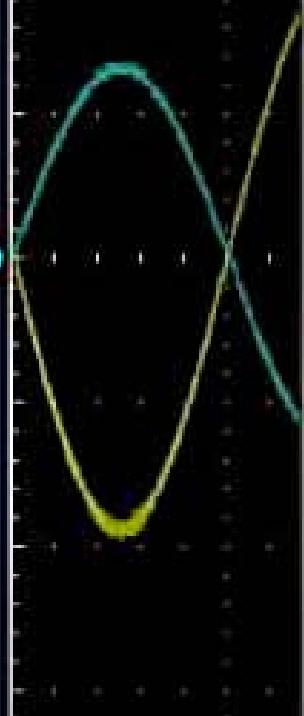


①	-1.25ms	-6.36V
②	1.25ms	-4.36V
$\Delta 2.50\text{ms}$		$\Delta 2.00\text{V}$
dV/dt		799V/s

Image saved to USB :/DS0011.PNG.



1 -1.25
2 1.25m
Δ2.50m
dU/dt



2 Measurement Summary

Pk-Pk	5.36V	Frequency	1.001kHz
Max	2.72V	Period	999.0us
Min	-2.64V	RiseTime	288.3us
Amplitude	5.20V	FallTime	289.6us
High	2.64V	+Width	511.5us
Low	-2.56V	-Width	487.5us
Mean	31.1nV	Dutycycle	51.20%
CycleMean	31.5nV	+Pulses	5
RMS	1.85V	-Pulses	4
CycleRMS	1.85V	+Edges	4
Area	155uVs	-Edges	5
CycleArea	31.5uVs		
ROVShoot	1.54%		
FOVShoot	1.54%		
RPRESShoot	1.54%		
FPRESShoot	1.54%		

- CH1
- CH2
- Math

Display All

Source
CH2

OFF

1 == 500mV 2 == 2.00V

500us 0.00000s

Add
Measurement

Remove
Measurement

Gating
Screen

Display All
CH2

High-Low
Auto

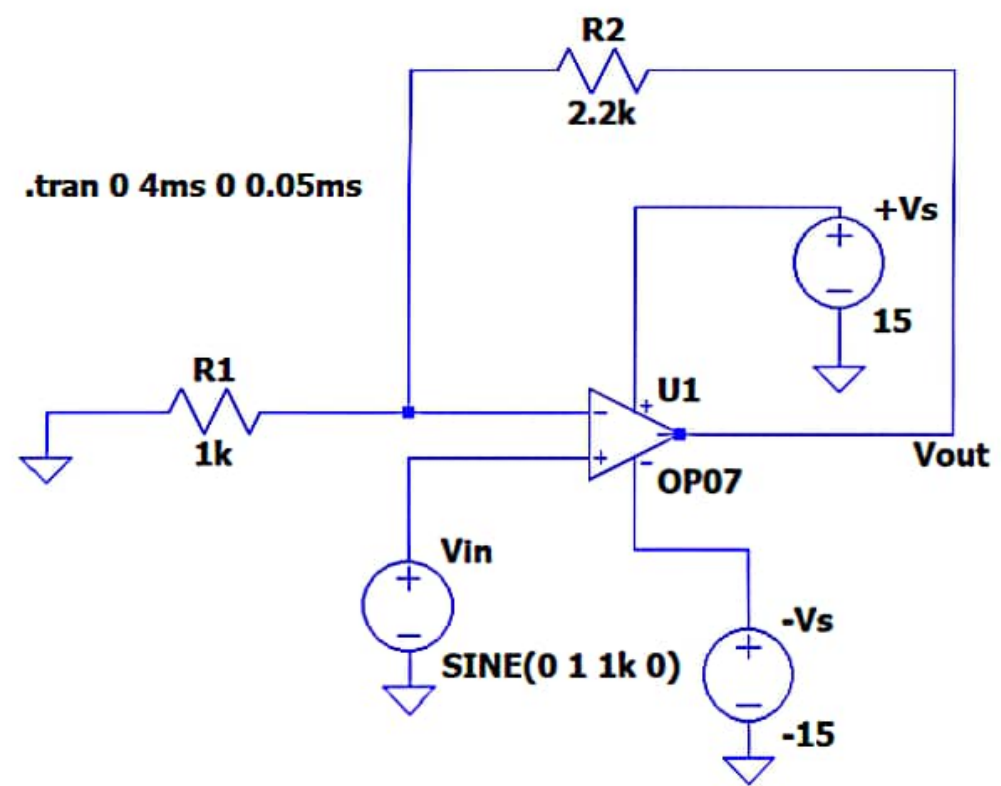
Statistics

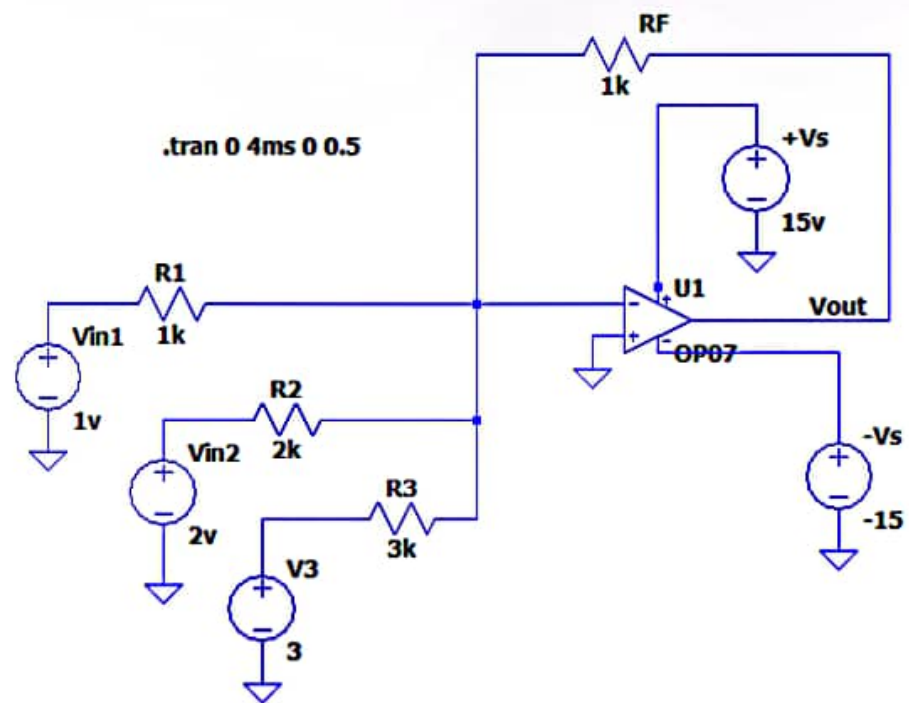
Reference
Levels

① -1.29ms -18.4V
② 1.28ms 8.80V
 $\Delta 2.58\text{ms}$ $\Delta 19.2\text{V}$
 dV/dt 7.68kV/s

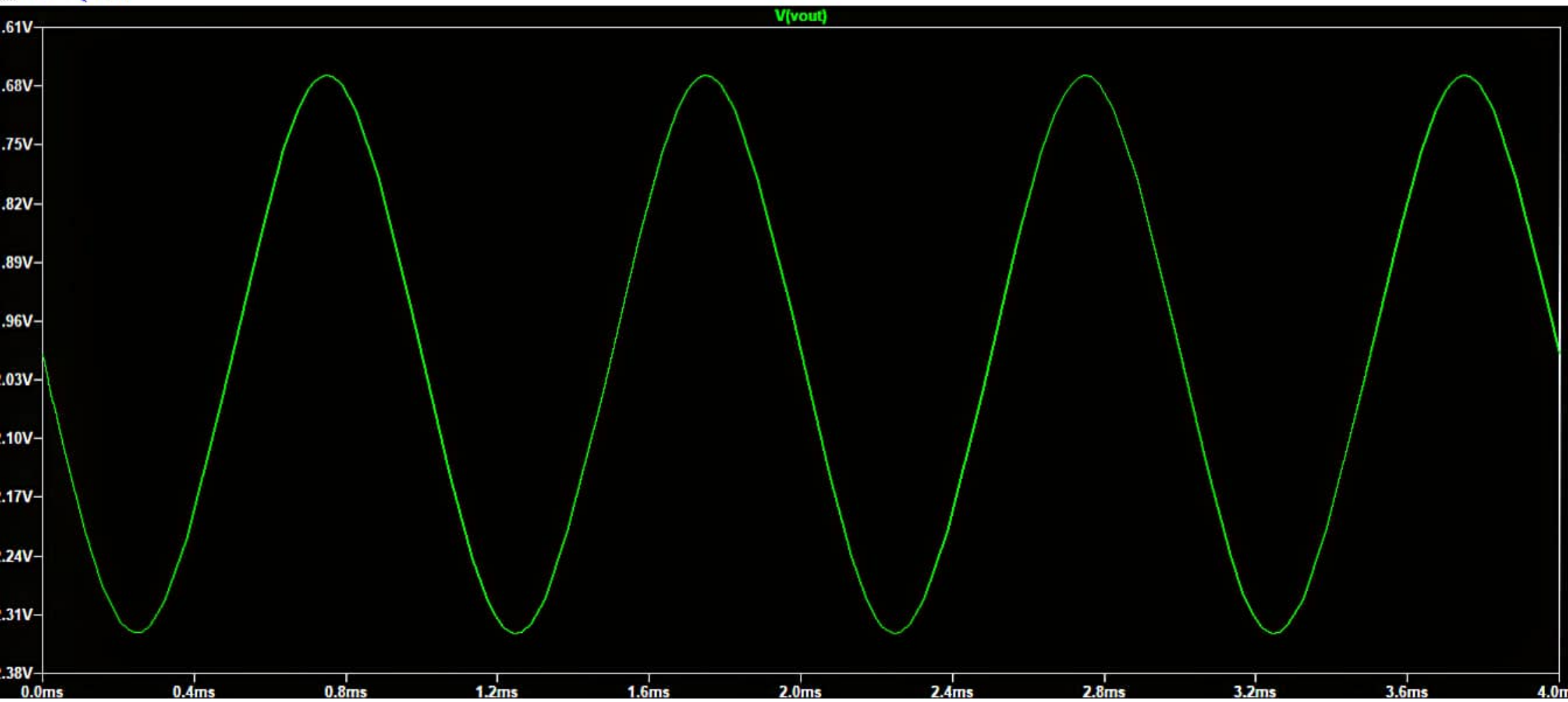
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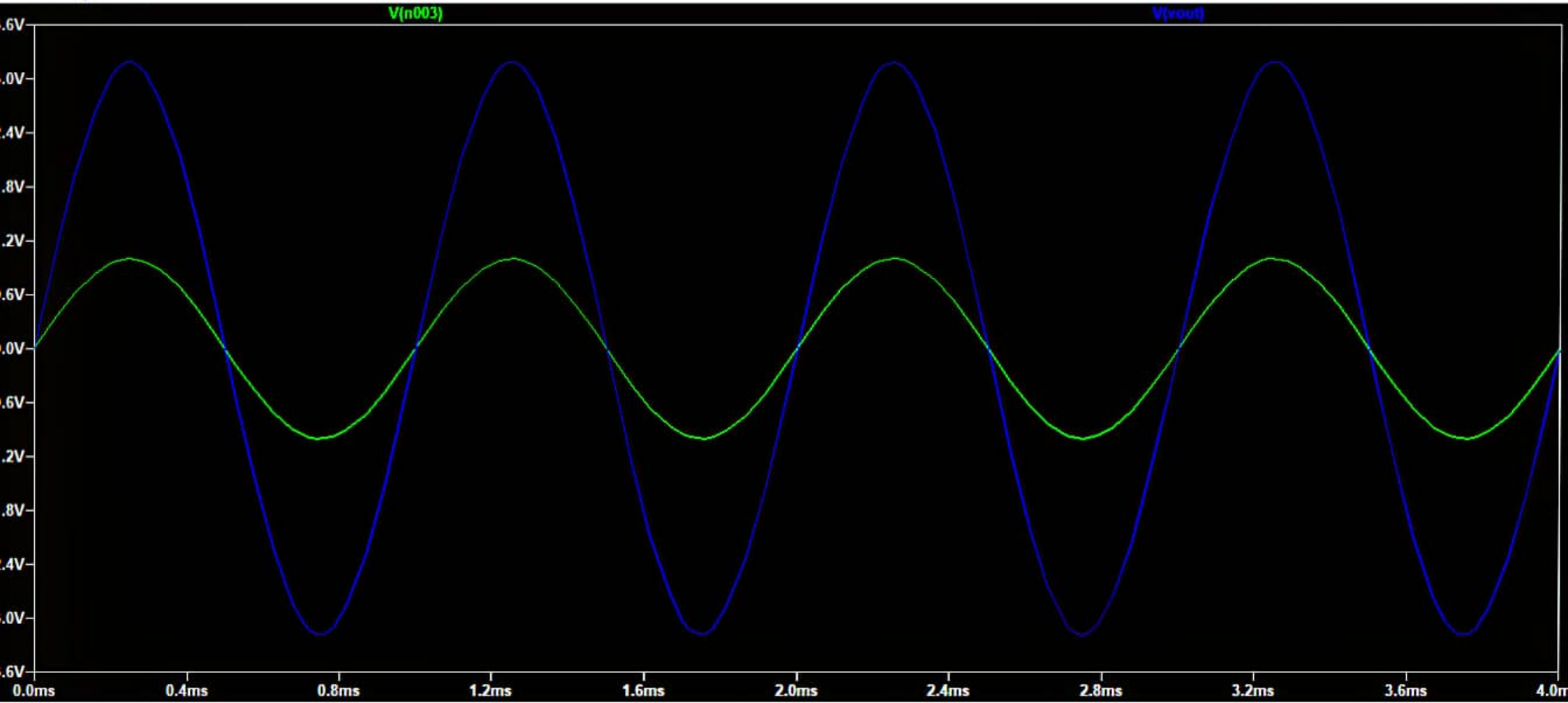
① = 1.00V ② = 10.0V 500us 0.00000s ② f 1.00000kHz -400mV DC





Draft1 Draft1





▣ Inverting amplifier : (From sheet)
→ Theoretically

$$V_1 = 2V$$

$$R_1 = 1K\Omega$$

$$R_2 = 2.7K\Omega$$

$$V_o = - \left(\frac{R_2}{R_1} \right) \times V_1$$

$$= - \frac{2.7}{1} \times 2$$

$$= -5.4V$$

▣ Inverting Summing Amplifier :
(From equation)

$$\text{Part 1 : } V_{I1} = V_{I2} = 1V$$

$$R_1 = 1K\Omega$$

$$R_2 = 2.7K\Omega$$

$$R_F = 2.7K\Omega$$

$$\therefore V_o = - \left(\frac{R_F}{R_1} \times V_{I1} + \frac{R_F}{R_2} \times V_{I2} \right)$$

$$= - \left(\frac{2.7}{1} \times 1 + \frac{2.7}{2.7} \times 1 \right)$$

$$= -(2.7 + 1)$$

$$= -3.7$$

$$\text{Part 2 : } V_{I1} = 2V, V_{I2} = 2V$$

$$R_1 = 1K\Omega, R_2 = 2.7K\Omega, R_F = 2.7K\Omega$$

$$\therefore V_o = - \left(\frac{2.7}{1} + \frac{2.7}{2.7} \right) \times 2$$

$$= -(2.7 + 1) \times 2$$

$$= -(3.7) \times 2$$

$$= -7.4V$$

Discussion: While doing the experiment there was a fluctuation of the voltage source. We also faced problem in the voltage meter. There were also problem when implementing the circuit in breadboard.