

**Cannonball
Platform Firmware
Release Notes**

Revision: 003
Date: 07-10-2017

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II. Overview

This document will provide a brief overview of the Cannonball Sequence CPLD firmware changes, and the firmware components affected by the changes for this release.

III. Version

Firmware Component	Version	Release Date
CPLD firmware	V01	04/20/2017
CPLD firmware	V02	06/16/2017
CPLD firmware	V03	07/10/2017

IV. Cross Dependency

Firmware Component	Version	Remarks
N/A	N/A	N/A

v. Specification Compliance

Specification	Revision
Intel Platform Design Guide	546835_PDG_Purley_546835_v0_95

VI. CPLD Changes

A. Version

Component		Version / Comments
CPLD	CPLD Version	V03
	CPLD Binary filename	Cannonball_SEQ_EVT_V03.jed
	CPLD Binary File Checksum	0xECBD
	Motherboard Hardware Revision	X01

B. Changes and Bug Fixes

Feature/Issue	Description	Remarks
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Add I2C Bus function	BMC can read the CPLD version via I2C bus	N/A
Add the SGPIO function	BMC can read the CPLD version via SGPIO	N/A
DC Auto power on issue	When DC off, user can't press the power button to power on system	N/A
ADR logic	Fine tune the ADR logic	N/A

C. Known Issues

N/A

D. Notes

Lattice Jtag cable and Diamond programmer tool for programming CPLD

Cannonball CPLD update instruction

1. Chan the path to CPLD folder
2. Run ./update_cpld.sh(TBD)

E. Change Log

Release Version	Feature/Issue	Description	Remarks
V02	Support the LED debug function	Add the power up related signal to Debug LED	N/A
V02	Add the BMC on control logic	Add the AND logic to PSU enable	N/A
V02	PCH 1V05 Aux issue	Change the 1V05 enable to high	N/A

Blacklist document

N/A

vii. Confidential Notice

This section will contain the confidential notice from the ODMs regarding this release notes and its contents.

viii. Disclaimer

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