Cannonball
Platform Firmware
Release Notes

Revision: 03
Date: 07-10-2017

I. **Contents**

II. Overview

This document will provide a brief overview of the Cannonball Management CPLD firmware changes, and the firmware components affected by the changes for this release.

III. Version

Firmware Component	Version	Release Date
CPLD firmware	V01	04/20/2017
CPLD firmware	V02	06/16/2017
CPLD firmware	V03	07/10/2017

IV. Cross Dependency

Firmware Component	Version	Remarks
N/A	N/A	N/A

V. Specification Compliance

Specification	Revision
Amazon Web Services Server Board Specification	800-027740-004

VI. CPLD Changes

A. Version

Component		Version / Comments
CPLD	CPLD Version	V03
	CPLD Binary filename	Cannonball_MGMT_EVT_V03.jed
	CPLD Binary File Checksum	0x7B77
	Motherboard Hardware Revision	X01

B. Changes and Bug Fixes

Feature/Issue	Description	Remarks

Add I2C Bus 11	In order to BMC can read the SEQ CPLD via MGMT CPLD	N/A

C.   Known Issues
N/A

D.   Notes
 Lattice Jtag cable and Diamond programmer tool for programming CPLD

Cannonball CPLD update instruction
1.   Chan the path to CPLD folder
2.   Run ./update_cpld.sh(TBD)

E.   Change Log

Release Version	Feature/Issue	Description	Remarks
V02	MCU Image	Update the MCU Image from Jax	
V02	I2C bridge	Robust and more logic-efficient, enabled on all BMC and PCH busses	
V02	SPI signals	Better treatment of the SPI signals so they remain tristated and do not have their internal pull-down enabled by default	
V02	LPC Switch Enable	Add the invert logic for enable LPC	

Blacklist document
N/A

VII. Confidential Notice
This section will contain the confidential notice from the ODMs regarding this release notes and its contents.

VIII.   Disclaimer
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