TAIC PART

Testing: Academic & Industrial Conference
Practice And Research Techniques

TAIC PART is a testing workshop that aims to forge collaboration between industry and academia on the challenging and exciting problem of software testing. It is sponsored by representatives of both industry and academia, bringing together commercial and industrial software developers and users with academic researchers working on the theory and practice of software testing.

The conference will be held at Cumberland Lodge, Windsor, UK, 12-14th September, 2007. Cumberland Lodge is a former royal residence given to the nation in 1946 by the late Queen Mother. It is an ideal setting for a productive and enjoyable conference, providing world-class conference facilities in an ideal location, which resonates with centuries of historical significance, dating back to the mid 17th century.

Call for Papers

Authors are invited to submit papers describing original research, experience, tools or industrial "challenges" in testing.

Research paper topics include:
- Theory of testing (adequacy criteria, relationship with formal methods etc.)
- Testing strategies (black-box, white-box, non-functional, state-based etc.)
- Test data generation techniques (search-based, constraint logic etc.)
- Model based testing and model checking
- Process issues and methods

Experience papers should describe industrial experience applying testing techniques and/or tools to commercial projects or open source solutions; the problems tackled and the results.

Tools papers should describe novel developments in environments and systems of potential benefit to the testing community.

Industrial "challenge" papers should have at least one author whose affiliation is non-academic and should describe an example of a real-world software testing problem, for which help is sought from the academic research community.

http://www.taicpart.org

Cumberland Lodge, Windsor, UK

General Chair Professor Mark Harman
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Industrial Sponsors

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The full proceedings will be published by the IEEE Computer Society. Materials for the proceedings should be typeset to conform to IEEE conference style guidelines. These are described and mirrored at http://www.taicpart.org/formatting.php. There is a page limit for all submissions. For research papers this is 10 pages in IEEE conference style format. For "challenge", "tools" and "experience" submissions the page limit is 5 pages in IEEE conference style format. All papers should be submitted electronically via the website – http://www.taicpart.org/papersubmission.php.

There will be a special issue of the Journal of Systems and Software (JSS) journal containing extended versions of selected papers from TAIC PART 2007. The special issue will have a focus on articles concentrating on the theme "Technology Transfer from Academia to Industry in Software Testing".

PhD Program

The workshop will include a half-day PhD program. The goal of the half-day PhD program is to provide a supportive yet questioning setting in which students can present their work. Students will be able to discuss their goals, methods, and results at an early stage in their research. The PhD program aims to provide useful guidance for completion of the dissertation research and initiation of a research career. Students should consider participating in the PhD program after having settled on a research area or dissertation topic. Submissions should be in the form of a research abstract and submitted to the website as for other papers. This should be a maximum of 5 pages long in IEEE conference style format. Abstracts will be reviewed separately from the other types of submission, but are subject to the same deadlines. For more information, contact David Willmor, email: d.willmor@cs.manchester.ac.uk, or visit the website – http://www.taicpart.org/phdprogram.php

Important Dates

Deadline for submission of papers:
Friday 13th April 2007

Workshop:
Wednesday 12th - Friday 14th September 2007

Program Committee (confirmed so far)

Academic Members

Giulio Antoniol, *University of Sannio*, Italy
Kirill Bogdanov, *University of Sheffield*, UK
David Binkley, *Loyola College Maryland*,USA
Jonathan Bowen, *London South Bank University*, UK
Myra Cohen, *University of Nebraska-Lincoln*, USA
John Clark, *University of York,* UK
Mireille Ducassé, *IRISA / INSA de Rennes*, France
Matthew Dwyer, *University of Nebraska-Lincoln,* USA
Mark Harman, *Kings College London*, UK
Mary Jean Harrold, *Georgia Institute of Technology,* USA
Rob Hierons, *Brunel University*, UK
Mike Holcombe, *University of Sheffield*, UK
Gregory Kapfhammer, *Allegheny College,* USA
Bogdan Korel, *Illinois Institute of Technology*, USA
Paul Krause, *University of Surrey*, UK
Yvan Labiche, *Carleton University*, Canada
Bev Littlewood, *City University London*, UK
Wassim Masri, *American University of Beirut*, Lebanon
John May, *University of Bristol*, UK
Phil McMinn, *University of Sheffield*, UK

Keynote Speakers

Michael Ernst, *MIT*, USA
(Discovering Program Specifications)

Andreas Zeller, *Saarland University*, Germany
(Automated Debugging)

Robby, *Kansas State University,* USA
Stuart Reid, *Cranfield University*, UK
Fillippo Ricca, *ITC-Irst Trento,* USA
Marc Roper, *University of Strathclyde*, UK
Mary Lou Soffa, *University of Virginia,* USA
Hasan Ural, *University of Ottawa*, Canada
Jianjun Zhao, *Shanghai Jiao Tong University*, China
Hong Zhu, *Oxford Brookes University*, UK

Industrial Members

Paul Baker, *Motorola*, UK
Sigrid Eldh, *Ericsson*, Sweden
Isabel Evans, *Testing Solutions Group Ltd,* UK
Paul Gerrard, *Gerrard Consulting,* UK
Mathai Joseph, *Tata Consultancy Services Ltd,* India
Mike Hennell, LDRA Software Technology, UK
Brian Shea, *Vizuri Ltd.*, UK
Clive Stewart, *IBM*, UK
Joachim Wegener, *DaimlerChrysler*, Germany