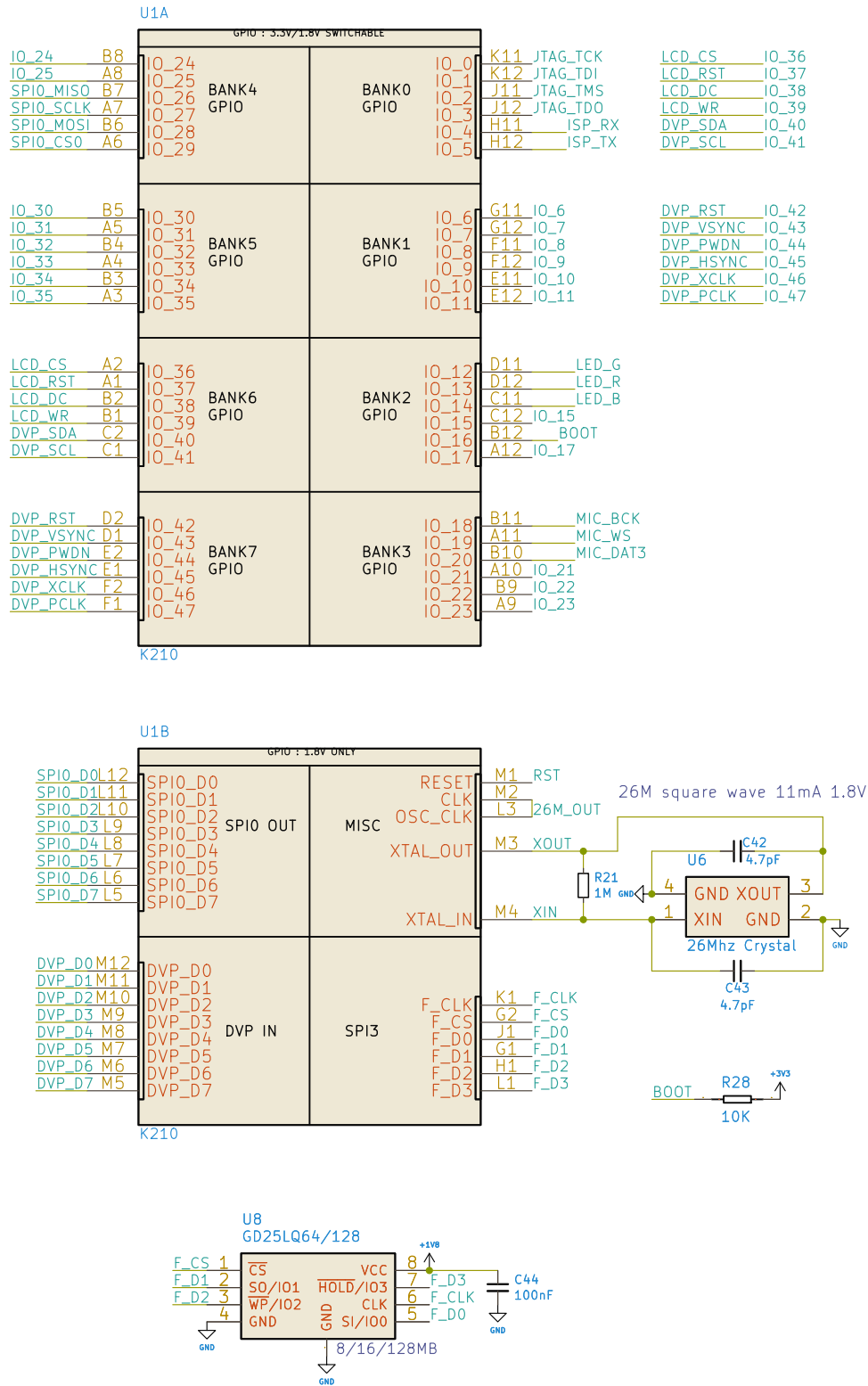


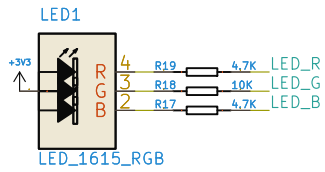
Core System



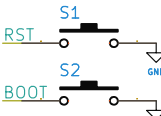
Note:

BOOT(I016) pull low to enter ISP mode, download from I04,5
SPI3 Support XIP SPI0/1/3 for Master, SPI2 for slave
3.3V IO speed 80M, 1.8V IO speed 208M. Simple test PWM is faster than 300MHz.

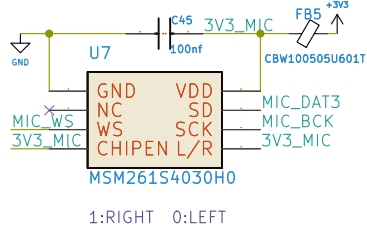
RGB LED



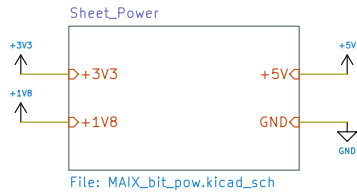
KEY



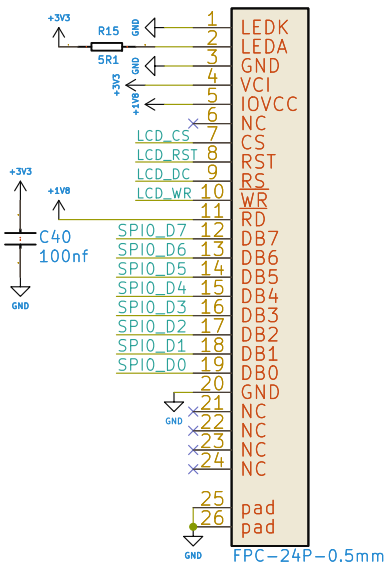
MIC



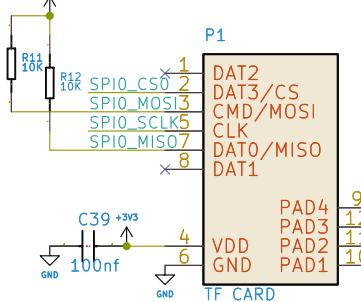
Power



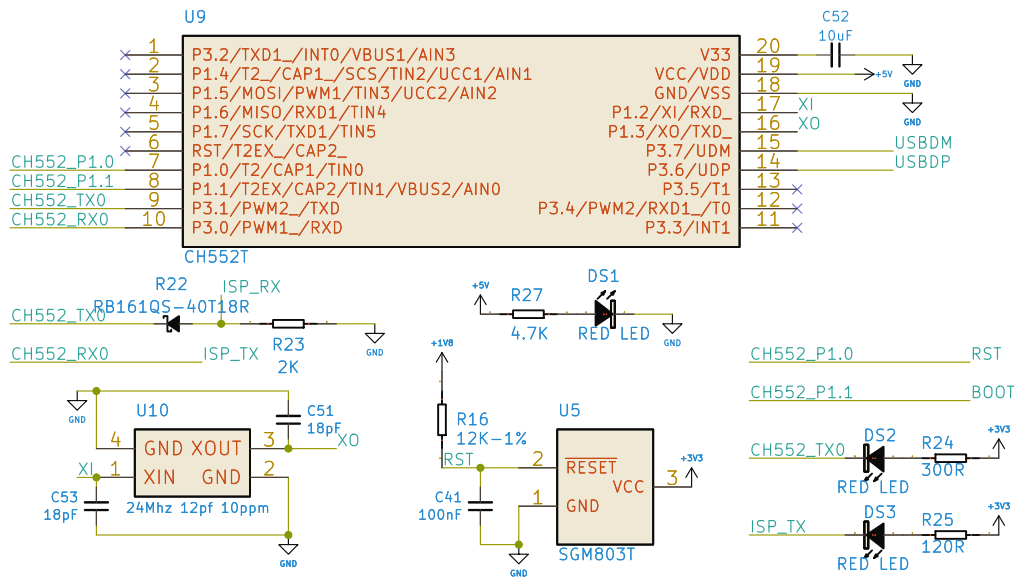
8bit MCU LCD



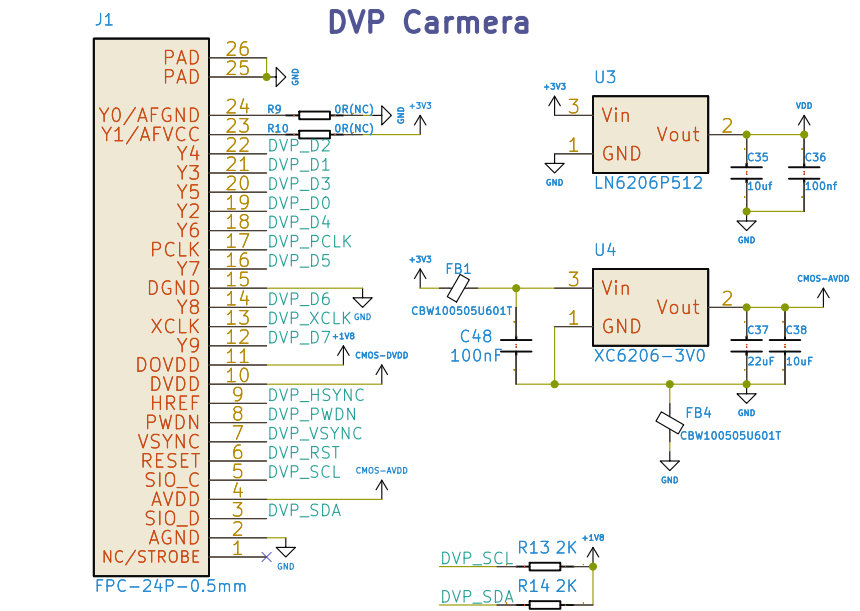
TF Card



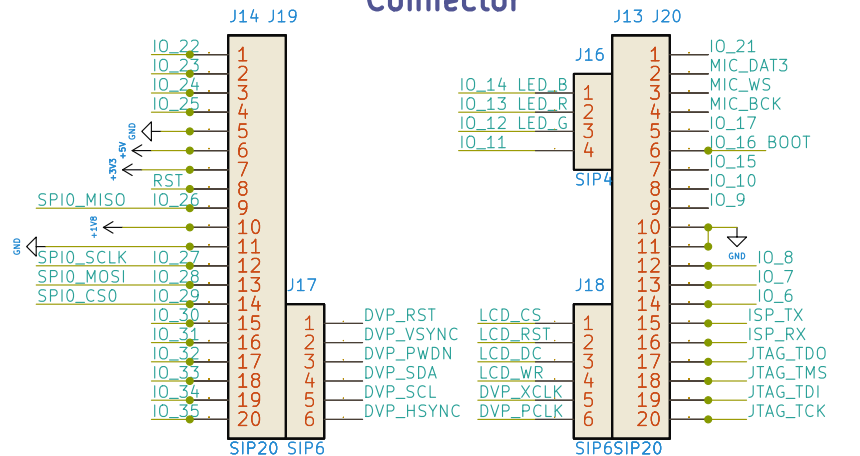
ISP DOWNLOAD



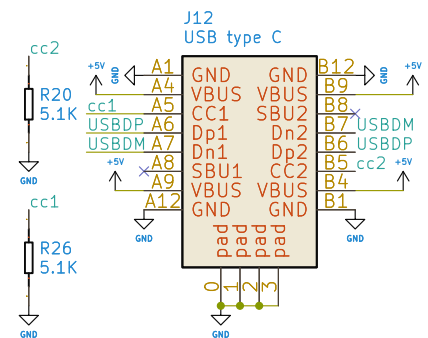
DVP Camera



Connector



USB Port



Open-source hardware with MaixPy, A.I. at edge.
Shenzhen Sipeed Technology Co, Ltd.

Sheet: /
File: MAIX_Bit_2832.kicad_sch

Title: Maix-Bit V2.1(Schematic)

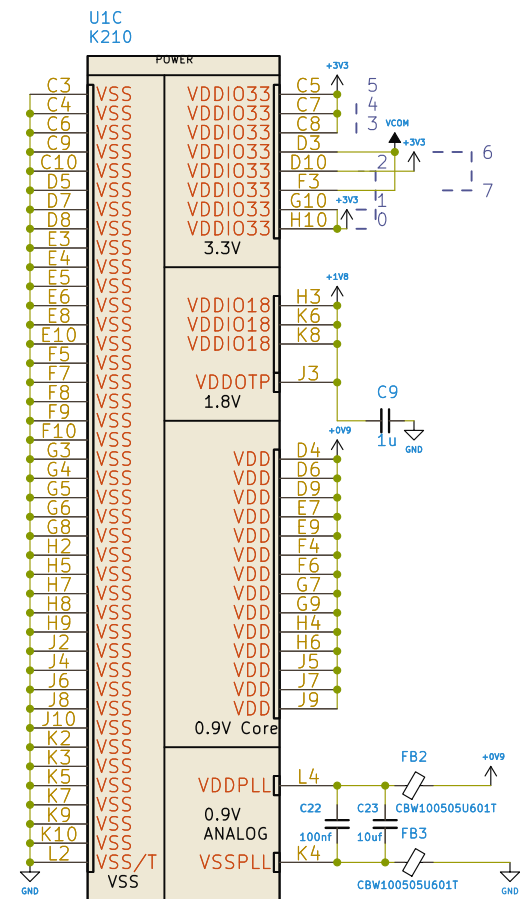
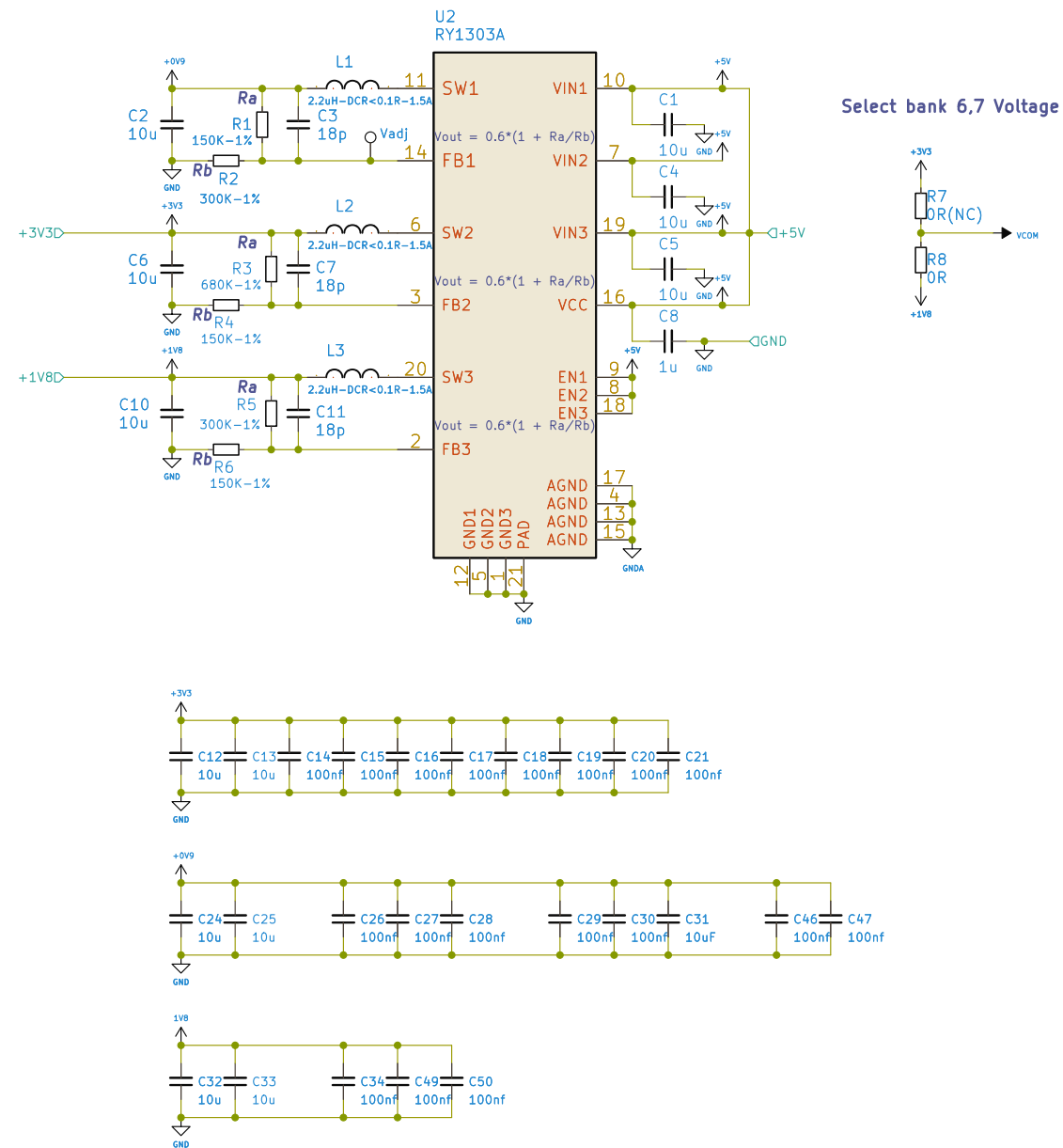
Size: B	Date: 2022-06-04
---------	------------------

KiCad E.D.A. eeschema (6.0.8)

Rev: 2.1

Id: 1/2

DCDC&LDO



Open-source hardware with MaixPy, A.I. at edge.

Shenzhen Sipeed Technology Co, Ltd.

Sheet: /Sheet_Power/

File: MAIX_bit_pow.kicad_sch

Title: Maix-Bit V2.1(Schematic)

Size: B

Date: 2022-06-04

Rev: 2.1

Size: 0	Date: 2022-11-11
KiCad E.D.A. eeschema (6.0.8)	

REV. 2.1

id: 2/2