

EE598: VLSI Design Automation

Grading rubrics for Mini Project 1 (STA)

| | c17 | c6288 | c7552 | b15_c | b20_c | Hidden | Total points |
|---|-----|-------|-------|-------|-------|--------|--------------|
| Parsing correctness | 2 | 2 | 2 | 2 | 2 | 2 | 12 + 5 (LUT) |
| Delay estimation | 4 | 4 | 4 | 4 | 4 | 3 | 23 |
| Slack estimation | 4 | 4 | 4 | 4 | 4 | 3 | 23 |
| Critical path | 2 | 2 | 2 | 2 | 2 | 2 | 12 |
| Coding style, documentation, commented code, modular code, etc. | | | | | | | 10 |
| Runtime (graded relatively) | 3 | 3 | 3 | 3 | 3 | - | 15 |
| Total | 15 | 15 | 15 | 15 | 15 | 10 | 100 |

Partial credit will be given for incorrect answers based on Professor Chhabria's discretion.

Best runtime numbers will get a full three points. Everyone's else runtime will be scaled to the best runtime for that specific benchmark.