

# OpenCL Parallel Reduction

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## **Agenda**

- Synchronization
- Parallel Reduction
  - CPU version
  - GPU variants
    - only one work-item performs the partial sum
    - previous version memory optimized
    - introducing binary sum
    - previous version optimize loop iterations
    - 2nd kernel for summing up final partial sums

#### Acknowledgements

This presentation is an excerpt of:

Introduction to OpenCL, Training course June 2012, George Leaver, University of Manchester



# **Synchronization**

- Synchronization can be performed between
  - work-items in a work-group
  - kernels (commands) in a command-queue
  - kernels (commands) in seperate queues within the same context
  - host and queues
- Synchronization can be enforced implicitly
  - in-order-queue: commands execute in order submitted
- Synchronization can be requested by user
  - out-of-order queue: commands scheduled by OpenCL
  - barriers in kernels and queues
  - events in queues



# **Work-item Synchronization**

- Only possible within a work-group
  - Can't sync with work-items in other work-groups
  - Can't sync one work-group with another
- Use barrier(type) in kernel where type is
  - CLK\_LOCAL\_MEM\_FENCE: ensure consistency in local memory
  - CLK\_GLOBAL\_MEM\_FENCE: ensure consistency in global memory
- All work-items in work-group must issue the barrier() call and same number of calls

```
__kernel void BadKernel(...) {
  int i = get_global_id(0);
  ...
  // ERROR: Not all WIs reach barrier
  if ( i % 2 )
    barrier(CLK_GLOBAL_MEM_FENCE);
}
__kernel void BadKernel(...) {
    int i = get_local_id(0);
    ...
  // ERROR: WIs issue different number
  for ( j=0; j<=i; j++ )
    barrier(CLK_LOCAL_MEM_FENCE);
}</pre>
```



#### **Use with \_\_local memory**

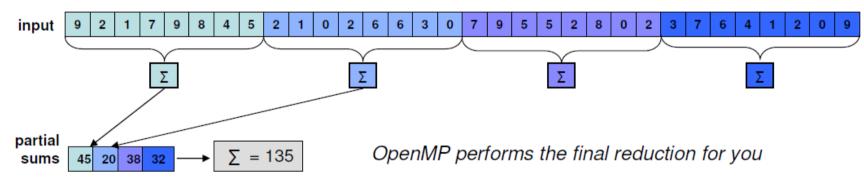
- Barrier often used when initializing \_\_local memory
  - Kernel must initialize local memory

```
__kernel void kMat( const int n, __global float *A, __local float *tmp_arr )
  // Could also have some fixed size local array
  // local float tmp arr[64];
  int gbl id = get global id(0); // ID within entire index space
  int loc id = get local id(0);  // ID within this work-group
  int loc sz = get local size(0); // Size of this work-group
  // For some reason we want to fill up the first half of the local array
  if ( loc id < loc sz/2 )</pre>
    tmp arr[loc id] = A[qbl id];
  // All work-items must hit barrier. They'll all see a consistent tmp_arr[]
 barrier(CLK LOCAL MEM FENCE);
  // Each work-item can now use the elements from tmp_arr[] safely.
  // Often used if we'd be repeatedly accessing the same A[] elements.
  for ( j=0; j<loc sz; j++ )</pre>
   my_compute( gbl_id, tmp_arr[j], A );
```



# **Example: Parallel Reduction (CPU)**

- Reduction of an array of elements to a single value
  - e.g. sum:
  - OpenMP on host performs partial in each thread
    - Linear (serial) sum within thread

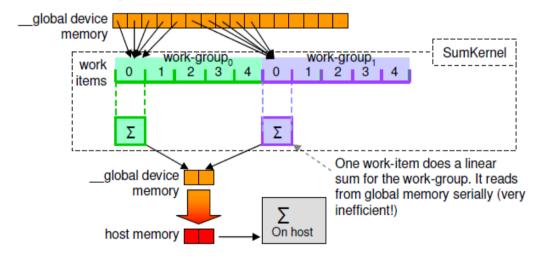


Can recreate in OpenCL using work-groups



# Parallel Sum on GPU (I)

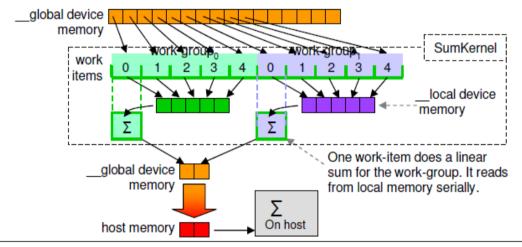
- Use one work-item to perform a sum within a work-group
  - Inefficient only one work-item forms the partial sum





## Parallel Sum on GPU (II)

- Memory optimization copy to \_\_local memory in parallel
  - Still inefficient only one work-item performs the partial sum

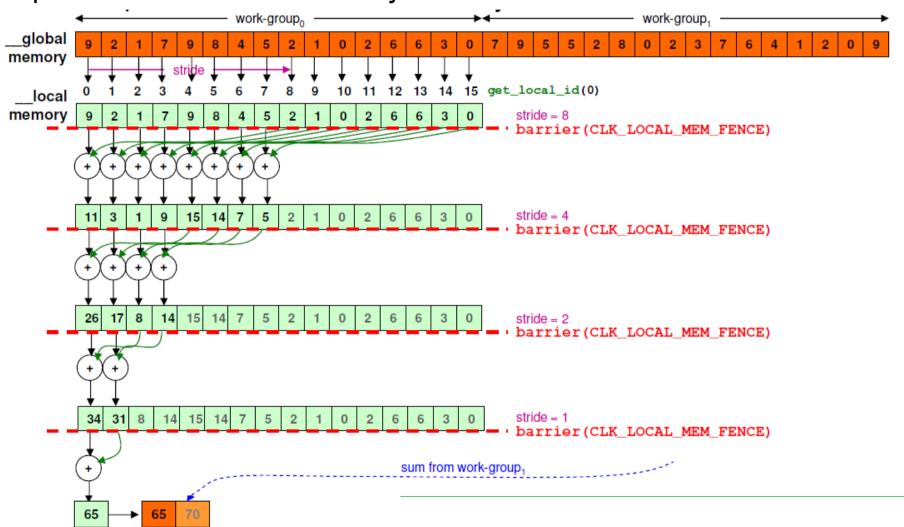


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## **Parallel Reduction (III)**

■ Improve on linear sum – binary sum





## Parallel Reduction (III) Kernel

- Copy all work-group's values into \_\_local memory
  - Repeatedly half the work-group, adding one half to the other

```
kernel void sumGPU3 (const uint n, global const float *x,
                      global float *partialSums, local float *localSums )
uint local id = get local id(0);
uint group size = get local size(0);
// Copy from global mem in to local memory (should check for out of bounds)
localSums[local id] = x[get global id(0)];
for (uint stride=group size/2; stride>0; stride /= 2) { // stride halved at loop
  // Synchronize all work-items so we know all writes to localSums have occurred
  barrier(CLK_LOCAL_MEM_FENCE);
  // First n work-items read from second n work-items (n=stride)
  if ( local id < stride )</pre>
    localSums[local id] += localSums[local id + stride]
// Write result to nth position in global output array (n=work-group-id)
if ( local id == 0 )
  partialSum[get_group_id(0)] = localSums[0];
```



## Improved Reduction (IV) Kernel

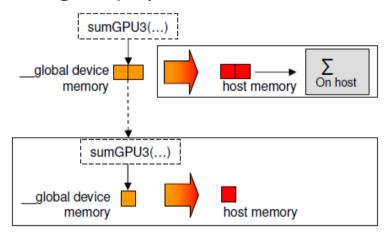
- Slight re-order to remove a couple of loop iterations
  - Set global\_work\_size to be half the input array length

```
kernel void sumGPU4( const uint n, global float *x,
                     global float *partialSums, local float *localSums ) {
uint global id = get global id(0);  // Gives where to read from
uint global_size = get_global_size(0); // Used to calc where to read from
uint local_id = get_local_id(0);  // Gives where to read/write local mem
uint group size = get local size(0);  // Used to calc initial stride
// Copy from global mem in to local memory (doing first iteration)
localSums[local id] = x[global id] + x[global id + global size];
barrier(CLK LOCAL MEM FENCE);
for (uint stride=group size/2; stride>1; stride>>=1 ) { // >>=1 does same as /=2
  // First n work-items read from second n work-items (n=stride)
  if ( local id < stride )</pre>
    localSums[local id] += localSums[local id + stride];
  // Synchronize so we know all work-items have written to localSums
  barrier(CLK LOCAL MEM FENCE);
// Last iter: write result to nth position in global x array (n=work-group id)
if ( local id == 0 )
  x[get group id(0)] = localSums[0]+localSums[1];
```



#### **Partial Sums**

- Still have n partial sums (n=number of work-groups)
  - Sum on host
  - Sum on GPU
    - Linear sum (use one work-item)
    - Parallel reduction (use one work-group)
       provided n is small enough; iterate if not



- Both GPU options can be done with another kernel call
  - Data is still on the GPU (in the partialSums array)
    - Avoid a device-to-host transfer
    - Simply make another kernel passing in the device memory object
    - DO NOT transfer back to host then pass back to device!



# **Exercise Parallel\_Reduction/parallel-reduction**

- inspect the parallel reduction program and kernels
  - par\_reduction.cc
  - SumGPU[1-4].cl
- and run it on different platforms:
  - make
  - ./par\_reduction cpu|gpu|acc 1|2|3|4
- complete the device-only program by filling the TODO gap in
  - par\_reduction\_device\_only.cc
- build and run it:
  - make
  - ./par\_reduction\_device\_only cpu|gpu|acc