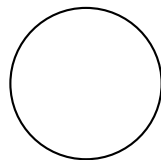
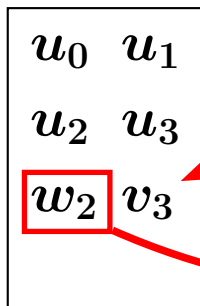


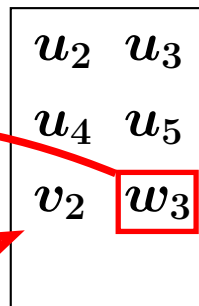
Cœur 1



Cœur 2



Mémoire  
cache 1



Mémoire  
cache 2



Mémoire

$u$	$u_0$	$u_1$	$u_2$	$u_3$	$u_4$	$u_5$	$\dots$
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$v$	$v_0$	$v_1$	$v_2$	$v_3$	$v_4$	$v_5$	$\dots$
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