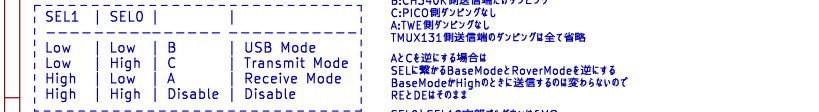


Interface

[illegible]

Low freq属性でないピンをsoftware ser

Debug

The diagram shows a microcontroller system with the following components and connections:

- Microcontroller (U5):** A central component with multiple pins connected to various peripherals.
- Power Supply:** A +3.3V supply connected to VCC and GND. A 1uF capacitor (C7) is connected between VCC and GND.
- IO Pins:**
 - IO 4mA max: Connected to TWE_CLR (pin 12) and TWE_RST (pin 21).
 - TWE_CLR (pin 27): Connected to SW4.
 - TWE_RST (pin 19): Connected to BPS.
 - RESET (pin 21): Connected to BPS.
 - DIO17/BPS (pin 19): Connected to BPS.
 - DIO12/DI1 (pin 13): Connected to JP2.
 - DIO13/DI2 (pin 15): Connected to JP3.
 - DIO11/DI3 (pin 17): Connected to EX1.
 - DIO16/DI4 (pin 23): Connected to EX1.
 - ADC1/AI1 (pin 24): Connected to EX1.
 - DIO0/AI2 (pin 22): Connected to EX1.
 - ADC2/AI3 (pin 25): Connected to EX1.
 - DIO1/AI4 (pin 26): Connected to EX1.
- Peripherals:**
 - SW4:** A switch connected to TWE_CLR and TWE_RST.
 - JP2, JP3, JP1:** Jumper pins connected to various microcontroller pins.
 - EX1:** A connector connected to multiple microcontroller pins.
 - SW5:** A switch connected to TWE_PRG (pin 10) and TWE_RX (pin 9).
 - SW3:** A switch connected to TWE_CLEAR (pin 3) and TWE_RX_SUB (pin 17).
 - 74AHC1G08 (U7):** An 8-input NAND gate connected to TWE_RX (pin 9), TWE_TX (pin 8), TWE_TX_SUB (pin 14), and TWE_RX_SUB (pin 17).
 - 74AHC1G08 (U7):** An 8-input NAND gate connected to TWE_RX (pin 9), TWE_TX (pin 8), TWE_TX_SUB (pin 14), and TWE_RX_SUB (pin 17).
 - 74AHC1G08 (U7):** An 8-input NAND gate connected to TWE_RX (pin 9), TWE_TX (pin 8), TWE_TX_SUB (pin 14), and TWE_RX_SUB (pin 17).
- Other Components:**
 - R14, R15:** Resistors connected to TWE_CLR and TWE_RST.
 - R16:** A resistor connected to TWE_RX.
 - R17:** A resistor connected to TWE_TX.
 - R18:** A resistor connected to TWE_TX_SUB.
 - R19:** A resistor connected to TWE_RX_SUB.
 - R20:** A resistor connected to TWE_CLEAR.
 - R21:** A resistor connected to TWE_RX.
 - R22:** A resistor connected to TWE_TX.
 - R23:** A resistor connected to TWE_TX_SUB.
 - R24:** A resistor connected to TWE_RX_SUB.
 - R25:** A resistor connected to TWE_CLEAR.
 - R26:** A resistor connected to TWE_RX.
 - R27:** A resistor connected to TWE_TX.
 - R28:** A resistor connected to TWE_TX_SUB.
 - R29:** A resistor connected to TWE_RX_SUB.
 - R30:** A resistor connected to TWE_CLEAR.
 - R31:** A resistor connected to TWE_RX.
 - R32:** A resistor connected to TWE_TX.
 - R33:** A resistor connected to TWE_TX_SUB.
 - R34:** A resistor connected to TWE_RX_SUB.
 - R35:** A resistor connected to TWE_CLEAR.
 - R36:** A resistor connected to TWE_RX.
 - R37:** A resistor connected to TWE_TX.
 - R38:** A resistor connected to TWE_TX_SUB.
 - R39:** A resistor connected to TWE_RX_SUB.
 - R40:** A resistor connected to TWE_CLEAR.
 - R41:** A resistor connected to TWE_RX.
 - R42:** A resistor connected to TWE_TX.
 - R43:** A resistor connected to TWE_TX_SUB.
 - R44:** A resistor connected to TWE_RX_SUB.
 - R45:** A resistor connected to TWE_CLEAR.
 - R46:** A resistor connected to TWE_RX.
 - R47:** A resistor connected to TWE_TX.
 - R48:** A resistor connected to TWE_TX_SUB.
 - R49:** A resistor connected to TWE_RX_SUB.
 - R50:** A resistor connected to TWE_CLEAR.

Notes:

- M1:** M1 is GNDへ接続すると規格、開放またはVCCへ接続すると子機フラッシュクリア時、M1はHiまたはオープン→インタラクティブモードで解決
- SPICLK:** 始動時に外部からGNDまたは中間的な電圧を印加するとプログラムモードに遷移しない可能性
- TWE_RXはMUX側でプルアップ済**

Rev: 1/1