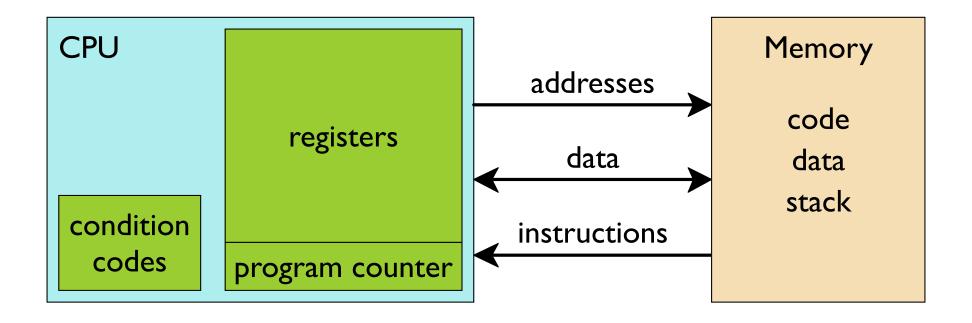
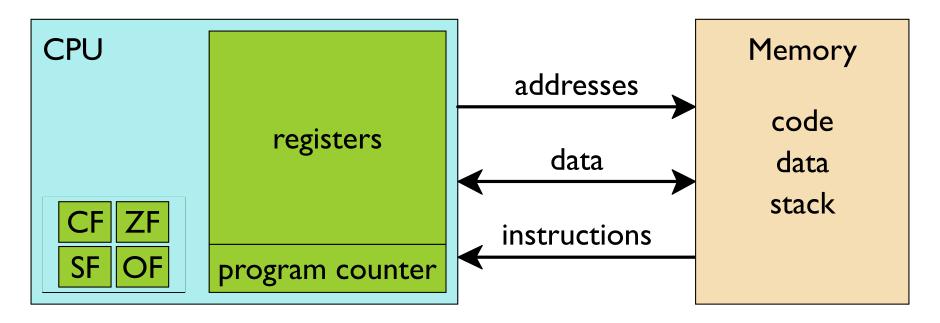
Condition Codes



Condition Codes



Set by most recent arithmetic (not counting leax):

- **CF**: carry carry out of most-significant bit
- **ZF**: zero produced zero
- **SF**: sign produced negative
- **OF**: overflow two's complement overflow

CPU

register	value	unsigned	signed
%eax	0x0000001	1	1
%ebx	0x00000002	2	2
%ecx	0x7FFFFFFF	2147483647	2147483647
%edx	0x80000001	2147483649	-2147483647

subl %eax, %eax

Sets **ZF** zero

CPU

register	value	unsigned	signed
%eax	0x0000001	1	1
%ebx	0x00000002	2	2
%ecx	0x7FFFFFFF	2147483647	2147483647
%edx	0x80000001	2147483649	-2147483647

addl %eax, %ebx

Sets no flags

CPU

register	value	unsigned	signed
%eax	0x0000001	1	1
%ebx	0x00000002	2	2
%ecx	0x7FFFFFFF	2147483647	2147483647
%edx	0x80000001	2147483649	-2147483647

subl %eax, %ebx %ebx = %ebx - %eax

Sets no flags

CPU

register	value	unsigned	signed
%eax	0x0000001	1	1
%ebx	0x00000002	2	2
%ecx	0x7FFFFFFF	2147483647	2147483647
%edx	0x80000001	2147483649	-2147483647

CPU

register	value	unsigned	signed
%eax	0x0000001	1	1
%ebx	0x00000002	2	2
%ecx	0x7FFFFFFF	2147483647	2147483647
%edx	0x80000001	2147483649	-2147483647

addl %ecx, %edx

Sets **ZF CF** zero carry

CPU

register	value	unsigned	signed
%eax	0x0000001	1	1
%ebx	0x00000002	2	2
%ecx	0×7FFFFFFF	2147483647	2147483647
%edx	0x80000001	2147483649	-2147483647

subl %ecx, %edx

Sets **OF** overflow

CPU

register	value	unsigned	signed
%eax	0x0000001	1	1
%ebx	0x00000002	2	2
%ecx	0×7FFFFFFF	2147483647	2147483647
%edx	0x80000001	2147483649	-2147483647

subl %edx, %ecx

Sets **CF SF OF** carry sign overflow

CPU

register	value	unsigned	signed
%eax	0x0000001	1	1
%ebx	0x00000002	2	2
%ecx	0×7FFFFFFF	2147483647	2147483647
%edx	0x80000001	2147483649	-2147483647

subl source, dest

dest == source **ZF**

dest ≠ source ~**ZF**

CPU

register	value	unsigned	signed
%eax	0x0000001	1	1
%ebx	0x00000002	2	2
%ecx	0×7FFFFFFF	2147483647	2147483647
%edx	0x80000001	2147483649	-2147483647
%eax	0X80000001	214/483649	-214/48364/

subl source, dest

unsigned: dest < source CF

dest ≤ source **CF** | **ZF**

dest ≤ source (SF^OF) | ZF

CPU

register	value	unsigned	signed
%eax	0x0000001	1	1
%ebx	0x00000002	2	2
%ecx	0x7FFFFFFF	2147483647	2147483647
%edx	0x80000001	2147483649	-2147483647

subl %eax, %ebx

subl source, dest

Sets no flags

dest ≤ source **CF** | **ZF**

dest ≤ source (SF^OF) | ZF

CPU

١				
	register	value	unsigned	signed
	%eax	0x0000001	1	1
	%ebx	0x00000002	2	2
	%ecx	0x7FFFFFFF	2147483647	2147483647
	%edx	0x80000001	2147483649	-2147483647
- 1				

subl source, dest

dest ≤ source **CF** | **ZF**

dest ≤ source (SF^OF) | ZF

subl %ebx, %eax

Sets CF SF

carry sign

CPU

١				
	register	value	unsigned	signed
	%eax	0x0000001	1	1
	%ebx	0x00000002	2	2
	%ecx	0x7FFFFFFF	2147483647	2147483647
	%edx	0x80000001	2147483649	-2147483647
- 1				

subl source, dest

dest ≤ source **CF** | **ZF**

dest ≤ source (SF^OF) | ZF

subl %ecx, %edx

OF

overflow

Sets

CPU

register	value	unsigned	signed
%eax	0x0000001	1	1
%ebx	0x00000002	2	2
%ecx	0x7FFFFFFF	2147483647	2147483647
%edx	0x80000001	2147483649	-2147483647

subl %edx, %ecx

subl source, dest

Sets CF SF OF

carry sign overflow

dest ≤ source **CF** | **ZF**

dest ≤ source (SF^OF) | ZF

Condition Codes and Comparisons

setcc dest

sets one byte in dest to 0 or 1

equal / zero	ZF a.k.a. setz
not equal / not zero	~ZF a.k.a. setnz
negative	SF
non-negative	~SF
greater signed	~ (SF^OF) &~ZF
greater or equal signed	~ (SF^OF)
less signed	(SF^OF)
less or equal signed	(SF^OF) ZF
above unsigned	~CF&~ZF
below unsigned	CF
	not equal / not zero negative non-negative greater signed greater or equal signed less signed less or equal signed above unsigned

Using setco

```
int gt (long x, long y) {
  return x > y;
}
```

```
.... # %rdi = x
.... # %rsi = y
subq %rsi, %rdi # Compare x to y
setg %al # Set when >
movzbl %al, %eax
ret # Result in %eax
```

movzxx source, dest

move with zero extension

Comparisons

```
cmpx source, dest
```

dest - source

cmpq source , dest

is the same as

subq source, dest

without writing to dest

```
.... # %rdi = x
.... # %rsi = y
cmpq %rsi, %rdi # Compare x to y
setg %al # Set when >
movzbl %al, %eax
ret # Result in %eax
```

Bitwise Comparisons

testx source, dest

dest & source

testq source, dest

is the same as

andq source, dest

without writing to dest

```
.... # %rdi = x
testq %rdi, %rdi  # Compare by x & x
setne %al  # Set when x != 0
movzbl %al, %eax
ret  # Result in %eax
```

```
char ctest(int a, int b, int c)
  char t1 = a __ b;
  char t2 = b __ (__)a;
  char t3 = (__)c __ (__)a;
  char t4 = (__)a __ (__)c;
  char t5 = c __ b;
  char t6 = a __ 0;
  return t1+t2+t3+t4+t5+t6;
}
```

```
# Get a
movl 8(%rbp),%ecx
movl 12(%rbp),%esi # Get b
cmpl %esi,%ecx # Compare a-b
                        # t1
setl %al
cmpl %ecx,%esi # Compare b-a
                        # t2
setb -1(%rbp)
cmpw %cx,16(%rbp) # Compare c-a
                        # t3
setge -2(%rbp)
movb %cl,%dl
cmpb 16(%rbp),%dl # Compare a-c
setne %bl
                        # t4
cmpl %esi,16(%rbp) # Compare c-b
setg -3(%rbp)
                        # t5
testl %ecx,%ecx # Test a&a
                        # t6
sete %dl
addb -1(%rbp),%al # t1+=t2
addb -2(%rbp),%al
                 # t1+=t3
                  # t1+=t4
addb %bl,%al
addb -3(%rbp),%al # t1+=t5
addb %d1,%al # t1+=t6
movsbl %al, %eax # Convert type 31
```

```
char ctest(int a, int b, int c)
  char t1 = a < b;
  char t2 = b __ (__)a;
  char t3 = (__)c __ (__)a;
  char t4 = (__)a __ (__)c;
  char t5 = c __ b;
  char t6 = a __ 0;
  return t1+t2+t3+t4+t5+t6;
}</pre>
```

```
# Get a
movl 8(%rbp),%ecx
movl 12(%rbp),%esi # Get b
cmpl %esi,%ecx # Compare a-b
                        # t1
setl %al
cmpl %ecx,%esi # Compare b-a
                        # t2
setb -1(%rbp)
cmpw %cx,16(%rbp) # Compare c-a
                        # t3
setge -2(%rbp)
movb %cl,%dl
cmpb 16(%rbp),%dl # Compare a-c
setne %bl
                        # t4
cmpl %esi,16(%rbp) # Compare c-b
setg -3(%rbp)
                        # t5
testl %ecx,%ecx # Test a&a
                        # t6
sete %dl
addb -1(%rbp),%al # t1+=t2
                  # t1+=t3
addb -2(%rbp),%al
                  # t1+=t4
addb %bl,%al
addb -3(%rbp),%al # t1+=t5
addb %d1,%al # t1+=t6
movsbl %al, %eax # Convert type 32
```

```
char ctest(int a, int b, int c)
  char t1 = a < b;
  char t2 = b < (unsigned)a;
  char t3 = (__)c __ (__)a;
  char t4 = (__)a __ (__)c;
  char t5 = c __ b;
  char t6 = a __ 0;
  return t1+t2+t3+t4+t5+t6;
}</pre>
```

```
# Get a
movl 8(%rbp),%ecx
movl 12(%rbp),%esi # Get b
cmpl %esi,%ecx # Compare a-b
                        # t1
setl %al
cmpl %ecx,%esi # Compare b-a
                        # t2
setb -1(%rbp)
cmpw %cx,16(%rbp) # Compare c-a
                        # t3
setge -2(%rbp)
movb %cl,%dl
cmpb 16(%rbp),%dl # Compare a-c
setne %bl
                        # t4
cmpl %esi,16(%rbp) # Compare c-b
setg -3(%rbp)
                        # t5
testl %ecx,%ecx # Test a&a
                        # t6
sete %dl
addb -1(%rbp),%al # t1+=t2
addb -2(%rbp),%al
                  # t1+=t3
                  # t1+=t4
addb %bl,%al
addb -3(%rbp),%al # t1+=t5
addb %d1,%al # t1+=t6
movsbl %al, %eax # Convert type 33
```

```
char ctest(int a, int b, int c)
  char t1 = a < b;
  char t2 = b < (unsigned)a;
  char t3 = (short)c >= (short)a
  char t4 = (__)a __ (__)c;
  char t5 = c __ b;
  char t6 = a __ 0;
  return t1+t2+t3+t4+t5+t6;
}
```

```
# Get a
movl 8(%rbp),%ecx
movl 12(%rbp),%esi # Get b
cmpl %esi,%ecx # Compare a-b
                        # t1
setl %al
cmpl %ecx,%esi # Compare b-a
                        # t2
setb -1(%rbp)
cmpw %cx,16(%rbp) # Compare c-a
                        # t3
setge -2(%rbp)
movb %cl,%dl
cmpb 16(%rbp),%dl # Compare a-c
setne %bl
                        # t4
cmpl %esi,16(%rbp) # Compare c-b
setg -3(%rbp)
                        # t5
testl %ecx,%ecx # Test a&a
                        # t6
sete %dl
addb -1(%rbp),%al # t1+=t2
                  # t1+=t3
addb -2(%rbp),%al
                  # t1+=t4
addb %bl,%al
addb -3(%rbp),%al # t1+=t5
addb %d1,%al # t1+=t6
movsbl %al, %eax # Convert type 34
```

```
char ctest(int a, int b, int c)
  char t1 = a < b;
  char t2 = b < (unsigned)a;
  char t3 = (short)c >= (short)a
  char t4 = (char)a != (char)c;
  char t5 = c __ b;
  char t6 = a __ 0;
  return t1+t2+t3+t4+t5+t6;
}
```

```
# Get a
movl 8(%rbp),%ecx
movl 12(%rbp),%esi # Get b
cmpl %esi,%ecx # Compare a-b
                         # t1
setl %al
cmpl %ecx,%esi # Compare b-a
                        # t2
setb -1(%rbp)
cmpw %cx,16(%rbp) # Compare c-a
                         # t3
setge -2(%rbp)
movb %cl,%dl
cmpb 16(%rbp),%dl # Compare a-c
setne %bl
                         # t4
cmpl %esi,16(%rbp) # Compare c-b
setg -3(%rbp)
                        # t.5
testl %ecx,%ecx # Test a&a
                        # t6
sete %dl
addb -1(%rbp),%al # t1+=t2
addb -2(%rbp),%al
                  # t1+=t3
                  # t1+=t4
addb %bl,%al
addb -3(%rbp),%al # t1+=t5
addb %d1,%al # t1+=t6
movsbl %al, %eax # Convert type 35
```

```
char ctest(int a, int b, int c)
  char t1 = a < b;
  char t2 = b < (unsigned)a;
  char t3 = (short)c >= (short)a
  char t4 = (char)a != (char)c;
  char t5 = c > b;
  char t6 = a ___ 0;
  return t1+t2+t3+t4+t5+t6;
}
```

```
# Get a
movl 8(%rbp), %ecx
movl 12(%rbp),%esi # Get b
cmpl %esi,%ecx # Compare a-b
                         # t1
setl %al
cmpl %ecx,%esi # Compare b-a
                         # t2
setb -1(%rbp)
cmpw %cx,16(%rbp) # Compare c-a
                         # t3
setge -2(%rbp)
movb %cl,%dl
cmpb 16(%rbp),%dl # Compare a-c
setne %bl
                         # t4
cmpl %esi,16(%rbp) # Compare c-b
setg -3(%rbp)
                         # t.5
testl %ecx,%ecx # Test a&a
                         # t6
sete %dl
addb -1(%rbp),%al
                  # t1+=t2
addb -2(%rbp),%al
                     # t1+=t3
                   # t1+=t4
addb %bl,%al
addb -3(%rbp),%al # t1+=t5
addb %dl,%al
                  # t1+=t6
movsbl %al, %eax # Convert type
```

```
char ctest(int a, int b, int c)
  char t1 = a < b;
  char t2 = b < (unsigned)a;
  char t3 = (short)c >= (short)a
  char t4 = (char)a != (char)c;
  char t5 = c > b;
  char t6 = a == 0;
  return t1+t2+t3+t4+t5+t6;
}
```

```
# Get a
movl 8(%rbp),%ecx
movl 12(%rbp),%esi
                      # Get b
cmpl %esi,%ecx # Compare a-b
                          # t1
setl %al
cmpl %ecx, %esi # Compare b-a
                          # t2
setb -1(%rbp)
cmpw %cx,16(%rbp) # Compare c-a
setge -2(%rbp)
                          # t3
movb %cl,%dl
cmpb 16(%rbp),%dl # Compare a-c
setne %bl
                          # t4
cmpl %esi,16(%rbp) # Compare c-b
setg -3(%rbp)
                          # t5
testl %ecx,%ecx # Test a&a
                         # t6
sete %dl
                   # t1+=t2
addb -1(%rbp),%al
addb -2(%rbp),%al
                     # t1+=t3
                     # t1+=t4
addb %bl,%al
addb -3(%rbp),%al # t1+=t5
addb %dl,%al
                     # t1+=t6
                # Convert type 37
movsbl %al, %eax
```

Conditional Assignment

```
char x = (a < b);
```

use set1

```
long x = ((a < b) ? 17 : 42);
```

Use **cmovlq**, which is like **movq**, but only if the condition codes imply "less than"

```
.... # %rdi = a
.... # %rsi = b
.... # %rax as x
movql $42, %rax # Guess 42
cmpq %rsi, %rdi # Compare a to b
cmovlq $17, %rax # Maybe correct guess
```

Conditional Move

cmovccx source, dest

conditionally copies source to dest

cmovex	equal / zero	ZF a.k.a. cmovz x
cmovnex	not equal / not zero	~ ZF a.k.a. cmovnz x
cmovsx	negative	SF
cmovnsx	non-negative	~SF
cmovgx	greater signed	~(SF^OF) &~ZF
cmovgex	greater or equal signed	~ (SF^OF)
$cmovl_X$	less signed	(SF^OF)
cmovlex	less or equal signed	(SF^OF) ZF
cmovax	above unsigned	~CF&~ZF
cmovbx	below unsigned	CF

Comparisions and Conditionals

```
if (a < b) {
    ....
}
....</pre>
```

```
0x200: cmovgeq $0x203, %rip
0x201: ....
0x202: ....
0x203: ....
```

```
if (a < b) {
    ....
} else {
    ....
}</pre>
```

```
0x300: cmovgeq $0x303, %rip
0x301: ....
0x302: movq $0x305, %rip
0x303: ....
0x304: ....
```

but we can't use %rip as a destination

Conditional Jumps

jcc source

conditionally sets the program counter to the value of source

je	equal / zero	ZF a.k.a. jz
jne	not equal / not zero	~ZF a.k.a. jnz
js	negative	SF
jns	non-negative	~SF
jg	greater signed	~ (SF^OF) &~ZF
jge	greater or equal signed	~ (SF^OF)
jl	less signed	(SF ^O F)
jle	less or equal signed	(SF^OF) ZF
ja	above unsigned	~CF&~ZF
jb	below unsigned	CF

Unconditional Jump

jmp source

always sets the program counter to the value of source

Comparisions and Conditionals

```
if (a < b) {
    ....
}
....</pre>
```

```
0x200: jge $0x203
0x201: ....
0x202: ....
0x203: ....
```

```
if (a >= b) goto skip;
....
skip:
....
```

```
if (a < b) {
    ....
} else {
    ....
}</pre>
```

```
0x300: jge $0x303
0x301: ....
0x302: jmp $0x305
0x303: ....
0x304: ....
```

```
if (a >= b) goto else;
.... goto skip;
else:
....
skip:
....
```

Test and Jump Alternatives

```
if (a < b) {
   then
} else {
   else
}
....</pre>
```

```
if (a >= b) goto else;
then goto skip;
else:
    else
skip:
....
```

```
if (a < b) goto then;
else goto skip;
then:
  then
skip:
  ....</pre>
```

Conditional Example

```
long absdiff(long x, long y) {
  long result;
  if (x > y)
    result = x-y;
  else
    result = y-x;
  return result;
}
```

```
0: cmpq %rsi, %rdi
3: jle 0xc
5: movq %rdi, %rax
8: subq %rsi, %rax
b: retq
c: movq %rsi, %rax
f: subq %rdi, %rax
12: retq
```

Conditional Example

```
long absdiff(long x, long y) {
  long result;
  if (x > y)
    result = x-y;
  else
    result = y-x;
  return result;
}
```

```
cmpq %rsi, %rdi
 3: jle 0xc
 5: movq %rdi, %rax
    subq %rsi, %rax
 8:
    retq
b:
    movq %rsi, %rax
 f: subq %rdi, %rax
12:
     retq
    absdiff:
      cmpq %rsi, %rdi
      jle .L4
      movq %rdi, %rax
      subq
            %rsi, %rax
      ret
    . L4:
      movq %rsi, %rax
      subq
            %rdi, %rax
      ret
```

More C Control Forms

- do while loops
- while loops
- for loops

do-while Loops

```
do {
   body
} while (test);
```

```
loop:
  body
  if (test)
    goto loop;
```

```
int fib_dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
 do {
  int t = val + nval;
  val = nval;
  nval = t;
   i++;
 } while (i < n);
 return val;
```

```
int fib_dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
 do {
   int t = val + nval;
  val = nval;
  nval = t;
   i++;
  } while (i < n);</pre>
 return val;
```

```
int fib_dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
loop:
 int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n);
  goto loop;
 return val;
```

```
int fib dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
 do {
   int t = val + nval;
  val = nval;
   nval = t;
   i++;
  } while (i < n);
 return val;
```

```
register variable initial value %ecx i 0 %esi n n %ebx val 0 %edx nval 1 %eax t
```

```
int fib dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n);
  goto loop;
  return val;
.L6:
```

```
.L6:
    leal (%edx,%ebx),%eax
    movl %edx,%ebx
    movl %eax,%edx
    incl %ecx
    cmpl %esi,%ecx
    jl .L6
    movl %ebx,%eax
```

```
int fib dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
 do {
   int t = val + nval;
  val = nval;
   nval = t;
   i++;
  } while (i < n);
 return val;
```

```
register variable initial value %ecx i 0  
%esi n n  
%ebx val 0  
%edx nval 1  
%eax t
```

```
int fib dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n);
  goto loop;
  return val;
.L6:
```

```
.L6:
    leal (%edx,%ebx),%eax
    movl %edx,%ebx
    movl %eax,%edx
    incl %ecx
    cmpl %esi,%ecx
    jl .L6
    movl %ebx,%eax
```

t = ...

```
int fib dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
 do {
   int t = val + nval;
  val = nval;
   nval = t;
   i++;
  } while (i < n);
 return val;
```

```
register variable initial value %ecx i 0  
%esi n n  
%ebx val 0  
%edx nval 1  
%eax t
```

```
int fib dw(int n) {
  int i = 0;
 int val = 0;
 int nval = 1;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n);
  goto loop;
  return val;
```

```
.L6:
    leal (%edx,%ebx),%eax
    movl %edx,%ebx
    movl %eax,%edx
    incl %ecx
    cmpl %esi,%ecx
    jl .L6
    movl %ebx,%eax
```

```
t = ...
val = nval
```

```
int fib dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
 do {
   int t = val + nval;
  val = nval;
   nval = t;
   i++;
  } while (i < n);
 return val;
```

```
register variable initial value %ecx i 0  
%esi n n  
%ebx val 0  
%edx nval 1  
%eax t
```

```
int fib dw(int n) {
  int i = 0;
 int val = 0;
 int nval = 1;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n);
  goto loop;
  return val;
```

```
.L6:
    leal (%edx,%ebx),%eax
    movl %edx,%ebx
    movl %eax,%edx
    incl %ecx
    cmpl %esi,%ecx
    jl .L6
    movl %ebx,%eax
```

```
t = ...
val = nval
nval = t
```

```
int fib dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
 do {
   int t = val + nval;
  val = nval;
   nval = t;
   i++;
  } while (i < n);
 return val;
```

```
register variable initial value %ecx i 0 %esi n n %ebx val 0 %edx nval 1 %eax t
```

```
int fib dw(int n) {
  int i = 0;
 int val = 0;
 int nval = 1;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n);
  goto loop;
  return val;
```

```
.L6:
    leal (%edx,%ebx),%eax
    movl %edx,%ebx
    movl %eax,%edx
    incl %ecx
    cmpl %esi,%ecx
    jl .L6
    movl %ebx,%eax
```

```
t = ...
val = nval
nval = t
i++
```

```
int fib dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
 do {
   int t = val + nval;
  val = nval;
   nval = t;
   i++;
  } while (i < n);
 return val;
```

```
register variable initial value %ecx i 0 %esi n n %ebx val 0 %edx nval 1 %eax t
```

```
int fib dw(int n) {
  int i = 0;
 int val = 0;
 int nval = 1;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n);
  goto loop;
  return val;
```

```
.L6:
    leal (%edx,%ebx),%eax
    movl %edx,%ebx
    movl %eax,%edx
    incl %ecx
    cmpl %esi,%ecx
    jl .L6
    movl %ebx,%eax
```

```
t = ...
val = nval
nval = t
i++
i - n
```

```
int fib dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
 do {
   int t = val + nval;
  val = nval;
   nval = t;
   i++;
  } while (i < n);
 return val;
```

```
register variable initial value %ecx i 0  
%esi n n  
%ebx val 0  
%edx nval 1  
%eax t
```

```
int fib dw(int n) {
  int i = 0;
 int val = 0;
 int nval = 1;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n);
  goto loop;
  return val;
```

```
.L6:
    leal (%edx,%ebx),%eax
    movl %edx,%ebx
    movl %eax,%edx
    incl %ecx
    cmpl %esi,%ecx
    jl .L6
    movl %ebx,%eax
```

```
t = ...
val = nval
nval = t
i++
i - n
if (i < n)</pre>
```

```
int fib dw(int n) {
 int i = 0;
 int val = 0;
 int nval = 1;
 do {
   int t = val + nval;
  val = nval;
   nval = t;
   i++;
  } while (i < n);
 return val;
```

```
register variable initial value %ecx i 0  
%esi n n  
%ebx val 0  
%edx nval 1  
%eax t
```

```
int fib dw(int n) {
  int i = 0;
 int val = 0;
 int nval = 1;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n);
  goto loop;
  return val;
```

```
.L6:
    leal (%edx,%ebx),%eax
    movl %edx,%ebx
    movl %eax,%edx
    incl %ecx
    cmpl %esi,%ecx
    jl .L6
    movl %ebx,%eax
```

```
t = ...
val = nval
nval = t
i++
i - n
if (i < n)
return val<sub>67</sub>
```

while Loops

```
while (test) {
   body
}
```

```
loop:
   if (!test)
      goto done;
   body
   goto loop;
done:
```

```
if (!test)
    goto done;
loop:
    body
    if (test)
       goto loop;
done:
```

```
int fib_w(int n) {
 int i = 1;
 int val = 1;
 int nval = 1;
 while (i < n) {
   int t = val + nval;
  val = nval;
  nval = t;
   i++;
 return val;
```

```
int fib_w(int n) {
 int i = 1;
 int val = 1;
 int nval = 1;
 while (i < n) {
   int t = val + nval;
  val = nval;
  nval = t;
   i++;
 return val;
```

```
int fib_dw(int n) {
 int i = 1;
 int val = 1;
 int nval = 1;
 if (i >= n) goto done;
loop:
 int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n) goto loop;</pre>
done:
 return val;
```

```
int fib w(int n) {
 int i = 1;
 int val = 1;
 int nval = 1;
 while (i < n) {
    int t = val + nval;
  val = nval;
  nval = t;
   i++;
 return val;
```

```
register variable initial value %edx nmi n-i %ebx val 1 %ecx nval 1 %eax n / t
```

```
int fib dw(int n) {
  int i = 1;
 int val = 1;
 int nval = 1;
  if (i \ge n) goto done;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
  if (i < n) goto loop;</pre>
done:
  cmpl %eax,%ebx
  jge .L9
  leal -1(%eax), %edx
.L10:
  leal (%ecx,%ebx),%eax
 movl %ecx, %ebx
 movl %eax, %ecx
 decl %edx
  jnz .L10
. L9:
```

```
int fib w(int n) {
 int i = 1;
 int val = 1;
 int nval = 1;
 while (i < n) {
    int t = val + nval;
  val = nval;
  nval = t;
   i++;
 return val;
```

```
register variable initial value %edx nmi n-i %ebx val 1 %ecx nval 1 %eax n / t
```

```
int fib dw(int n) {
  int i = 1;
 int val = 1;
 int nval = 1;
  if (i \ge n) goto done;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
  if (i < n) goto loop;</pre>
done:
  cmpl %eax,%ebx
  jge .L9
  leal -1(%eax),%edx
.L10:
  leal (%ecx,%ebx),%eax
 movl %ecx, %ebx
 movl %eax, %ecx
 decl %edx
  jnz .L10
. L9:
```

val - n

```
int fib w(int n) {
 int i = 1;
 int val = 1;
 int nval = 1;
 while (i < n) {
    int t = val + nval;
  val = nval;
  nval = t;
   i++;
 return val;
```

```
register variable initial value %edx nmi n-i %ebx val 1 %ecx nval 1 %eax n / t
```

```
int fib dw(int n) {
  int i = 1;
 int val = 1;
 int nval = 1;
  if (i \ge n) goto done;
loop:
 int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n) goto loop;</pre>
done:
 cmpl %eax,%ebx
  jge .L9
 leal -1(%eax),%edx
.L10:
  leal (%ecx,%ebx),%eax
 movl %ecx, %ebx
 movl %eax,%ecx
 decl %edx
  jnz .L10
. L9:
```

```
val - n
if (val >= n)
```

```
int fib w(int n) {
 int i = 1;
 int val = 1;
 int nval = 1;
 while (i < n) {
    int t = val + nval;
  val = nval;
  nval = t;
   i++;
 return val;
```

```
register variable initial value %edx nmi n-i %ebx val 1 %ecx nval 1 %eax n / t
```

```
int fib dw(int n) {
  int i = 1;
 int val = 1;
 int nval = 1;
  if (i \ge n) goto done;
loop:
 int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n) goto loop;
done:
 cmpl %eax,%ebx
  jge .L9
 leal -1(%eax), %edx
.L10:
  leal (%ecx,%ebx),%eax
 movl %ecx, %ebx
 movl %eax,%ecx
 decl %edx
  jnz .L10
. L9:
```

```
val - n
if (val >= n)
nmi = n-1
```

```
int fib w(int n) {
 int i = 1;
 int val = 1;
 int nval = 1;
 while (i < n) {
    int t = val + nval;
  val = nval;
  nval = t;
   i++;
 return val;
```

```
register variable initial value %edx nmi n-i %ebx val 1 %ecx nval 1 %eax n / t
```

```
int fib dw(int n) {
  int i = 1;
 int val = 1;
 int nval = 1;
  if (i \ge n) goto done;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
  if (i < n) goto loop;</pre>
done:
  cmpl %eax,%ebx
  ige .L9
  leal -1(%eax), %edx
.L10:
  leal (%ecx, %ebx), %eax
 movl %ecx, %ebx
 movl %eax, %ecx
 decl %edx
  jnz .L10
. L9:
```

```
val - n
if (val >= n)
nmi = n-1
t = ...
```

```
int fib w(int n) {
 int i = 1;
 int val = 1;
 int nval = 1;
 while (i < n) {
    int t = val + nval;
  val = nval;
  nval = t;
   i++;
 return val;
```

```
register variable initial value %edx nmi n-i %ebx val 1 %ecx nval 1 %eax n / t
```

```
int fib dw(int n) {
  int i = 1;
 int val = 1;
 int nval = 1;
  if (i \ge n) goto done;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
  if (i < n) goto loop;</pre>
done:
  cmpl %eax,%ebx
  ige .L9
  leal -1(%eax),%edx
.L10:
  leal (%ecx, %ebx), %eax
 movl %ecx, %ebx
 movl %eax, %ecx
 decl %edx
  jnz .L10
. L9:
```

```
val - n
if (val >= n)
nmi = n-1

t = ...
val = nval
```

```
int fib w(int n) {
 int i = 1;
 int val = 1;
 int nval = 1;
 while (i < n) {
    int t = val + nval;
  val = nval;
   nval = t;
   i++;
 return val;
```

```
register variable initial value %edx nmi n-i %ebx val 1 %ecx nval 1 %eax n / t
```

```
int fib dw(int n) {
  int i = 1;
 int val = 1;
 int nval = 1;
  if (i \ge n) goto done;
loop:
  int t = val + nval;
 val = nval;
 nval = t;
 i++;
  if (i < n) goto loop;</pre>
done:
  cmpl %eax,%ebx
  ige .L9
  leal -1(%eax),%edx
.L10:
  leal (%ecx, %ebx), %eax
 movl %ecx, %ebx
 movl %eax, %ecx
 decl %edx
  jnz .L10
. L9:
```

```
val - n
if (val >= n)
nmi = n-1

t = ...
val = nval
nval = t
```

```
int fib w(int n) {
 int i = 1;
 int val = 1;
 int nval = 1;
 while (i < n) {
    int t = val + nval;
  val = nval;
   nval = t;
   i++;
 return val;
```

```
register variable initial value %edx nmi n-i %ebx val 1 %ecx nval 1 %eax n / t
```

```
int fib dw(int n) {
  int i = 1;
 int val = 1;
 int nval = 1;
  if (i \ge n) goto done;
loop:
 int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n) goto loop;
done:
 cmpl %eax,%ebx
  ige .L9
 leal -1(%eax),%edx
.L10:
  leal (%ecx, %ebx), %eax
 movl %ecx, %ebx
 movl %eax, %ecx
 decl %edx
  jnz .L10
. L9:
```

```
val - n
if (val >= n)
nmi = n-1

t = ...
val = nval
nval = t
nmi--
```

```
int fib w(int n) {
 int i = 1;
 int val = 1;
 int nval = 1;
 while (i < n) {
    int t = val + nval;
  val = nval;
   nval = t;
   i++;
 return val;
```

```
register variable initial value %edx nmi n-i %ebx val 1 %ecx nval 1 %eax n / t
```

```
int fib dw(int n) {
  int i = 1;
 int val = 1;
 int nval = 1;
  if (i \ge n) goto done;
loop:
 int t = val + nval;
 val = nval;
 nval = t;
 i++;
 if (i < n) goto loop;
done:
 cmpl %eax,%ebx
  ige .L9
  leal -1(%eax),%edx
.L10:
  leal (%ecx, %ebx), %eax
 movl %ecx, %ebx
 movl %eax, %ecx
 decl %edx
  jnz .L10
. L9:
```

```
val - n
if (val >= n)
nmi = n-1
t = \dots
val = nval
nval = t
nmi--
if (nmi != 0)
           79
```

for Loops

```
for (init; test; update) {
   body
}
```

```
init
loop:
   if (!test)
      goto done;
   body
   update
   goto loop;
done:
```

for Example

```
int fib_f(int n) {
  int i;
  int val = 1;
  int nval = 1;

for (i = 1; i < n; i++) {
    int t = val + nval;
    val = nval;
    nval = t;
  }

return val;
}</pre>
```

for Example

```
int fib_f(int n) {
  int i;
  int val = 1;
  int nval = 1;

for (i = 1; i < n; i++) {
    int t = val + nval;
    val = nval;
    nval = t;
  }

return val;
}</pre>
```

```
int fib_w(int n) {
 int i = 1;
 int val = 1;
  int nval = 1;
 while (i < n) {
   int t = val + nval;
   val = nval;
   nval = t;
   i++;
 return val;
```

Same machine code

```
int loop while(int a, int b) {
  int i = 0;
  int result = a;
 while (i < 256) {
    result += a;
   a -= b;
    i += b;
  return result;
```

```
movl 8(%rbp),%eax # Get a
movl 12(%rbp),%ebx # Get b
xorl %ecx,%ecx
movl %eax,%edx
.L5:
  addl %eax,%edx
  subl %ebx,%eax
  addl %ebx,%ecx
  cmpl $255,%ecx
  jle .L5
  movl %edx,%eax
```

```
register variable initial value %eax %ebx %ecx %edx
```

```
int loop while(int a, int b) {
  int i = 0;
  int result = a;
 while (i < 256) {
    result += a;
   a -= b;
    i += b;
  return result;
```

```
movl 8(%rbp),%eax # Get a
movl 12(%rbp),%ebx # Get b
xorl %ecx,%ecx
movl %eax,%edx
.L5:
  addl %eax,%edx
  subl %ebx,%eax
  addl %ebx,%ecx
  cmpl $255,%ecx
  jle .L5
  movl %edx,%eax
```

```
register variable initial value %eax a a %ebx %ecx %edx
```

```
int loop while(int a, int b) {
  int i = 0;
  int result = a;
 while (i < 256) {
    result += a;
   a -= b;
    i += b;
  return result;
```

```
movl 8(%rbp),%eax # Get a
movl 12(%rbp),%ebx # Get b
xorl %ecx,%ecx
movl %eax,%edx
.L5:
  addl %eax,%edx
  subl %ebx,%eax
  addl %ebx,%ecx
  cmpl $255,%ecx
  jle .L5
  movl %edx,%eax
```

```
register variable initial value %eax a a %ebx b b %ecx %edx
```

```
int loop while(int a, int b) {
  int i = 0;
  int result = a;
 while (i < 256) {
    result += a;
   a -= b;
    i += b;
  return result;
```

```
movl 8(%rbp),%eax # Get a
movl 12(%rbp),%ebx # Get b
xorl %ecx,%ecx
movl %eax,%edx
.L5:
  addl %eax,%edx
  subl %ebx,%eax
  addl %ebx,%ecx
  cmpl $255,%ecx
  jle .L5
  movl %edx,%eax
```

```
register variable initial value %eax a a %ebx b b %ecx i 0 %edx
```

```
int loop while(int a, int b) {
  int i = 0;
  int result = a;
 while (i < 256) {
    result += a;
   a -= b;
    i += b;
  return result;
```

```
movl 8(%rbp),%eax # Get a
movl 12(%rbp),%ebx # Get b
xorl %ecx,%ecx
movl %eax,%edx
.L5:
  addl %eax,%edx
  subl %ebx,%eax
  addl %ebx,%ecx
  cmpl $255,%ecx
  jle .L5
  movl %edx,%eax
```

```
register variable initial value %eax a a %ebx b b %ecx i 0 %edx result a
```

```
int loop while(int a, int b) {
  int i = 0;
  int result = a;
 while (i < 256) {
    result += a;
   a -= b;
    i += b;
  return result;
```

```
movl 8(%rbp),%eax # Get a
movl 12(%rbp),%ebx # Get b
xorl %ecx,%ecx
movl %eax,%edx
.L5:
  addl %eax,%edx
  subl %ebx,%eax
  addl %ebx,%ecx
  cmpl $255,%ecx
  jle .L5
  movl %edx,%eax
```

Compiler optimizes away initial i < 255, since i clearly starts as 0

```
register variable initial value %eax a a %ebx b b %ecx i 0 %edx result a
```

```
long switch_eg(long x, long y, long z) {
 long w = 1;
 switch (x) {
 case 1:
   w = y*z;
   break;
 case 2:
   w = y/z;
 case 3:
   w += z;
   break;
 case 5:
 case 6:
   w -= z;
   break;
 default:
   w = 2;
 return w;
```

```
long switch_eg(long x, long y, long z) {
 long w = 1;
 switch (x) {
 case 1:
              Jump here if x == 1
   w = y*z;
   break;
 case 2:
   w = y/z;
 case 3:
   w += z;
   break;
 case 5:
 case 6:
   w = z;
   break;
 default:
   w = 2;
 return w;
```

```
long switch_eg(long x, long y, long z) {
 long w = 1;
 switch (x) {
 case 1:
   w = y*z;
              Jump out of switch
   break;
 case 2:
   w = y/z;
 case 3:
   w += z;
   break;
 case 5:
 case 6:
   w = z;
   break;
 default:
   w = 2;
 return w;
```

```
long switch_eg(long x, long y, long z) {
 long w = 1;
 switch (x) {
 case 1:
   w = y*z;
   break;
 case 2:
             Jump here if x == 2
   w = y/z;
 case 3:
   w += z;
   break;
 case 5:
 case 6:
   w = z;
   break;
 default:
   w = 2;
 return w;
```

switch Statements

```
Multi-way branching on
long switch_eg(long x, long y, long z) {
 long w = 1;
                                           an integer value
 switch (x) {
 case 1:
   w = y*z;
   break;
 case 2:
              No break, so fall through for
   w = y/z;
 case 3:
   w += z;
   break;
 case 5:
 case 6:
   w = z;
   break;
 default:
   w = 2;
 return w;
```

switch Statements

```
long switch_eg(long x, long y, long z) {
 long w = 1;
 switch (x) {
 case 1:
   w = y*z;
   break;
 case 2:
   w = y/z;
 case 3:
   w += z;
   break;
 case 5:
 case 6:
              Jump here if x == 5 or x == 6
   w -= z;
   break;
 default:
   w = 2;
 return w;
```

Multi-way branching on an integer value

switch Statements

```
Multi-way branching on
long switch_eg(long x, long y, long z) {
 long w = 1;
                                          an integer value
 switch (x) {
 case 1:
   w = y*z;
   break;
 case 2:
   w = y/z;
 case 3:
   w += z;
   break;
 case 5:
 case 6:
   w = z;
   break;
              Jump here if x is some value not
 default:
              covered above:
   w = 2;
              x < 1 or x == 4 or x > 6
 return w;
```

Implementing switch

Small number of cases: if plus goto

```
switch (x) {
case 1:
  one
  break;
case 2:
  two
case 3:
  two-and-three
}
```

```
if (x == 1)
   goto one;
  else if (x == 2)
    goto two;
  else if (x == 3)
   goto three;
 else
    goto done;
one:
  one
 goto done;
two:
 two
three:
 two-and-three
done:
```

Implementing switch

Many consecutive cases: **jump table**

```
switch (x) {
case 1:
  one
  break;
case 2:
  ....
case 99:
  ninety-nine
}
```

```
void *jump_table[] = { &&one, &&two, ...
                           &&ninety_nine };
  if ((x >= 1) && (x <= 99))
    goto *jump table[x-1];
 else
   goto done;
one:
 one
 goto done;
two:
ninety_nine:
 ninety-nine
done:
```

Jump Table Machine Code

```
int jump(int v, int a, int b, int c) {
  void *jump table[] = { &&one, &&two, &&three };
  goto *jump_table[v-1];
one:
 v = a;
 goto done;
two:
 v = b;
 goto done;
three:
 v = c;
done:
  return v;
                                                 Сору
```

Jump Table Machine Code

```
int jump(int v, int a, int b, int c) {
 void *jump table[] = { &&one, &&two, &&three };
 goto *jump table[v-1];
                 400530: movq $0x400552,-0x20(%rsp)
one:
                 400539: movq $0x400555, -0x18(%rsp)
 v = a;
                 400542: movq $0x400558,-0x10(%rsp)
 goto done;
                 40054b: movslq %edi,%rdi
two:
                 40054e: jmpg
                               *-0x20(%rsp,%rdi,8)
 v = b;
                 400552: mov
                               %esi,%eax
 goto done;
                 400554: retq
three:
                 400555: mov
                                %edx,%eax
 v = c;
                 400557: retq
done:
                 400558: mov %ecx, %eax
  return v;
                 40055a: nopw 0x0(%rax, %rax, 1)
                 400560: retq
```

```
long switch_eg(long x, long y, long z) {
  long w = 1;

  switch(x) {
    ...
  }

  return w;
}
```

```
long switch_eg(long x, long y, long z) {
  long w = 1;

  switch(x) {
    ...
  }

  return w;
}
```

```
.section .rodata
  .align 8
.L4:
  .quad   .L8 # x == 0
  .quad   .L3 # x == 1
  .quad   .L5 # x == 2
  .quad   .L9 # x == 3
  .quad   .L8 # x == 4
  .quad   .L7 # x == 5
  .quad   .L7 # x == 6
```

```
long switch_eg(long x, long y, long z) {
  long w = 1;

  switch(x) {
    ...
  }

  return w;
}
```

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.section .rodata
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  .quad   .L8 # x == 4
  .quad   .L7 # x == 5
  .quad   .L7 # x == 6
```

```
switch_eg:
  movq %rdx, %rcx
  cmpq $6, %rdi  # Compare x to 6
  ja    .L8  # > 6 as unsigned ⇒ use default
  jmp *.L4(,%rdi,8) # Goto *jump_table[x]
```

```
.section .rodata
  .align 8
.L4:
  .quad   .L8 # x == 0
  .quad   .L3 # x == 1
  .quad   .L5 # x == 2
  .quad   .L9 # x == 3
  .quad   .L8 # x == 4
  .quad   .L7 # x == 5
  .quad   .L7 # x == 6
```

```
long switch eg(long x, long y, long z) {
  long w = 1;
  switch (x) {
 case 1:
   w = y*z;
   break;
 case 2:
   w = y/z;
 case 3:
   w += z;
   break;
  case 5:
  case 6:
   w -= z;
   break;
  default:
   w = 2;
  return w;
```

```
.L3:
.section .rodata
 .align 8
.L4:
         .L8 \# x == 0
 . quad
        .L3 \# x == 1
 . quad
 . quad
        .L5 # x == 2
 . quad
        .L9 \# x == 3
        .L8 \# x == 4
 .quad
        .L7 \# x == 5
 .quad
 .quad
         .L7 \# x == 6
                                  .L8:
```

```
long w = 1;
switch (x) {
case 2:
 w = y/z;
case 3:
 w += z;
 break;
return w;
```

```
.L5:
                   \# Case x == 2
         %rsi, %rax \# w = y
  movq
  cqto
                   # Expands w
        rcx # w = w / z
  idivq
         .L6 # Goto merge
  jmp
.L9:
                   \# Case x == 3
  movl $1, %eax # w = 1
.L6:
                   # merge:
  addq %rcx, %rax # w += z
  ret
```

```
register variable
%rdi x
%rsi y
%rcx z
%rax w
%rdx w(high bits for division)
```