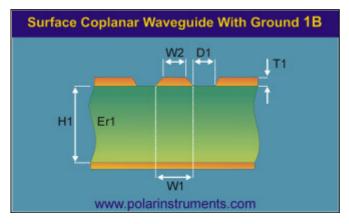
Polar Si8000 Controlled Impedance Quick Solver



				<u>Tolerance</u>	<u>Minimum</u>	<u>Maximum</u>	
Substrate 1 Height	H1	14.1055	+/-	0.0000	14.1055	14.1055	
Substrate 1 Dielectric	Er1	4.6000	+/-	0.0000	4.6000	4.6000	
Lower Trace Width	W1	20.0000	+/-	0.0000	20.0000	20.0000	
Upper Trace Width	W2	19.0000	+/-	0.0000	19.0000	19.0000	
Ground Strip Separation	D1	10.0000	+/-	0.0000	10.0000	10.0000	
Trace Thickness	T1	2.0000	+/-	0.0000	2.0000	2.0000	
-							
Impedance	Zo	50.026			0.000	0.000	
Impedance Delay (ps/in)	Zo D	50.026 145.835			0.000 0.000	0.000 0.000	
•							
Delay (ps/in)		145.835			0.000	0.000	
Delay (ps/in) Inductance (nH/in)	D L	145.835 7.296			0.000 0.000	0.000 0.000	
Delay (ps/in) Inductance (nH/in) Capacitance (pF/in)	D L C EEr	145.835 7.296 2.915			0.000 0.000 0.000	0.000 0.000 0.000	

Notes: (First 5 lines will print)

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