```
# create work library
    vlib work
 2
 3
 4
    # compile project files
 5
     \verb|vcom -2008 -explicit -work work ../../source/testbench_simple_dff\_circ.vhd|\\
     vcom -2008 -explicit -work work ../../source/simple_dff_circ.vhd
 6
 7
8
9
     # run the simulation
10
     vsim -novopt -t 1ns -lib work work.testbench_simple_dff_circ
11
    do ../scripts/wave.do
run 30.0 us
12
13
14
```