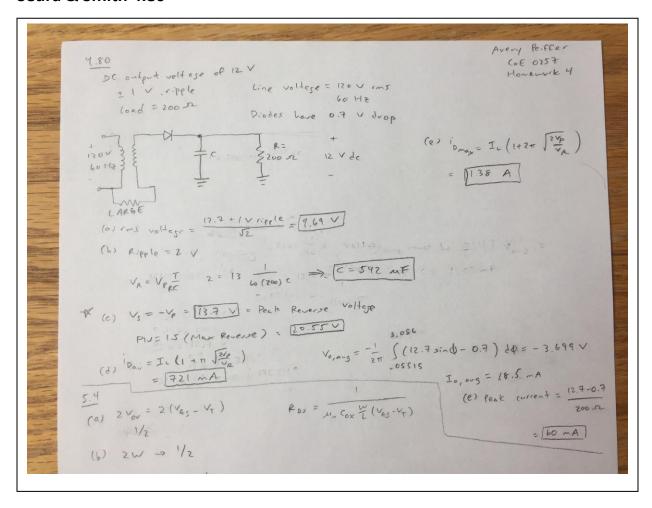
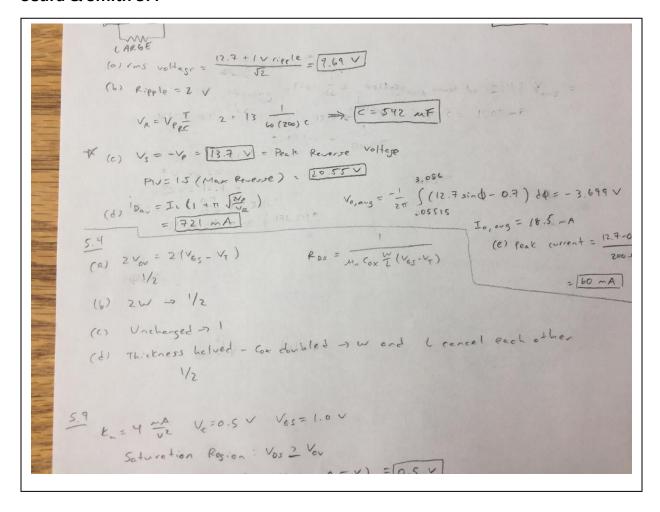
Homework Assignment #4

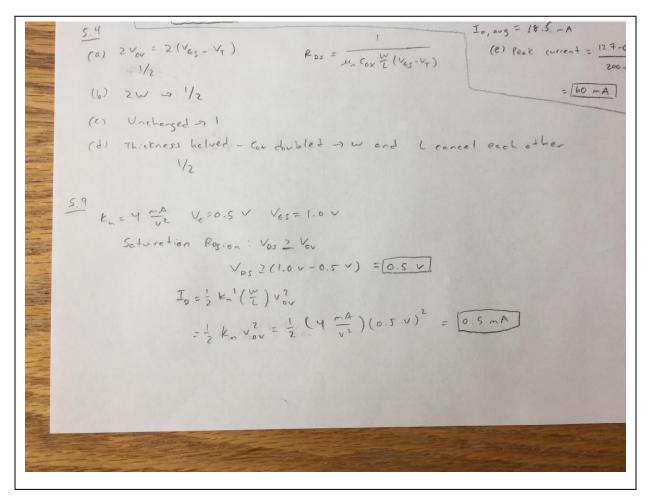
ECE 0257 – Spring 2019

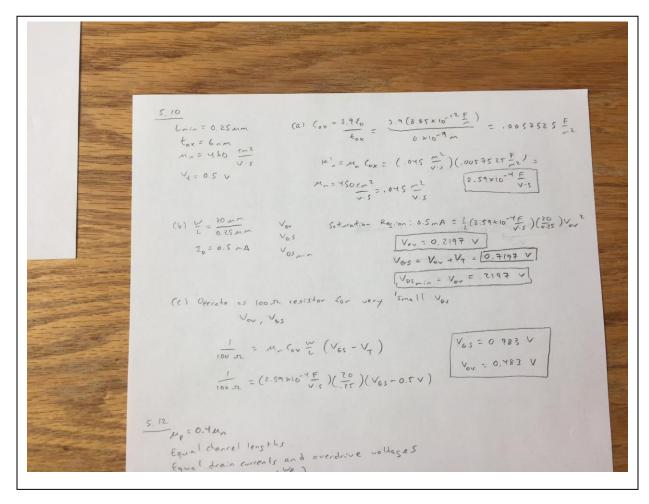
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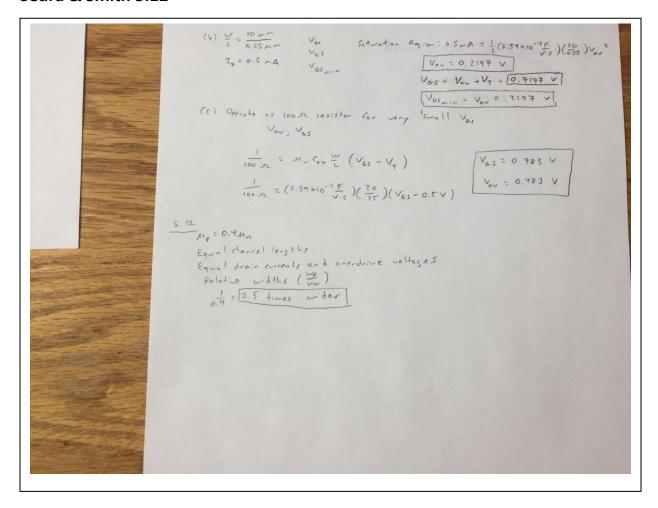
Avery Peiffer
Collaborators
Daniel Stumpp
Book Problems
Sedra & Smith 4.80
Sedra & Smith 5.4
Sedra & Smith 5.9
Sedra & Smith 5.10
Sedra & Smith 5.12
Sedra & Smith 5.14
Simulation Problems
DC power supply design
Check-list Before Submission
☐ Write within boxes, no boxes are moved
☐ Write your full names in designated area
☐ Save this file as a PDF before uploading, keep the number of pages (16) unchanged
□ Notify "TO BE CONTINUED" accordingly if you used the extra pages (page 14-16)
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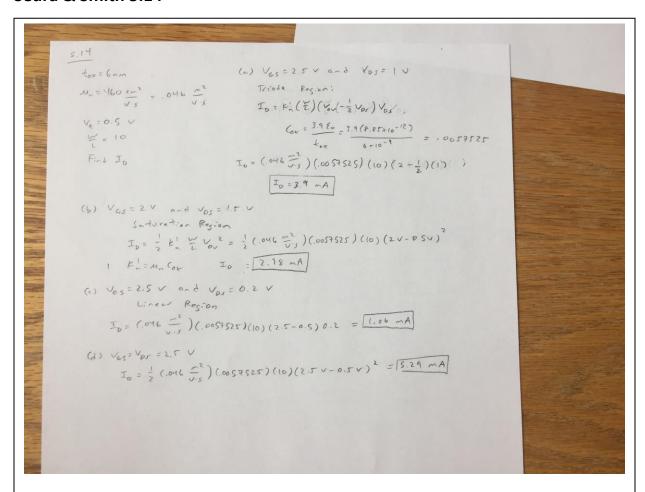








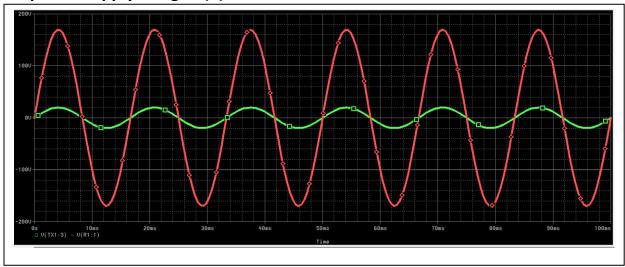




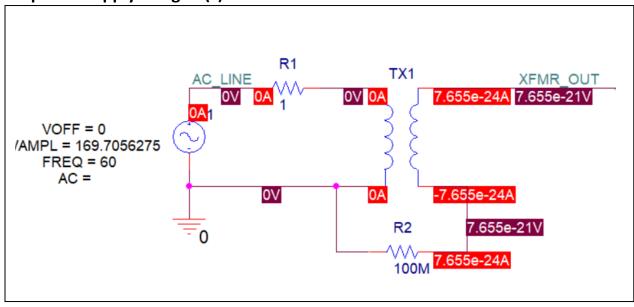
DC power supply design 1(a)

 $L_1 = 2 \text{ H}$ $L_2 = 27.2 \text{ mH}$ $Calculations: N_1/N_2 = V_1/V_2$ $N_1/N_2 = 120 \text{ V}/14 \text{ V}$ $N_1/N_2 = 60 \text{ winds}/7 \text{ winds}$ $N_1/N_2 = \text{sqrt}(L_1/L_2) 60/7 = \text{sqrt}(L_1/L_2)$ $L_1/L_2 = 73.469, \text{ Let } L_1 = 2 \text{ H then } L_2 = 27.2 \text{ mH}$

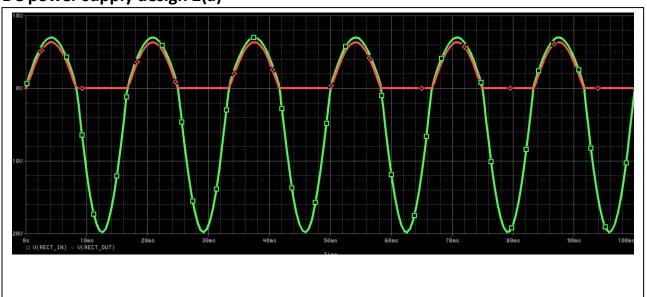
DC power supply design 1(b)



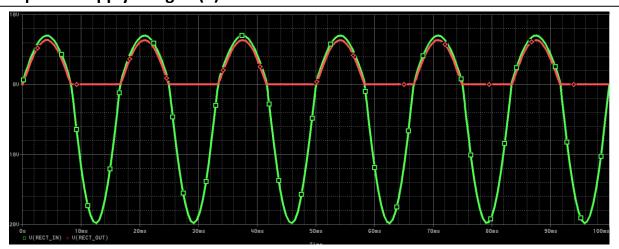
DC power supply design 1(c)



DC power supply design 2(a)



DC power supply design 2(b)



Cursor Calculations: Left Click on V(RECT_IN) at peak, Right Click on

V(RECT_OUT) immediately below

V(RECT_IN) = 7.0032 V

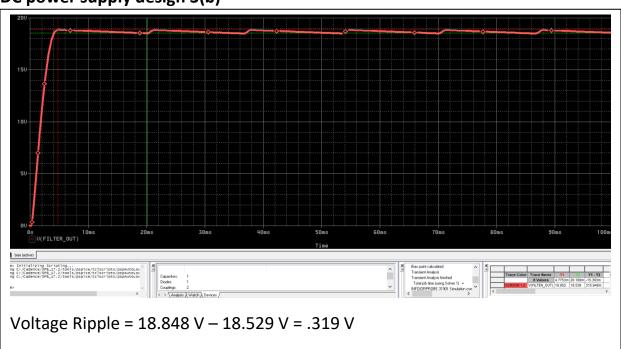
V(RECT_OUT) = 6.3538 V

 $V_d = .6494 V$

DC power supply design 3(a)



DC power supply design 3(b)



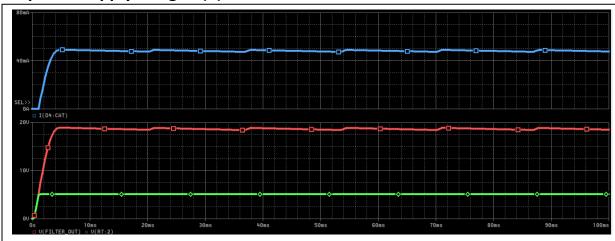
DC power supply design 4(a)

Zener voltage = 5.1 V at 49 mA.

49 mA = (18.848 V - 5.1 V) / R

-> R = 280.51 ohms

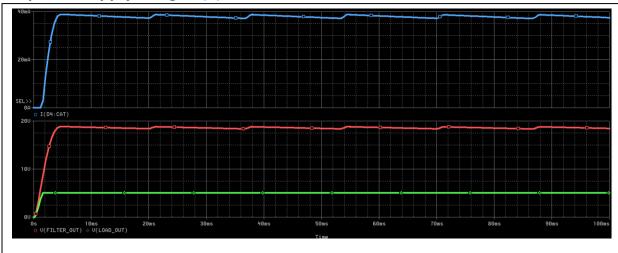
DC power supply design 4(b)



 $V(REG_IN) = .319 V$ $V(REG_OUT) = 5.1002 V - 5.0997 V = .0005 V$

Line Regulation = .0005 V/.319 V = 1.57 mV/V

DC power supply design 5(a)



DC power supply design 5(b)

Ripple 1 = 18.852 V - 18.431 V = .421 V

Ripple 2 = 5.0966 V - 50960 V = .0006 V

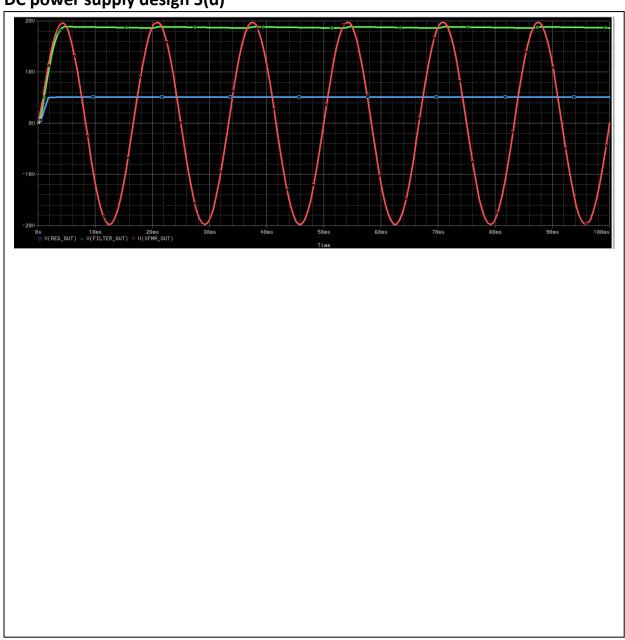
Line Regulation = .0006 V / .421 V = 1.43 mV/V

The ripple voltages increase for both measured values because the current through the Zener diode decreases, meaning it is not as far in the breakdown region.

DC power supply design 5(c)

I changed the capacitance in the filter circuit, which led to an improvement in the line regulation. Increasing the capacitance increases the time constant, which means that it will take longer for the voltage to decrease to a specified voltage. Likewise, when being measured at a specific point in time, the voltage will not be able to decrease as much, effectively decreasing the ripple voltage.

DC power supply design 5(d)



EXTRA PAGES

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EXTRA PAGES

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