

Fill in the 7 instructions yourself please. Again, the beq branch is taken so the first lw instruction will be repeated.

Cycle	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25
lw t0, 8(s0)	IF	ID	EX	MEM	WB																				
addi t0, t0, 20			IF	ID	EX	MEM	WB																		
sw t0, 8(s0)				IF	ID	EX	MEM	WB																	
lw t0, 12(s0)					IF	ID	EX	MEM	WB																
sw t0, 16(s0)							IF	ID	EX	MEM	WB														
beq t0, s0, loop							IF	ID	EX	MEM	WB														
lw t0, 8(s0)								IF	ID	EX	MEM	WB													

Write the list of VLIW instructions based on the above schedule (note: you may have less than 10 instructions).

Insts	ALU/Branch	Load/Store
1	nop	lw t0, 8(s0)
2	nop	nop
3	addi t0, t0, 20	nop
4	nop	sw t0, 8(s0)
5	nop	lw t0, 12(s0)
6	nop	nop
7	beq t0, s0, loop	sw t0, 16(s0)
8	nop	lw t0, 8(s0)
9		
10		