



X1000_BOARD_V1.0

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刘欣修改记录:

由于重画一遍时间太长容易出错。所以这次在参考原理图上进行修改。

- 1 test点封装修改。mark点封装修改。
- 2 c3216封装修改为t-b; 10uf的封装修改为0603
- 3 拨码开关修改为JP
- 4 usbhost改为mini
- 5 TF card changed to 8bit SDinterface
- 6 power changed to ea3036
- 7 修改掉一些不用的器件和Io
- 8 接口
- 9 网口

第一次修改记录:

power changed to SW801
启动模式连接stm32
10ufchangeto 0402
change to usbmicoro & modify pcbdecal
RJ45 chagne to HR911105A with transform in

change log 20160219:

- 1 CON2和CON3的vsys (+5V) 修改为+3.3V
- 2 J2从5脚改为6脚, 增加了rst
- 3 U6的jtag管脚一些变动,value改为STM32F103
- 4 连接x1000的rst到U6
- 5 x1000的uart2不连接接插件, 改为连到U6和J3
- 6 X1000的pwrn悬空。wkup连个按键并接到电源pwrn
- 7 RST上也添加按键
- 8 usbID接错了, 改正确

INGENIC SEMICONDUCTOR CO.,LTD			
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A3	COVER SHEET/POWER ARCHITECTURE	2.0	
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D

D

C

C

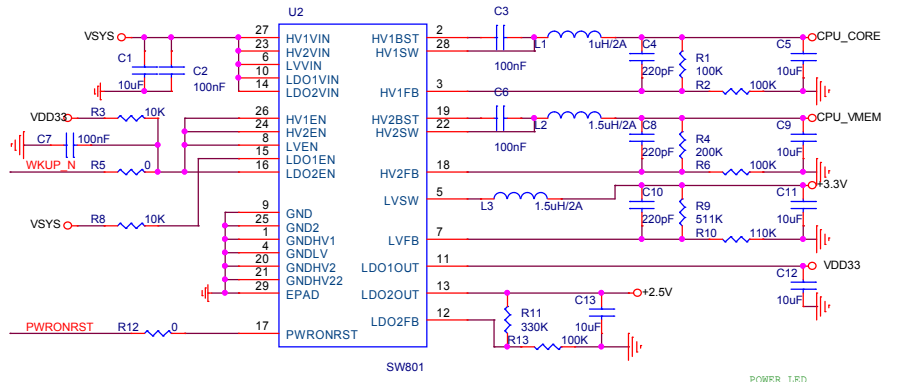
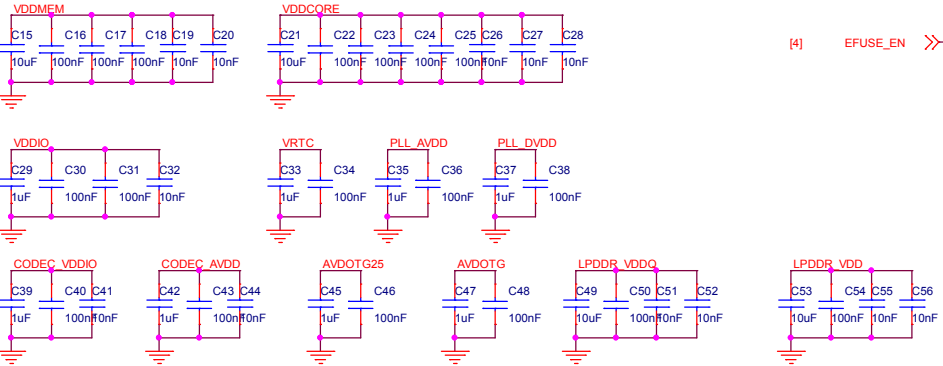
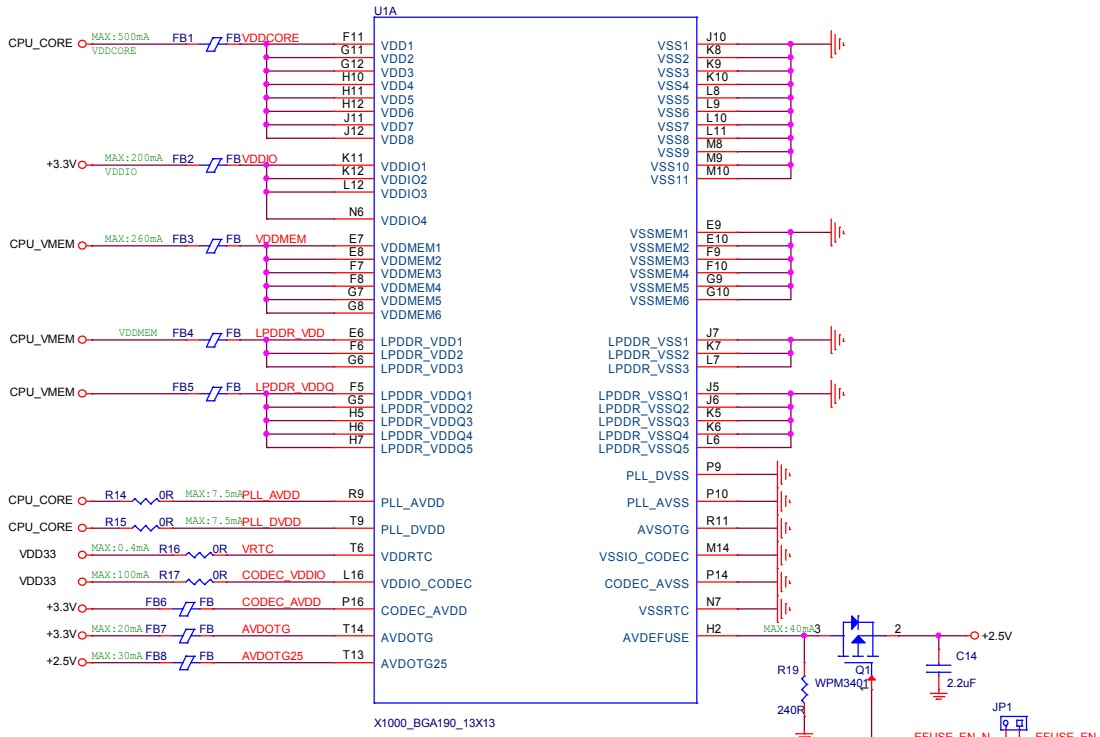
B

B

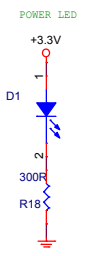
A

A

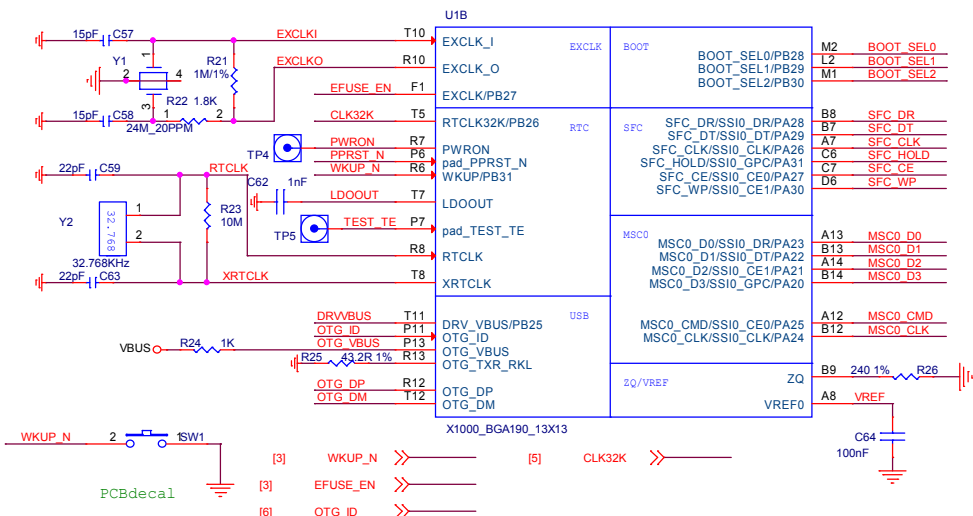
INGENIC SEMICONDUCTOR CO.,LTD			
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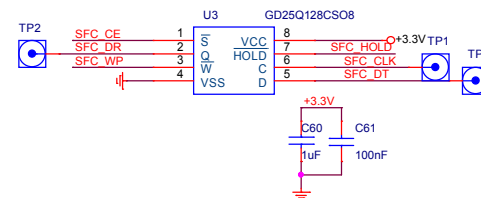
[4] WKUP_N >>>
[4,7] PWRONRST>>>



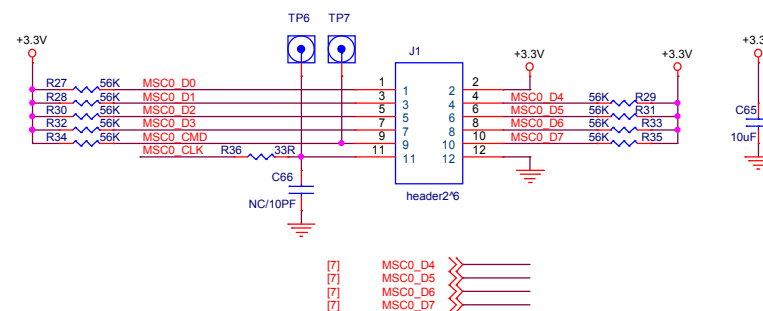
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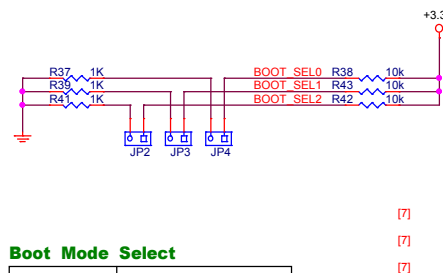
SFC FLASH



SD扩展板上要对SDCD处理！！



8bit SD CARD interface

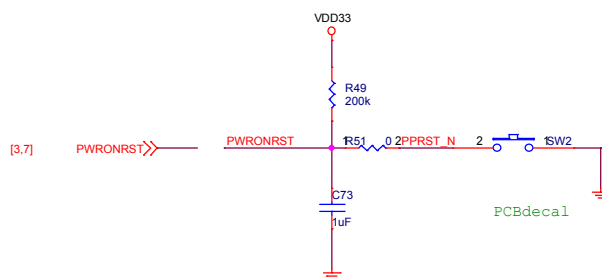


BOOT CFG & VREF

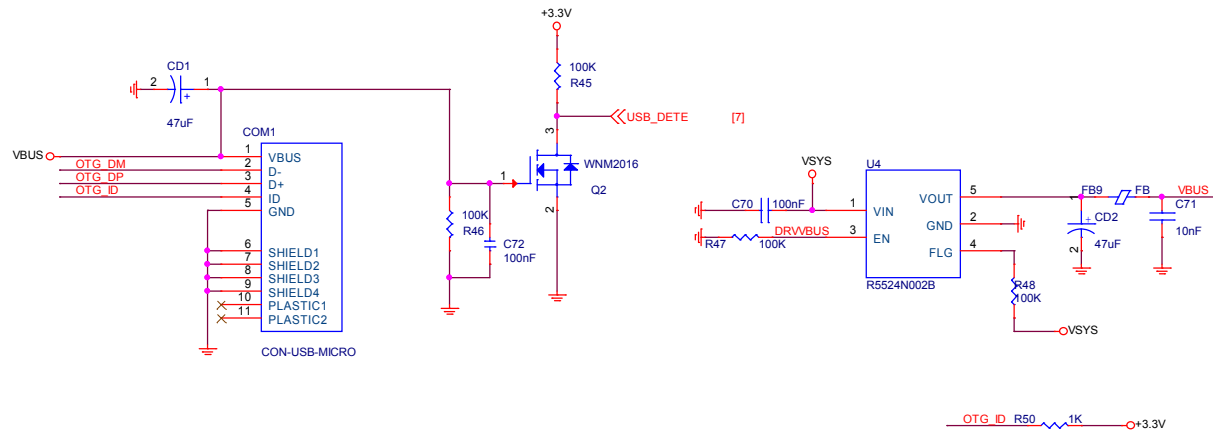
Boot Mode Select

BOOT_SEL[2:1:0]	BOOT FROM
111	SFC Boot @ 26M
011	SFC Boot @ 24M(default)
101	SD Boot @ 26M
001	SD Boot @ 24M
110	USB Boot @ 26M
010	USB Boot @ 24M

RESET & INDICATE

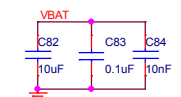


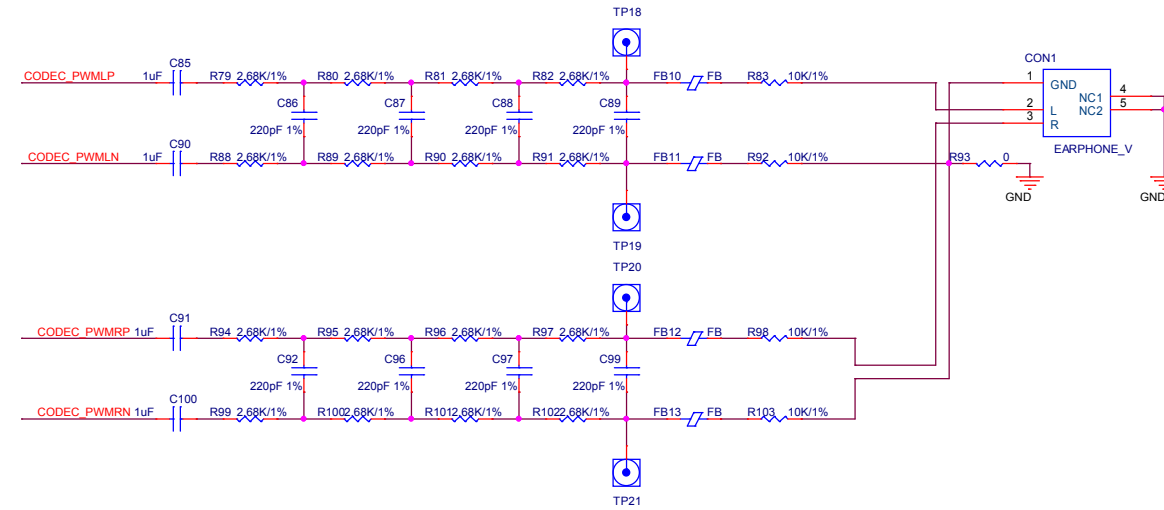
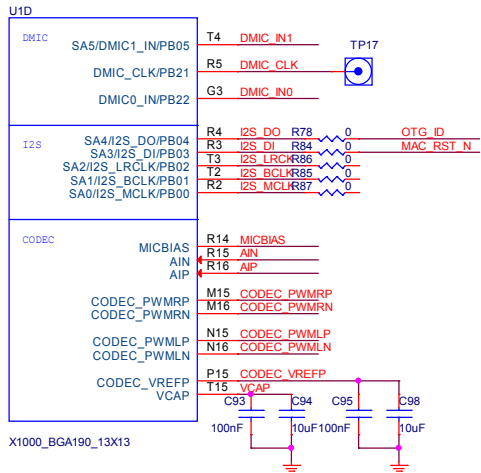
USB OTG



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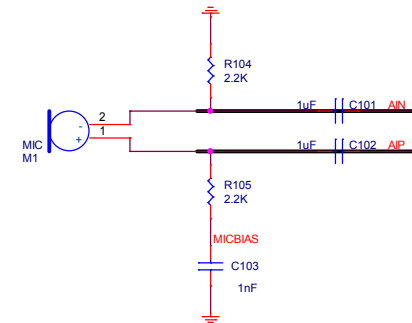


AUDIO AMPLIFIER

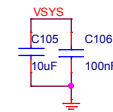
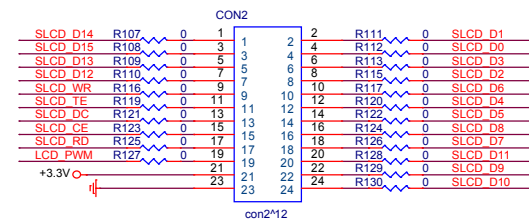
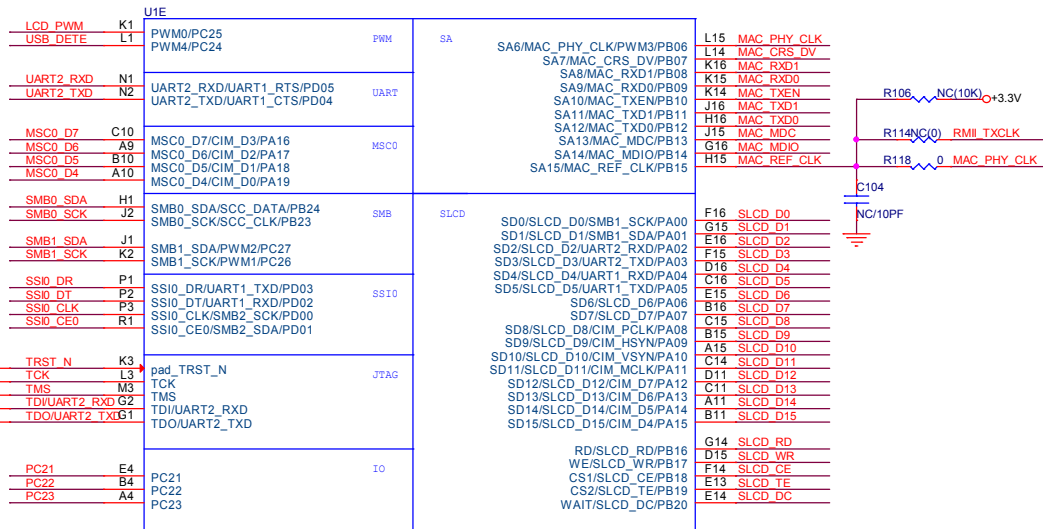
DMIC



AMIC

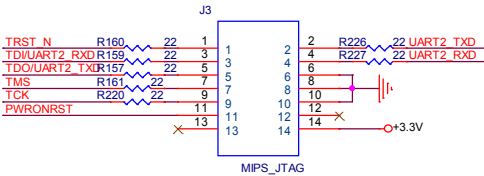


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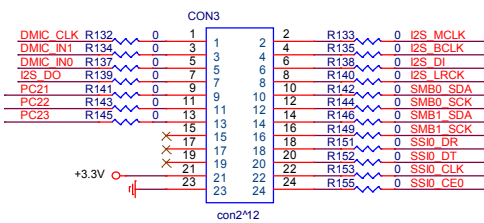


LCD

JTAG

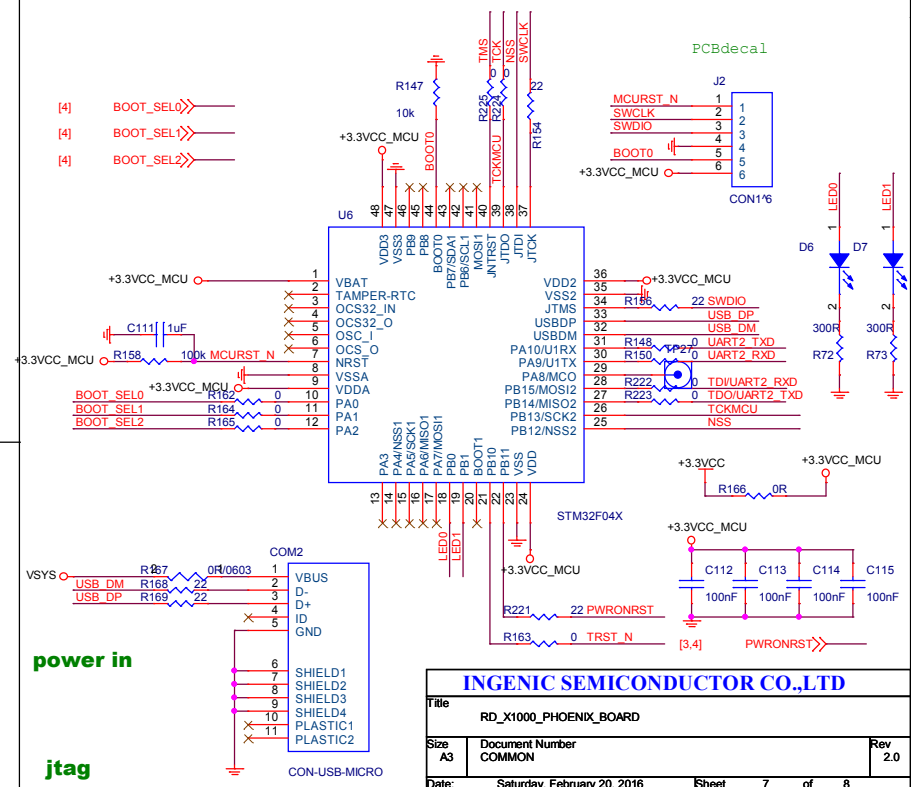
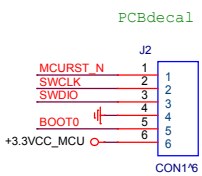


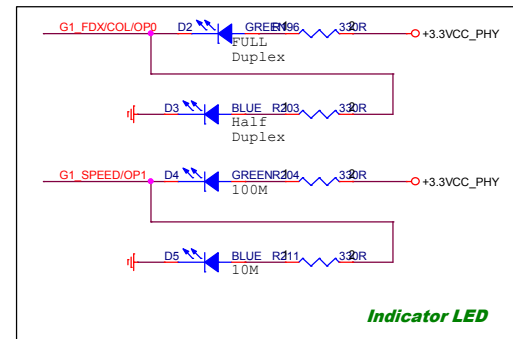
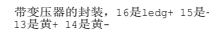
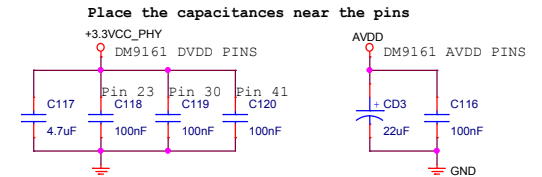
2*7 2.0间距，弯插。
pcb放置在板边，
奇数脚这排在里（板中），
偶数脚这排在外（板边）



peripheral

[4] BOOT_SEL0
[4] BOOT_SEL1
[4] BOOT_SEL2





[7] MAC_PHY_CLK
 [7] RMI_TXCLK
 [6] MAC_RST_N

 [7] MAC_CRS_DV
 [7] MAC_RXD1
 [7] MAC_RXD0
 [7] MAC_TXEN
 [7] MAC_TXD1
 [7] MAC_TXD0
 [7] MAC_MDC
 [7] MAC_MDIO