

```
1  #include "interrupt.h"
2
3  void SysTick_Init()
4  {
5
6      SysTick->CTRL = 0x00;
7      SysTick->VAL = 0x00;
8      SysTick->LOAD = 0xF0; //generates an interrupt every 10 microseconds
9      SysTick->CTRL = 0x07;
10 }
11 void NVIC_Init1()
12 {
13
14     RCC->APB2ENR |= RCC_APB2ENR_AFIOEN | RCC_APB2ENR_IOPCEN;
15     GPIOC->CRH |= GPIO_CRH_MODE9 | GPIO_CRH_MODE8 ;
16     GPIOC->CRH &= ~GPIO_CRH_CNF9 & ~GPIO_CRH_CNF8 ;
17     GPIOA->CRL |= GPIO_CRL_CNF4_1;
18     GPIOA->CRL &= ~GPIO_CRL_CNF4_0 & ~GPIO_CRL_MODE4;
19     AFIO->EXTICR[1] &= ~AFIO_EXTICR2_EXTI4;
20     AFIO->EXTICR[0] &= ~AFIO_EXTICR1_EXTI0;
21     EXTI->IMR |= EXTI_IMR_MR0 | EXTI_IMR_MR4;;
22     EXTI->FTSR |= EXTI_FTSR_TR4 | EXTI_FTSR_TR0;;
23     NVIC->ISER[0] |= NVIC_ISER_SETENA_10 | NVIC_ISER_SETENA_6;
24 }
```