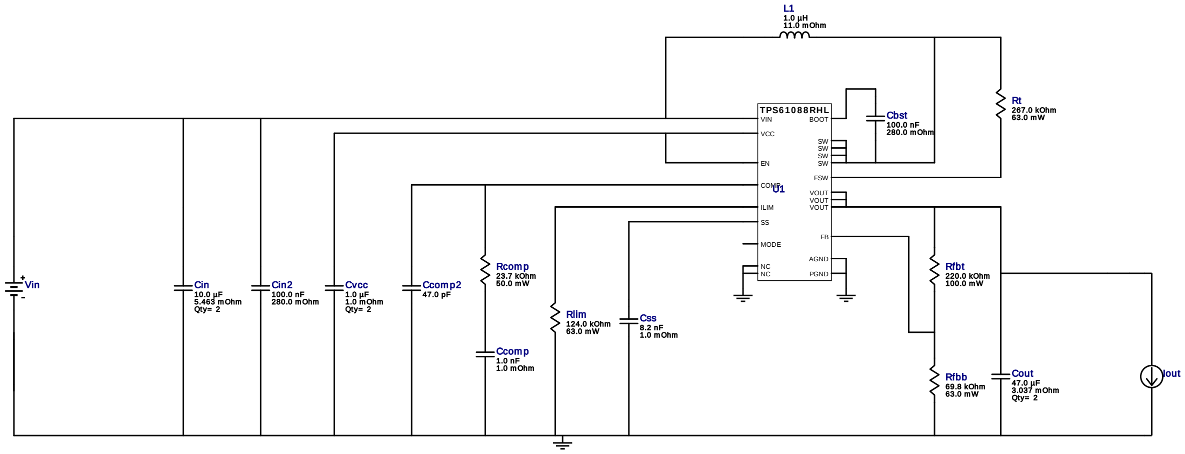
VinMin = 3.3V
VinMax = 4.2V
Vout = 5.0V
Iout = 4.0A

Device = TPS61088RHLR
Topology = Boost
Created = 2025-10-13 17:38:16.127
BOM Cost = $1.77
BOM Count = 18
Total Pd = 1.43W

WEBENCH ® Design Report

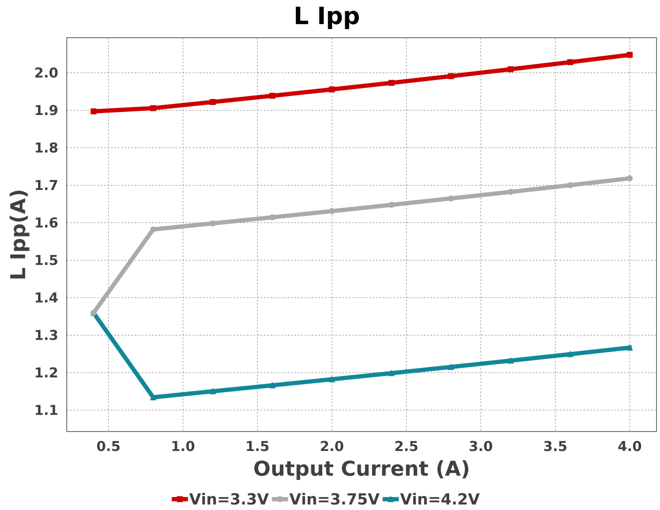
Design : 13 TPS61088RHLR
TPS61088RHLR 3.3V-4.2V to 5.00V @ 4A

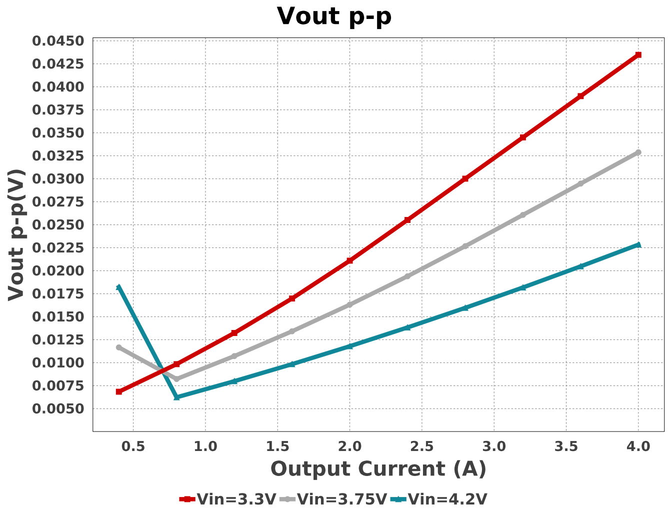


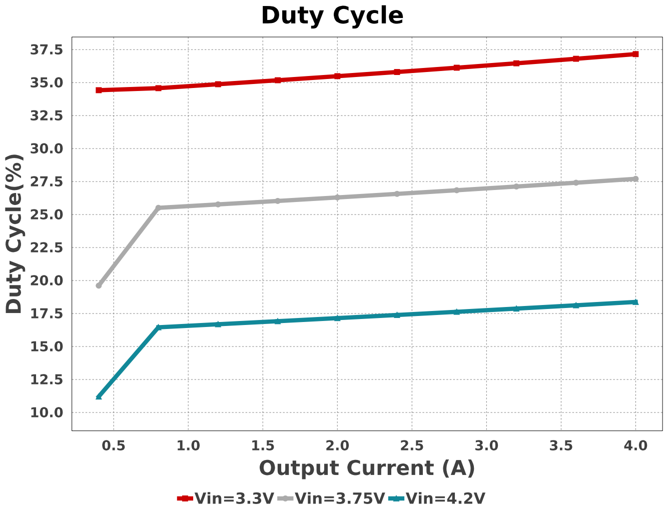
Electrical BOM

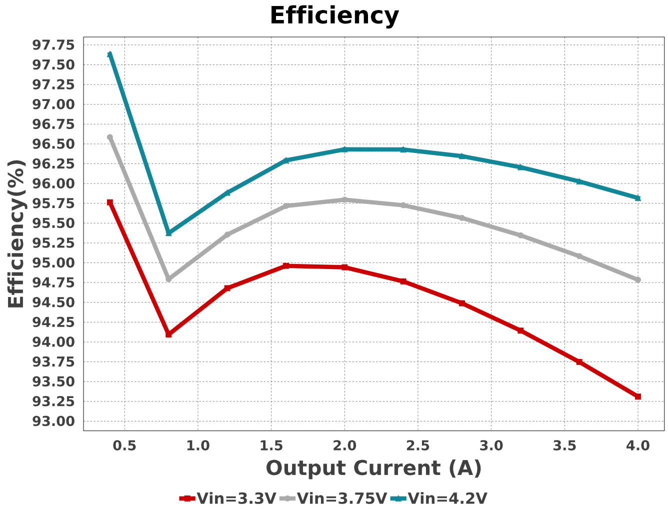
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cbst	AVX	08053C104KAT2A Series= X7R	Cap= 100.0 nF ESR= 280.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	$0.01	0805 7 mm²
Ccomp	MuRata	GRM033R71C102KA01D Series= X7R	Cap= 1.0 nF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	$0.01	0201 2 mm²
Ccomp2	MuRata	GRM0335C1E470JA01D Series= C0G/NP0	Cap= 47.0 pF VDC= 25.0 V IRMS= 0.0 A	1	$0.01	0201 2 mm²
Cin	Taiyo Yuden	MSAST21GBB5106MTNA01 Series= X5R	Cap= 10.0 uF ESR= 5.463 mOhm VDC= 25.0 V IRMS= 2.36768 A	2	$0.04	0805 7 mm²
Cin2	AVX	08053C104KAT2A Series= X7R	Cap= 100.0 nF ESR= 280.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	$0.01	0805 7 mm²
Cout	MuRata	GRM32ER61C476KE15L Series= X5R	Cap= 47.0 uF ESR= 3.037 mOhm VDC= 16.0 V IRMS= 4.59346 A	2	$0.17	1210_280 15 mm²
Css	MuRata	GRM155R71C822KA01D Series= X7R	Cap= 8.2 nF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	$0.01	0402 3 mm²
Cvcc	Taiyo Yuden	EMK107B7105KA-T Series= X7R	Cap= 1.0 uF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	2	$0.01	0603 5 mm²
L1	TDK	VLP8040T-1R0N	L= 1.0 µH 11.0 mOhm	1	$0.22	VLP8040 113 mm²

Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Rcomp	Yageo	RC0201FR-0723K7L Series= ?	Res= 23.7 kOhm Power= 50.0 mW Tolerance= 1.0%	1	$0.01	0201 2 mm^2
Rfbb	Vishay-Dale	CRCW040269K8FKED Series= CRCW..e3	Res= 69.8 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
Rfbt	Yageo	RC0603FR-07220KL Series= ?	Res= 220.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	$0.01	0603 5 mm^2
Rlim	Vishay-Dale	CRCW0402124KFKED Series= CRCW..e3	Res= 124.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
Rt	Vishay-Dale	CRCW0402267KFKED Series= CRCW..e3	Res= 267.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
U1	Texas Instruments	TPS61088RHLR	Switcher	1	$1.01	RHL0020A 25 mm^2

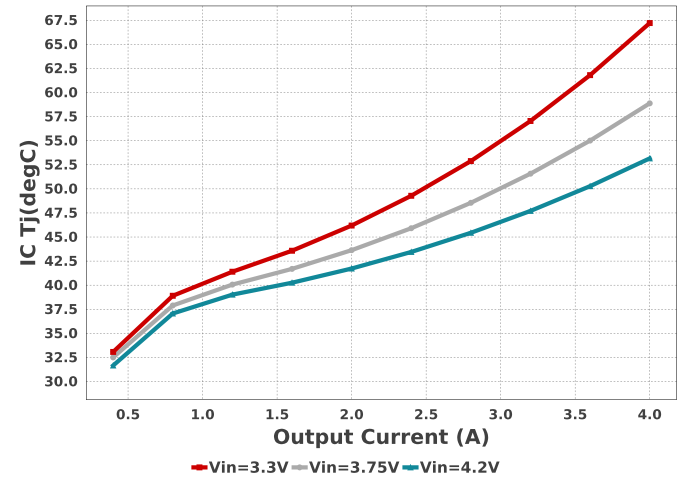




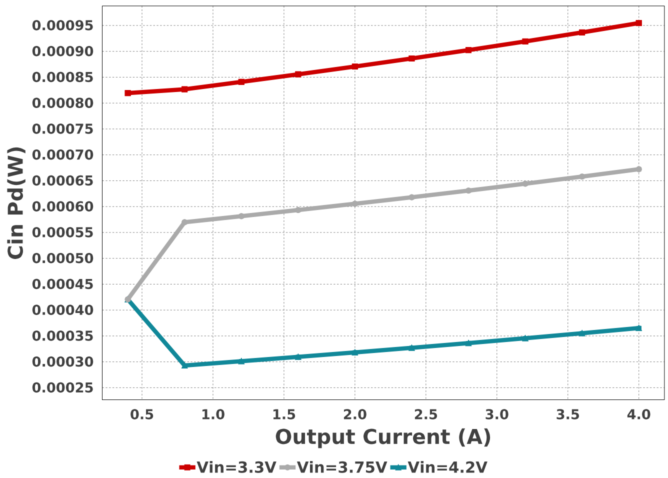




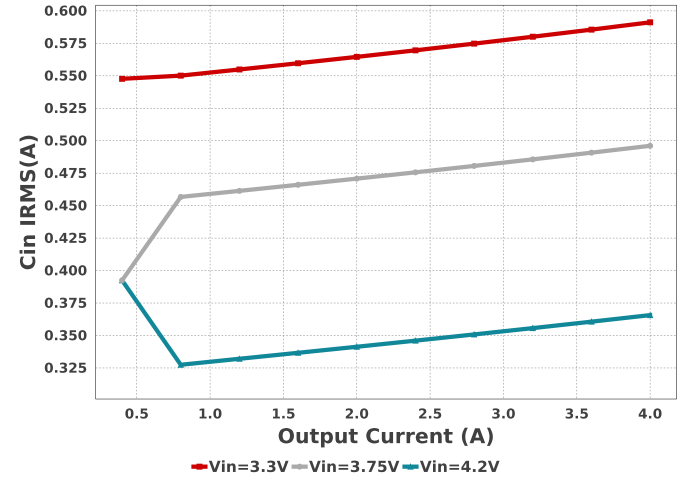
IC Tj



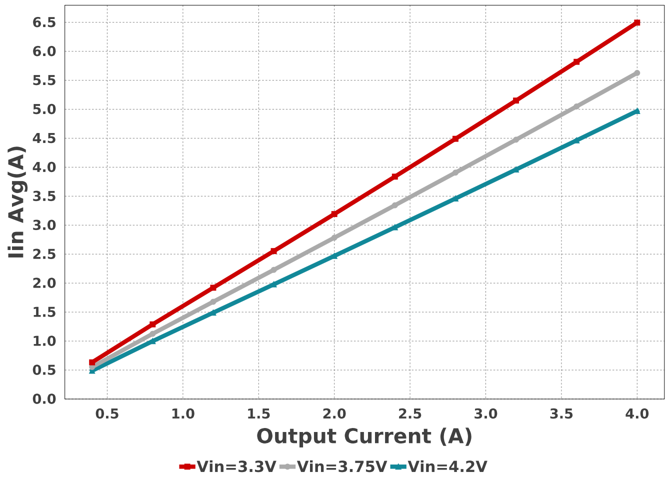
Cin Pd



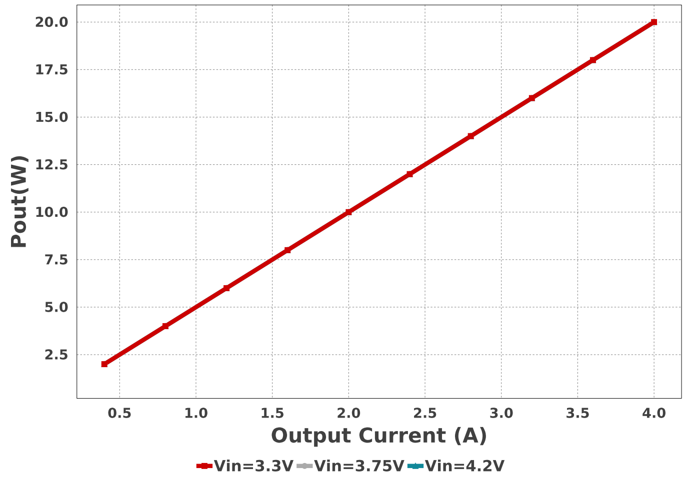
Cin IRMS



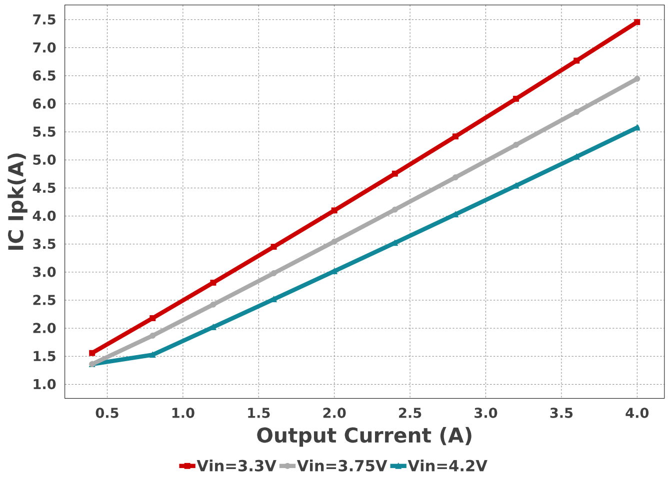
Iin Avg

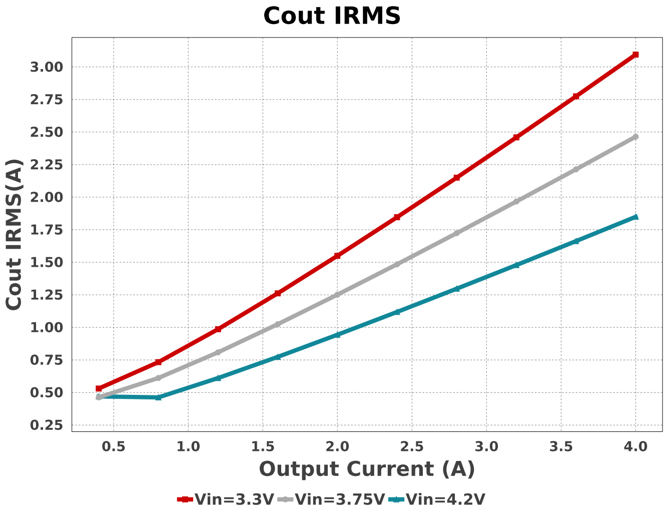


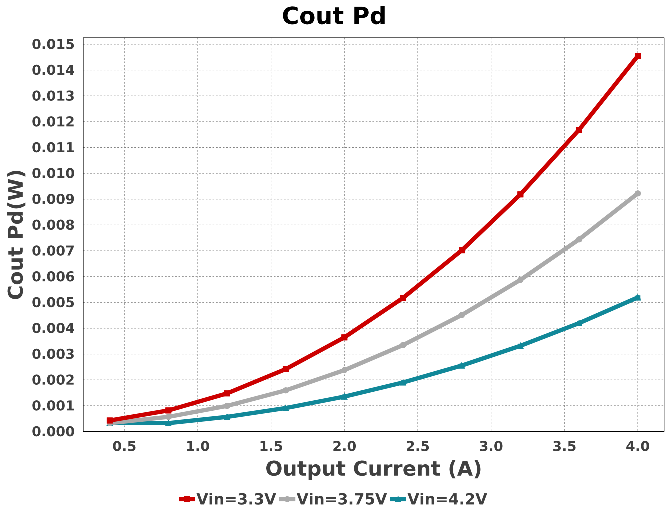
Pout

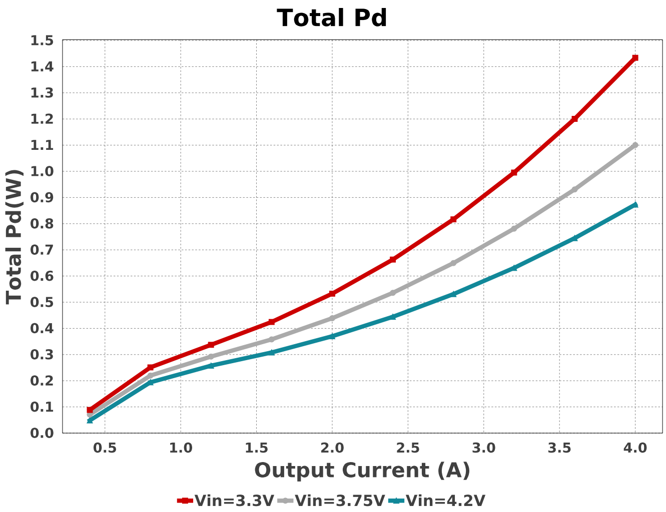


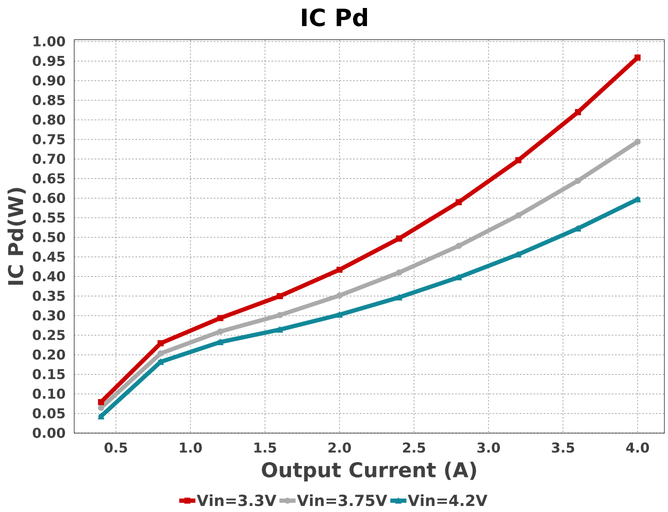
IC Ipk

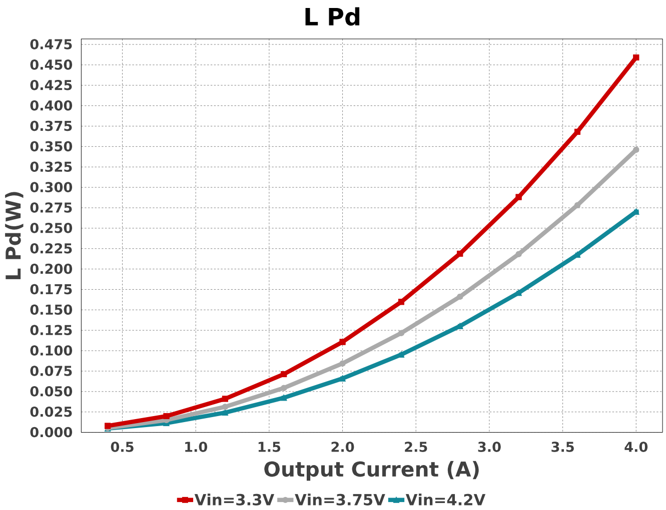


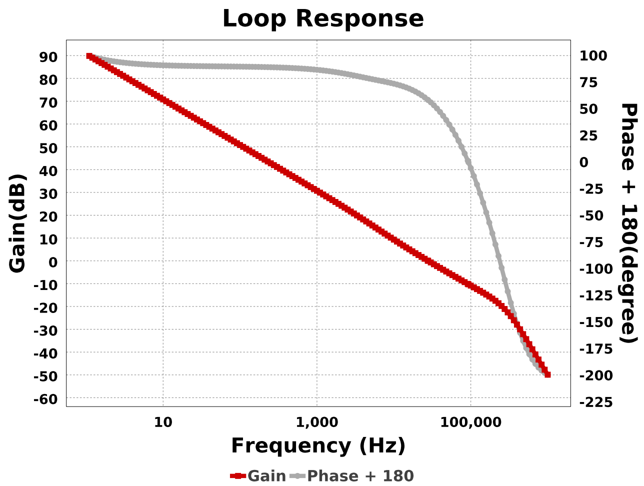

Cout IRMS
Cout IRMS(A)
Output Current (A)
Vin=3.3V Vin=3.75V Vin=4.2V

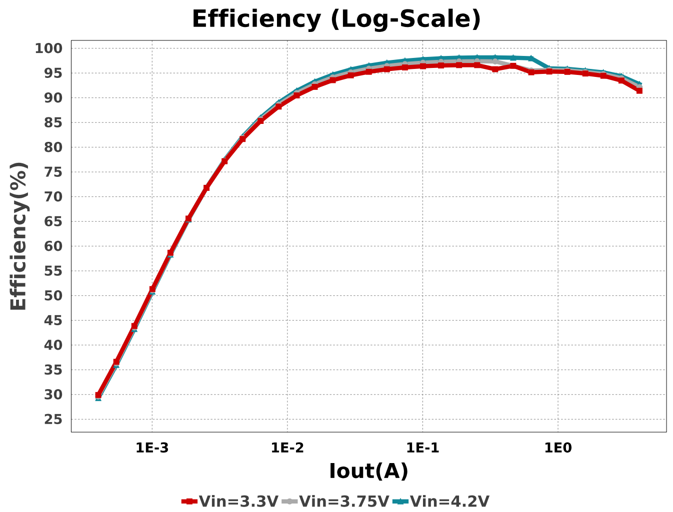

Cout Pd
Cout Pd(W)
Output Current (A)
Vin=3.3V Vin=3.75V Vin=4.2V


Total Pd
Total Pd(W)
Output Current (A)
Vin=3.3V Vin=3.75V Vin=4.2V


IC Pd
IC Pd(W)
Output Current (A)
Vin=3.3V Vin=3.75V Vin=4.2V


L Pd
L Pd(W)
Output Current (A)
Vin=3.3V Vin=3.75V Vin=4.2V


Loop Response
Gain(dB)
Phase + 180(degree)
Frequency (Hz)
Gain Phase + 180



Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	591.184 mA	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	954.65 µW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	3.095 A	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	14.542 mW	Capacitor	Output capacitor power dissipation
5.	IC Ipk	7.457 A	IC	Peak switch current in IC
6.	IC Pd	959.01 mW	IC	IC power dissipation
7.	IC Tj	67.209 degC	IC	IC junction temperature
8.	ICThetaJA	38.8 degC/W	IC	IC junction-to-ambient thermal resistance
9.	Iin Avg	6.495 A	IC	Average input current
10.	L Ipp	2.048 A	Inductor	Peak-to-peak inductor ripple current
11.	L Pd	459.04 mW	Inductor	Inductor power dissipation
12.	Cin Pd	954.65 µW	Power	Input capacitor power dissipation
13.	Cout Pd	14.542 mW	Power	Output capacitor power dissipation
14.	IC Pd	959.01 mW	Power	IC power dissipation
15.	L Pd	459.04 mW	Power	Inductor power dissipation
16.	Total Pd	1.434 W	Power	Total Power Dissipation
17.	BOM Count	18	System Information	Total Design BOM count
18.	Cross Freq	27.656 kHz	System Information	Bode plot crossover frequency
19.	Duty Cycle	37.158 %	System Information	Duty cycle
20.	Efficiency	93.311 %	System Information	Steady state efficiency
21.	FootPrint	227.0 mm^2	System Information	Total Foot Print Area of BOM components
22.	Frequency	598.767 kHz	System Information	Switching frequency
23.	Gain Marg	-10.183 dB	System Information	Bode Plot Gain Margin
24.	Iout	4.0 A	System Information	Iout operating point
25.	Low Freq Gain	89.662 dB	System Information	Gain at 1Hz
26.	Mode	BOOST CCM	System Information	PWM/PFM Mode
27.	Phase Marg	58.403 deg	System Information	Bode Plot Phase Margin
28.	Pout	20.0 W	System Information	Total output power
29.	Total BOM	$1.77	System Information	Total BOM Cost
30.	Vin	3.3 V	System Information	Vin operating point
31.	Vout Actual	4.999 V	System Information	Vout Actual calculated based on selected voltage divider resistors
32.	Vout Tolerance	4.19 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
33.	Vout p-p	43.478 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	4.0	Maximum Output Current
VinMax	4.2	Maximum input voltage
VinMin	3.3	Minimum input voltage
Vout	5.0	Output Voltage
base_pn	TPS61088	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

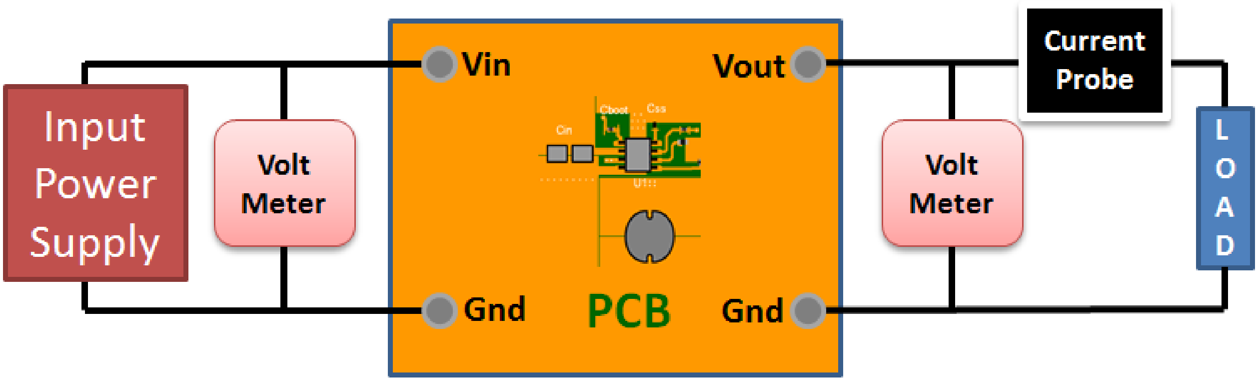
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 3.3V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. Master key : BA40E17A3BF621DF0B943C71FF12AB91[v1]

2. **TPS61088** Product Folder : http://www.ti.com/product/TPS61088 : contains the data sheet and other resources.

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