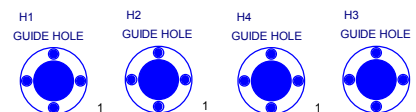


YG MEGA DCC CAMERA

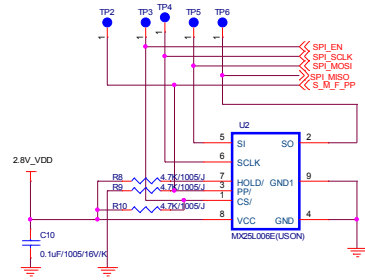
[illegible]

Version	Date	Description
0.1	2015.11.17	Prototype Ver 1.0

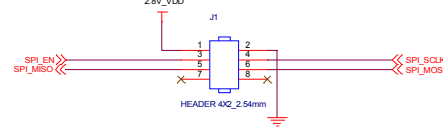
Documents	Description
BlockDiagrams	
Bill of Materials	
User's Manuals	



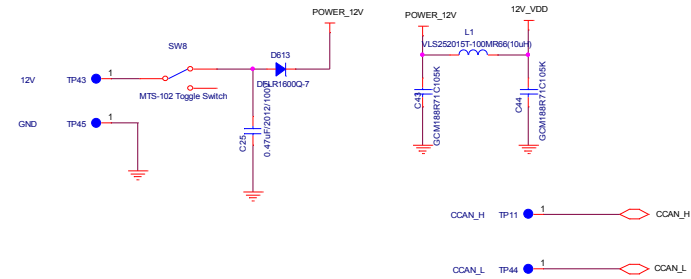
Serial Flash/2M



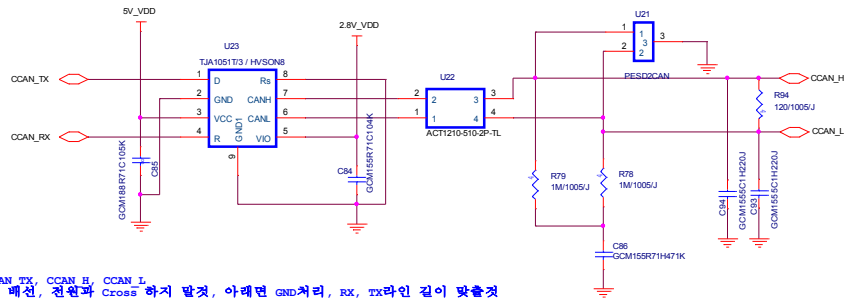
FLASH D/L PIN HEADER



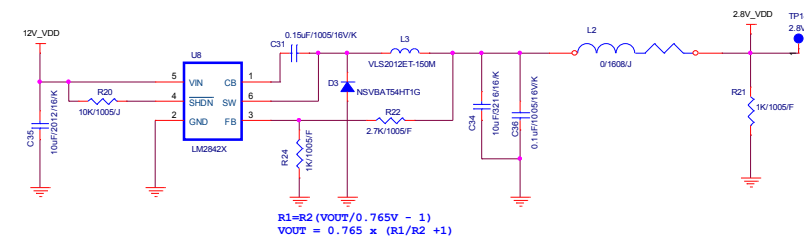
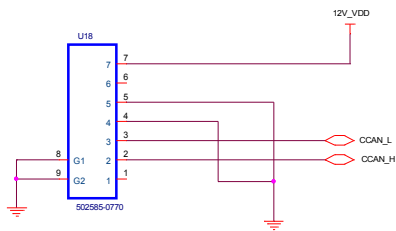
MAIN POWER INPUT



C CAN TRANCEIVER

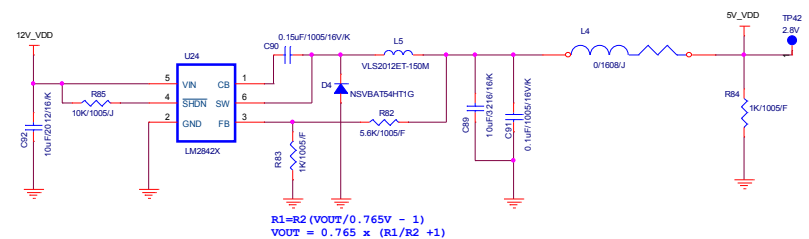


CCAN_RX, CCAN_TX, CCAN_H, CCAN_L : 평평하게 배선, 전원과 Cross 하지 않것, 아래면 GND처리, RX, TX라인 길이 맞출것



$$R1=R2(VOUT/0.765V - 1)$$

$$VOUT = 0.765 \times (R1/R2 + 1)$$



$$R1=R2(VOUT/0.765V - 1)$$

$$VOUT = 0.765 \times (R1/R2 + 1)$$