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| Phone number: +84 369 649 641  Email: dmt07071995@gmail.com  LinkedIn: <https://www.linkedin.com/in/minhtandam>  **DAM MINH TAN**  **Skills** | | |
| * **Programming languages:**   + Scripting Language: C-shell, Perl, Tcl, Python.  + Hardware Description language: Verilog, System Verilog.  + Assembly language: Arm assembly language(for Combine Test). * **Tools:**  + Synthesis compiler: DC compiler, Fusion compiler  + Simulation: VCS, NCsim.  + Other: FM, Primetime, Spyglass * **Languages:** TOEIC 615 (April 2020) | | |
| **Experience** | | |
| **Engineer** | **Renesas Design Vietnam** | **Mar 2018 – Now** |
| * Design and Verify for BUS module (1 year experience) * Design and verify for Error Controller Module (6 months experience) * Design for SD card host interface (1 year experience) | | |
| **Technical Skill and Knowledge** | | |
| **Hardware Design:** |  |  |
| * Have Knowledge about APB protocol, AXI protocol, BUS Structure of SoC, AMBA generator. * Have experience for Design Error checking/controller module, Bus module. * Basic knowledge of hardware design flow and basic knowledge of DFT, STA and Backend (Renesas Basic Training course) * Ability perform Synthesis phase as well as judgement synthesis checker (STAcheck, DFTcheck, HLDRC) and use SDC constraints for synthesis.   **Verification**:   * Ability to conduct Combine Test, trace and debug using waveform. * Ability to conduct code coverage, branch coverage. | | |
| **Working Manner** | | |
| * Good at communication and interpersonal skill. * Good at teamwork, as well as independently. * Ability to work under pressure. * Take personal responsibility and integrity. * Automation thinking with repeated work. | | |
| **Education** | | |
| **Ho Chi Minh City** | **Bach Khoa University** | **Sep 2013 – May 2018** |
| * Electrical & Electronics Engineering * Bachelor’s Degree | | |
| **Related Course:**  “Intermediate IC Design Course”of [Semiconvn](https://www.semiconvn.com/) | | |