

Grant Robinson

From: Mcconnell, DAVE P <dave.p.mcconnell@lmco.com>
Sent: Tuesday, May 08, 2018 2:13 PM
To: Daryl Dixon
Cc: Grant Robinson; Nattawut Wongyai; hums; Nakhon Avionics
Subject: S-92 chip detector processor channel assignments

Daryl;

FYI

As a companion piece to my previous email re S-76D chip detector process channel assignments, following is the CSE response to my request for similar information for the S-92.

Case Number: C0094057
Customer: Thai Aviation Services Limited
Aircraft S/N: 920000
Aircraft Model: S-92A
Part Number: 92600-02801-101
Case Subject: S-92 Chip Detector Processor Channel Assignments

Problem Description:

TAS Avionics note that information regarding chip system processor channel assignments is not easily found in the S-92 AMM and WSM.

TAS recommend that the following information be consolidated in a single location for easy reference.

Their research indicates the following:

Channel 1 = MGB chip detector
Channel 2 = L/H Input detector
Channel 3 = R/H input detector
Channel 4 = L/H Acc chip detector
Channel 5 = R/H Acc chip detector
Channel 6 = IGB chip detector
Channel 7 = TGB chip detector

Please confirm that the above is correct.

Also please advise if there are any unused channels. EoM

This email notification is provided as the case has been reviewed and does not contain technical data in the issue description.

Problem Resolution:

The channels provided by the customer are correct.

For a list of the channels please reference AMM 31-31-00 Page 198ED, TABLE 199CV - DCU_Chip0_Discrete. Channels 9-11 are not used.

This email notification is provided as the case has been reviewed and does not contain technical data in the resolution.

David P. McConnell

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