

Logic Design Lab Viva Questions And Answers

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Logic Design Lab Viva Questions

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Logic Design Laboratory Manual 3 _____ VIVA QUESTIONS: 1. Why NAND & NOR gates are called universal gates? 2. Realize the EX - OR gates using minimum number of NAND gates. 3. Give the truth table for EX-NOR and realize using NAND gates? 4. What are the logic low and High levels of TTL IC's and CMOS IC's? 5. Compare TTL logic family with ...

LOGIC DESIGN LABORATORY MANUAL - ElectricVLab

Home » Digital Logic Design LAB VIVA Questions and Answers » What is slack? What is slack? Posted on August 19, 2018 November 22, 2015 by engineer Leave a comment 'Slack' is the amount of time you have that is measured from when an event 'actually happens' and when it 'must happen'.. The term 'actually happens' can also be ...

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Digital logic circuits important question and answers for 5 units 1. 1 Digital Logic Circuits 2 marks1) Given the two binary numbers $X = 1010100$ and $Y = 1000011$, perform the subtraction (a) $X - Y$ and (b) $Y - X$ using 2's complements.

Digital logic circuits important question and answers for ...

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Engineers: DIGITAL ELECTRONICS LAB VIVA QUESTIONS

And in the digital electronic, the logic high is denoted by the presence of a voltage potential. Sushanth KJ, Asst. Pr of. Dept. of ECE, BIT, Mangaluru Page 10 VLSI Lab Viva questions and answer s 81. Mention what are the different gates where Boolean logic are applicable?

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Commonly Asked Algorithm Interview Questions | Set 1 What is an algorithm? Informally, an algorithm is any well-defined computational procedure that takes some value, or set of values, as input and produces some value, or set of values, as output.

Commonly Asked Algorithm Interview Questions | Set 1 ...

Digital Electronics questions and answers with explanation for interview, competitive examination and entrance test. Fully solved examples with detailed answer description, explanation are given and it would be easy to understand.

Digital Electronics Questions and Answers - Aptitude

DIGITAL ELECTRONICS LAB EXPERIMENT NO: 1 Aim : - Introduction to Digital Electronics Lab- Nomenclature of Digital Ics, Specifications, Study of the Data Sheet, Concept of V_{cc} and Ground, Verification of the Truth Tables of Logic Gates using TTL Ics.

DIGITAL ELECTRONICS LAB - Bhagwant University

Viva Questions. Lorem Ipsum is simply ... Number System and their Conversion, Combinational

circuits, Sequential Logic Design, Logic Families and Semiconductor Memories, VLSI Design flow Read Notes. Analog Communication. ... Fin the laboratory manuals for all semesters. Download Now. Probable Questions.

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Lecture 2 - Logic Gates Practice Questions I | Digital Logic Design | MyLearnCube

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EENG115/INFE115 Introduction to Logic Design . This is core course of Electrical and Electronic Engineering and Information System Engineering that presents basic tools for the design of digital circuits. It serves as a building block in many disciplines that utilize data of digital nature like digital control, data communication, digital computers etc.

Course: Introduction to Logic Design

Digital design interview questions, Digital design interview questions & answers Suppose that a logic path travels through combinational logic from a source flip-flop to a destination flip-flop. If the destination flip-flop receives the clock tick later than the source flip-flop, and if the logic path delay is short enough, then the data ...

Digital design interview questions & answers - ASIC

What are Basic logic gates? | Learn basic digital gates in 6 min | AND, OR and NOT gates | DE.10 - Duration: 6:00. Practical Ninjas 74,218 views

Lecture 1 - Basic Logic Gates | Digital Logic Design | MyLearnCube

PDF | On Jun 10, 2013, Sulieman Bani-Ahmad and others published Digital logic design - lab's manual.

(PDF) Digital logic design - lab's manual. - ResearchGate

Logic gates are the basic building blocks of any digital system. It is an electronic circuit having one or more than one input and only one output. The relationship between the input and the output is based on a certain logic. Based on this, logic gates are named as AND gate, OR gate, NOT gate etc ...

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