

Linear Integrated Circuits Questions And Answers

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Op-amp is used to design a circuit whose output is the sum of several input signals. Such a circuit is called a summing amplifier or a summer or adder. If the input resistors are equal in value ($R_1 = R_2 = R$) then the summed output voltage is as given and the gain is +1. If the input resistors are unequal then the output voltage is a weighted ...

Op-Amp Adder and Subtractor - Linear Integrated Circuits ...

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