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64-bit computing - Wikipedia
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CPU cache - Wikipedia
ARM calls FIQ the fast interrupt, with the implication that IRQ is normal priority.In any real system, there will be many more sources of interrupts than just two devices and there will therefore be some external hardware interrupt controller which allows masking, prioritization etc. of these multiple sources and which drives the interrupt request lines to the processor.

arm - What is the difference between FIQ and IRQ interrupt ...
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