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In computer architecture, 64-bit computing is the use of processors that have datapath widths, integer size, and memory address widths of 64 bits (eight octets).Also, 64-bit computer architectures for central processing units (CPUs) and arithmetic logic units (ALUs) are those that are based on processor registers, address buses, or data buses of that size. . From the software perspective, 64 ...

### 64-bit computing - Wikipedia
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### 微控制器（单片机、MCU） - Infineon Technologies
Very-large-scale integration (VLSI) is the process of creating integrated circuits by combining thousands of transistors into a single chip.VLSI began in the 1970s when complex semiconductor and communication technologies were being developed. The microprocessor is a VLSI device.

### FAQs Vector Institute
A system on chip consists of both the hardware, described in § Structure, and the software controlling the microcontroller, microprocessor or digital signal processor cores, peripherals and

interfaces. The design flow for an SoC aims to develop this hardware and software at the same time, also known as architectural co-design. The design flow must also take into account optimizations ...

### System on a chip - Wikipedia

First we need to understand what process control is and what are the important technical terms and parameters involve in this process. Process control is a statistics and engineering discipline that

### process control nptel lectures Archives - Instrumentation ...

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