
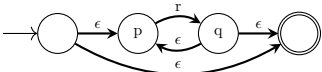

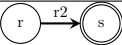
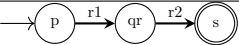
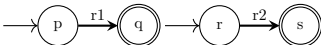
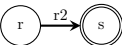
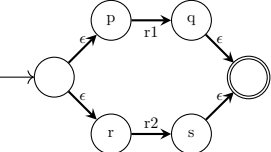


1 Lexer

Definition 1.1 (Context Free Grammar) *Contains a non-terminal start symbol, a set of productions, a set of terminals (tokens) and a set of non-terminals (S,P,t,nt).*

- A **sentence** is a derived string comprising only terminals.
- The **language** is the set of all sentences derived from the start symbol.
- It is **ambiguous** if the language contains strings that can be generated from two different ways

1.1 Regex \rightarrow NFA (Thompson’s Construction)

Regex	Given	Replace
(1) r^*		
(2) $r1\ r2$	 	
(3) $r1\ \ r2$	 	

1.2 NFA \rightarrow DFA (Subset Construction)

DFA start state = ϵ -Closure(NFA start state)
foreach new subset state S of the DFA:
 foreach unique symbol r leading out from any state of S:
 add a transition r from S to S' where S' = ϵ -Closure(states reached by r in 1 step)
Mark subset states accepting if any member state accepting in NFA

2 LR Bottom-Up/Shift-Reduce Parser

2.0.1 FIRST and FOLLOW

Definition 2.1 (FIRST) *FIRST(α) is the set of all terminals that could start the derivation of α*

FIRST(ϵ) = { ϵ }, FIRST(a) = {a}
FIRST(A) = foreach $A \rightarrow \beta_1\beta_2...\beta_n$
 include FIRST(β_1) - { ϵ } in FIRST(A)
 if ϵ in FIRST(β_1): include FIRST(β_2) - { ϵ } in FIRST(A) ...
 ... if ϵ in FIRST(β_n): include ϵ in FIRST(A)

Definition 2.2 (FOLLOW) *FOLLOW(A) is the set of all terminals that follows non-terminal A*

foreach $B \rightarrow CAD$
 include FIRST(D) - { ϵ } in FOLLOW(A)
 if ϵ in FIRST(D): include FOLLOW(B) in FOLLOW(A)
if A ends the input include \$ in FOLLOW(A)

2.1 LR Model

For LR, we add on an auxiliary rule with end-of-input symbol \$. For example, $E' \rightarrow E\ \$$
Parsing table contains all terminals under ACTIONS and non-terminals under GOTO
Push state 0 (start state) onto stack and then repeatedly perform

- **shift sN**: push state n onto stack, advance current token
- **goto gN**: not selected directly (reduce)
- **accept a**: accept input
- **reduce rN**: remove L elements from stack where L = length rhs of rule N
 push Table[stack.top(), LHS of rule N]

2.2 LR(0)

Definition 2.3 (LR(0) items) *are instances of the grammar rules with a • on the rhs of the rule*

2.2.1 LR(0) → NFA

Given a state with item $X \rightarrow A \bullet BC$, add $\boxed{X \rightarrow A \bullet BC} \xrightarrow{B} \boxed{X \rightarrow AB \bullet C}$

And if B is non-terminal, foreach initial item $B \rightarrow \bullet D$, add $\boxed{X \rightarrow A \bullet BC} \xrightarrow{\epsilon} \boxed{B \rightarrow \bullet D}$

2.2.2 DFA → LR(0) Parsing Table

State	Action				Goto		
	id	int	=	\$	E	V	S
0	S2		R3	A		G3	G1

- state X with terminal transition $X \xrightarrow{t} Y$, add $P[X,t] = sY$
- state X with non-terminal transition $X \xrightarrow{N} Y$, add $P[X,N] = gY$
- state containing item $R' \rightarrow \dots \bullet$, add $P[X,\$] = a$
- state containing item $R \rightarrow \dots \bullet$, add $P[X,t] = rN$ for all terminals t where N is R's rule number

2.3 LR(1)

Definition 2.4 (LR(1) items) *is a pair $[LR(0) \text{ item}, \text{look-ahead token } t]$*

2.3.1 LR(1) → NFA

Add initial item $[R' \rightarrow \bullet R, \$]$

Given a state with item $[X \rightarrow A \bullet BC, t]$, add $\boxed{[X \rightarrow A \bullet BC, t]} \xrightarrow{B} \boxed{[X \rightarrow AB \bullet C, t]}$

And if B is non-terminal, foreach rule $B \rightarrow \bullet D$, foreach token u in $FIRST(Ct)$,

add $\boxed{[X \rightarrow A \bullet BC, t]} \xrightarrow{\epsilon} \boxed{[B \rightarrow \bullet D, u]}$

2.3.2 NFA → LR(1) Parsing Table

Like LR(0) but for states containing $[X \rightarrow A \bullet, t]$, only add reduction for column t

2.4 LALR(1)

If any 2 LR(1) states have the same LR(0) items, combines the states

2.5 Conflicts

2.5.1 Shift-Reduce Conflict

$S \rightarrow \text{if } E \text{ then } S \mid \text{if } E \text{ then } S \text{ else } S \mid \text{other}$ *becomes*
 $S \rightarrow MS \mid UMS$
 $MS \rightarrow \text{if } E \text{ then } MS \text{ else } MS \mid \text{other}$
 $UMS \rightarrow \text{if } E \text{ then } S \mid \text{if } E \text{ then } UMS \text{ else } UMS$

2.5.2 Reduce-Reduce Conflict

$\text{Expr} \rightarrow \text{Expr} + \text{Expr} \mid \text{Expr} * \text{Expr} \mid (\text{Expr}) \mid \text{int}$ *becomes*
 $\text{Expr} \rightarrow \text{Expr} + \text{Term} \mid \text{Term}$
 $\text{Term} \rightarrow \text{Term} * \text{Factor} \mid \text{Factor}$
 $\text{Factor} \rightarrow (\text{Expr}) \mid \text{int}$

3 LL Top-Down Parsing

3.1 LL(1)

Definition 3.1 (LL(1) Grammar) *For all rules $A \rightarrow \alpha \mid \beta$, $FIRST(\alpha)$ and $FIRST(\beta)$ are disjoint. If $FIRST(\alpha)$ contains ϵ then $FIRST(\beta)$ and $FOLLOW(\alpha)$ are disjoint. Vice-versa.*

Definition 3.2 (Extended BNF) *contains $\{\alpha\}$ for 0 or more occurrences and $[\alpha]$ for 0 or 1 occurrences*

3.1.1 CFG → LL(1)

- **Left Factorisation** $\underline{A \rightarrow BC \mid BD} \longrightarrow \underline{A \rightarrow B(C \mid D)}$ and $\underline{A \rightarrow BC \mid B} \longrightarrow \underline{A \rightarrow B[C]}$
- **Substitution** replaces a rule with its alternatives to make conflicts direct
- **Left Recursion Removal** $\underline{A \rightarrow X_1 \mid \dots \mid X_n \mid AY_1 \mid \dots \mid AY_n} \longrightarrow \underline{A \rightarrow (X_1 \mid \dots \mid X_n)\{Y_1 \mid \dots \mid Y_n\}}$
more specifically $\underline{A \rightarrow X \mid AY} \longrightarrow \underline{A \rightarrow X\{Y\}}$

3.1.2 LL(1) to Parse Function

- **a B C**: match(A); b = B(); c = C(); return AST(b,c);
- **A|B**: if token in FIRST(A) then {a = A(); return AST1(a)} elif token in FIRST(B) then {return AST2(b); x = B();} else error();
- **{A}**: ls = []; while token in FIRST(A): ls.append(A()); return AST(ls);
- **[A]**: if token in FIRST(A): x = A(); else: x = None; return AST(x);

4 Diagrams

```
class FunctionDeclarationAST:
    String returntypename; String funcname; ParameterASTlist parameters
    FUNCTION funcObj # Semantic attribute

def Check():
    CheckFunctionNameAndReturnType(): # defined on next slide

    ST = new SymbolTable(ST) # create and link new symbol table
    funcObj.symtab = ST

    for P in parameters: # check parameter declarations
        P.check()
        funcObj.formals.append(P.paramObj)

    ST = ST.encSymTable # return to enclosing symbol table

    package A
    class Address { ... }
    int Age;
    double Calc(int Age, string Name, Address Addr) {...}
```

```
class FunctionDeclarationAST:
    String returntypename; String funcname; ParameterASTlist parameters
    FUNCTION funcObj # Semantic attribute

def CheckFunctionNameAndReturnType(): # Similar to variable decl. check
    T = ST.lookupAll(returntypename)
    F = ST.lookup(funcname)
    if T == None: error ("unknown type %s" % returntypename)
    elif ! T instanceof TYPE: error ("%s is not a type" % returntypename)
    elif ! T.isReturnable():
        error ("cannot return %s objects" % returntypename)
    elif ! F == None: error ("%s is already declared" % funcname)
    else
        funcObj = new FUNCTION(T) # link to T and parameter list
        ST.add(funcname, funcObj) # add F to symbol table
```

Variable	Access With
ga	Mem [1000]
gb[k]	Mem [1004 + 4*k]
gc.z	Mem [1052]
gd.q	Mem [Mem[1056] + 8]

Instr.	IA-32 Assembly Code
ga++	inc [1000]
gb[k]++	mov eax, k inc [1004+4*eax]
gc.z++	inc [1052]
gd.q++	mov eax, [1056] inc [eax + 8]

```
class Triple {int p, q, r}
static int ga
static int gb[10]
static struct {int x, y, z} gc
static Triple gd = new Triple()
```


Static Area	Heap Area
1000 ga	@MLT +0
1004 gb[0]	.p +4
...	.q +8
1040 gb[9]	.r +12
1044 gc.x	
1048 gc.y	
1052 gc.z	
1056 gd	

```
class Triple {int p, q, r}
class Alpha {
    int ca
    int cb [10]
    method M(int u,v) {
        int la
        int lb[10]
        struct {int x, y, z} lc
        Triple ld = new Triple()
    }
}
Alpha object = new Alpha()
object.M(2,3)
```


Var	Access With
v	Mem [FP + 16]
lb[k]	Mem [FP - 56 + 4*k]
ld.q	Mem [Mem[FP-4] + 8]
ca	Mem [Mem[FP+8] + 4]
cb[k]	Mem [Mem[FP+8]+8+4*k]

FP Register	Stack	Heap Area
9060		
9076(+16)	v=3	Alpha @MLT +0
9072(+12)	u=2	ca +4
9068(+8)	object addr	cb[0] +8
9064(+4)	return addr	...
9060(+0)	caller's FP	cb[9] +44
9056(-4)	ld	
9052(-8)	lc.z	
9048(-12)	lc.y	Triple @MLT +0
9044(-16)	lc.x	.p +4
9040	lb[9]	.q +8
...r +12
9004(-56)	lb[0]	
9000(-60)	la	

5 Garbage Collection

Heap Compaction: Mark live blocks → Relocate live blocks → Update pointers to relocated blocks

- **Reference-Counting**: Requires special techniques for cyclic data structures
- **Mark-Sweep**: **Mark**: Mark all blocks reachable from non-heap references as live
Sweep: Scan all blocks to reclaim dead blocks, unmark live blocks for next sweep
Use pointer reversal technique to visit all nodes of a Directed Graph without additional stack space
Provides the largest possible block available when combined with compaction

- **Two-Space:** Allocate blocks to **From-Space**. When it's exhausted, copy live blocks to **To-Space**. Automatic compaction, very fast to allocate objects, wastes half of memory, relocate long-lived objects
- **Generational:** Heap divided areas based on block age. Perform GC on younger generations more

6 Register Allocation

```
transExp (Binop Minus e1 e2) r
```

6.1 Register Machine Strategy

```
transExp e1 r ++
transExp e2 (r+1) ++
[Sub r (r+1)]
```

6.3 Sethi-Ullman Weights

```
if weight e2 > weight e1 then
  transExp (nxtreg:dstreg:regs) ++
  transExp (dstreg:regs) ++
  [Sub dstreg nxtreg]
```

6.4 Graph Colouring

1. Generate intermediate three-address code where values are always saved in named locations
2. Construct inference graph where nodes are locations and nodes are linked if their live ranges overlap
3. Try to colour the nodes so no connected nodes have the same colour

7 Optimisation

Definition 7.1 (Live Variables (Contains Temporaries - Backward))

$$LiveOut(n) = \bigcup_{s \in succ(n)} LiveIn(s)$$

$$LiveIn(n) = uses(n) \cup (LiveOut(n) - defs(n))$$

6.2 Accumulator Strategy

```
transExp e2 r ++
[Push r] ++
transExp e1 r ++
[SubStack r]
```

```
weight (Binop Minus e1 e2) = min [
  max [weight e1, (weight e2) + 1],
  max [weight e2, (weight e1) + 1]
]
```

```
foreach n in CFG:
  LiveIn(n) := {}; LiveOut(n) := {}
repeat: foreach n in CFG:
  LiveIn(n) = uses(n) + (LiveOut(n) - defs(n))
  LiveOut(n) = union [LiveIn(s) | s <- succ n]
until LiveIn and LiveOut stop changing
```

Definition 7.2 (Reaching Definitions (Contains Nodes - Forward)) *Can be applied to Points-To analysis*

$$ReachIn(n) = \bigcup_{s \in pred(n)} ReachOut(s)$$

$$ReachOut(n) = Gen(n) \cup (ReachIn(n) - Kill(n))$$

For $n : t = u1 \oplus u2$ ($defs(n) = \{t\}$, $uses(n) = \{u1, u2\}$)

- $Gen(n)$ is definitions generated by n , $\{n\}$
- $Kill(n)$ is all definitions of t except n

Definition 7.3 (Dominator (Contains Nodes - Forward)) *A node d dominates node n if every path from the start node to n must go through d . Every node dominates itself.*

$$Doms(s) = \{s\}, \text{ for start node } s \quad Doms(n) = \{n\} \cup \left(\bigcap_{p \in preds(n)} Doms(p) \right), \text{ otherwise}$$

Definition 7.4 (Loop) *A set of nodes S including a header node h such that*

- Any node in S has a path leading to h
- There is a path from h to any node in S
- There is no edge from any node outside S to any node in S other than h

Definition 7.6 (Loop Invariants) *For hoisting a node d : $t = a \oplus b$,*

- **Reaching Definitions:** All reaching defs used by d occur outside loop
- **Dominators:** d dominates all loop exits
- **Count:** There must only be one def of t in the loop
- **Live Variables:** t must not be LiveOut from the loop's preheader

Definition 7.5 (Back Edge) *An edge from node n to node h that dominates n is called a back edge. For every back edge, there is a loop. Two loops can share the same header.*

Definition 7.7 (Single State Assignment) *Introduces a new name each time a variable is assigned.*

*At control-flow joins, insert a dummy operator $t = \phi(t_1, t_2)$ which magically picks either value depending on what path is taken. In the generated code, we push the assignments of t backwards into the two predecessor paths. To hoist a node after SSA conversion, only need to check **Reaching definitions**.*