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1 /*
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4  動かし方:スイッチ操作でスタートからゴールまで行く
5 */
6
7 #include <avr/io.h>
8 #include <avr/interrupt.h>
9 #include <avr/wdt.h>
10
11 #define CTOP 10000UL;
12 #define BZ_CTOP 1000UL
13 #define BZ_CTOP2 500UL
14 #define BZ_CTOP3 100UL
15
16
17 volatile unsigned char map[8] =
18 {
19     0b10111111,
20     // 0b00000000,
21     0b10011111,
22     0b11000000,
23     0b11111110,
24     0b10000000,
25     0b10111111,
26     0b10000000,
27     0b11111110
28 };
29 volatile unsigned char stat;
30 volatile unsigned char sw;
31 volatile unsigned char sw_flag;
32 volatile unsigned char mv_flag;
33
34 static unsigned char my_state = 0;
35 static unsigned char scan = 0;
36 unsigned char x = 0x40;
37 unsigned char smog_b = 0xE0;
38
39 void update_led();
40
41 ISR(PCINT1_vect)
42 {
43     stat = 1;
44 }
45
46 void update_sw()
47 {
48     static unsigned long cnt;
49     switch (stat) {
50     case 0:
51         return;
52     case 1:
53         cnt = CTOP;
54         stat = 2;
55         return;
56     case 2:
57         cnt--;
58         if (cnt == 0) {
59             sw = ~(PINC >> 4) & 3;
60             sw_flag = 1;
61             stat = 0;
62         }
63         return;
64     }
65 }
66
67 void proc_bz1()

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68 {
69     static unsigned long cnt = 0;
70     cnt++;
71     if(cnt < BZ_CTOP){
72         return;
73     }
74     cnt = 0;
75     PORTD ^= 0x08;
76 }
77
78 void proc_bz2()
79 {
80     static unsigned long cnt2 = 0;
81     cnt2++;
82     if(cnt2 < BZ_CTOP2){
83         return;
84     }
85     cnt2 = 0;
86     PORTD ^= 0x08;
87 }
88
89 void proc_bz3()
90 {
91     static unsigned long cnt3 = 0;
92     cnt3++;
93     if(cnt3 < BZ_CTOP3){
94         return;
95     }
96     cnt3 = 0;
97     PORTD ^= 0x08;
98 }
99
100 ISR(TIMER0_COMPA_vect)
101 {
102     static int cnt;
103     cnt++;
104     if (cnt == 100) {
105         cnt = 0;
106         mv_flag = 1;
107     }
108     update_led();
109 }
110 void update_led()
111 {
112     static unsigned char sc = 0xFE;
113
114     PORTB = 0;
115     sc = (sc << 1) | (sc >> 7);
116     PORTD = (PORTD & 0x0F) | (sc & 0xF0);
117     PORTC = (PORTC & 0xF0) | (sc & 0x0F);
118     scan = (scan + 1) & 7;
119     // PORTB = map[scan];
120     if(my_state != 0) {
121         // if(scan == my_state || scan == my_state + 1 || scan == my_state - 1)
122
123         if(scan == my_state){
124             //PORTB = map[scan];
125             PORTB = map[scan] & smog_b;
126         }else if(scan == (my_state + 1)){
127             PORTB = map[scan] & smog_b;
128         }else if(scan == (my_state - 1)){
129             PORTB = map[scan] & smog_b;
130         }
131
132     }else{
133         if(scan == my_state){
134             //PORTB = map[scan];
135             PORTB = map[scan] & smog_b;

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136         }else if(scan == (my_state + 1)){
137     PORTB = map[scan] & smog_b;
138     }
139     }
140
141 }
142
143 int main()
144 {
145 //   unsigned char n;
146 //   unsigned char n2;
147
148     DDRB = 0xFF;
149     DDRC = 0x0F;
150     DDRD = 0xFE;
151
152     PORTB = 0x00;
153     PORTC = 0x30;
154     PORTD = 0x00;
155     PCICR = _BV(PCIE1);
156     PCMSK1 = 0x30;
157
158     TCNT0 = 0;
159     OCR0A = 249;
160     TCCR0A = 2;
161     TCCR0B = 3;
162     TIMSK0 |= _BV(OCIE0A);
163
164     sei();
165     for (;;) {
166         wdt_reset();
167         update_sw();
168         if(scan == my_state){
169             PORTB |= x;
170         }
171
172         if (sw_flag) {
173             sw_flag = 0;
174
175 /*             if(scan == my_state){
176                 if(my_state < 4){
177                     //PORTC =
178                 }else{
179                     //PORTD =
180                 }
181             }
182
183 */
184             switch (sw) {
185             case 0:
186                 break;
187             case 1:
188                 x = (x >> 7) | (x << 1);
189                 smog_b = (smog_b >> 7) | (smog_b << 1);
190                 proc_bz1();
191                 break;
192             case 2:
193                 proc_bz2();
194                 x = (x << 7) | (x >> 1);
195                 smog_b = (smog_b << 7) | (smog_b >> 1);
196                 break;
197             case 3:
198                 proc_bz3();
199                 my_state++;
200                 break;
201             }
202
203

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204     }
205     if (mv_flag == 1) {
206         mv_flag = 0;
207     }
208 }
209 return 0;
210 }
211

```