

## Project: EC2.201-VLSI Design- Monsoon 22

Design a 4x4 Multiplier to estimate leakage current/power for all input combinations and propagation delays for all possible arc including critical path. Design the layout of your multiplier, clearly indicate the location of each standard cell in the design. Compare your pre- and post-layout results. Also, verify the functionality of your multiplier in HDL. Below figure shown for reference.

**Following EDA tools can be used:**

- NG-SPIICE for circuit design
- Magic for layout
- Xilinx, other for HDL design

