

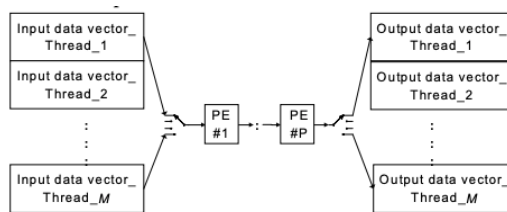
Simulating Systolic Arrays

Goal: Visualize the operation [cycle accurate] of Systolic Array Operations for implementations of

- Sorting
- Matrix Vector Multiplication
- 1D Convolution
- Integer Multiplication
- Solving Equations
 - Gaussian Elimination
 - Inverse
- Shortest Path

Input size of the array is to be taken as parameter.

Method: Write a multithreaded program to simulate the systolic architectures mentioned above.



Deliverable: Program which does the Visualization along with a pdf document that details how the program works and the snapshots of the Visualization.