

VIDYALANKAR INSTITUTE OF TECHNOLOGY

Vidyalankar Marg, Wadala (E), Mumbai 400 037
(Affiliated to University of Mumbai)

GRADE CARD

Name : MISHRA TANMAY RISHIKESH SUNITA

Seat No. : 236105

Examination : Second Year COMPUTER ENGINEERING (Semester 3) of
syllabus Revised (2016) held in November-2017

Scheme Code	Course Title	Head/s of Passing	Course Credits	Grade Obtained	Credits Earned (C)	Grade Point (G)	C x G
CSC301	APPLIED MATHEMATICS III	ESE+IA	05	O	05	10	50
CSC302	DIGITAL LOGIC DESIGN AND ANALYSIS	ESE+IA	04	O	04	10	40
CSC303	DISCRETE MATHEMATICS	ESE+IA	04	O	04	10	40
CSC304	ELECTRONIC CIRCUITS AND COMMUNICATION FUNDAMENTALS	ESE+IA	04	O	04	10	40
CSC305	DATA STRUCTURES	ESE+IA	04	O	04	10	40
CSL301	DIGITAL SYSTEM LAB	ORAL+TW	01	O	01	10	10
CSL302	BASIC ELECTRONICS LAB	ORAL+TW	01	O	01	10	10
CSL303	DATA STRUCTURE LAB	ORAL+TW	01	O	01	10	10
CSL304	OOPM(JAVA) LAB	ORAL+TW	02	O	02	10	20
Total			26	-	26	90	260

Result : PASSES

SGPI : 10

Principal

Issued On : April 03, 2018

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