VIT Vidyalankar Institute of Technology

VIDYALANKAR INSTITUTE OF TECHNOLOGY

Vidyalankar Marg, Wadala (E), Mumbai 400 037
(Affiliated to University of Mumbai)

GRADE CARD

Seat No. : 246104

Name : MISHRA TANMAY RISHIKESH SUNITA

Examination : Second Year COMPUTER ENGINEERING (Semester 4) of syllabus Revised (2016) held in May-2018

Scheme Code	Course Title	Head/s of Passing	Course Credits	Grade Obtained	Credits Earned (C)	Grade Point (G)	C x G
CSC401	APPLIED MATHEMATICS IV	ESE+IA	05	O	05	10	50
CSC402	ANALYSIS OF ALGORITHMS	ESE+IA	04	O	04	10	40
CSC403	COMPUTER ORGANIZATION AND ARCHITECTURE	ESE+IA	04	O	04	10	40
CSC404	COMPUTER GRAPHICS	ESE+IA	04	O	04	10	40
CSC405	OPERATING SYSTEM	ESE+IA	04	O	04	10	40
CSL401	ANALYSIS OF ALGORITHMS LAB	ORAL+TW	01	O	01	10	10
CSL402	COMPUTER GRAPHICS LAB	ORAL+TW	01	C	01	07	07
CSL403	PROCESSOR ARCHITECTURE LAB	ORAL+TW	01	O	01	10	10
CSL404	OPERATING SYSTEM LAB	ORAL+TW	01	O	01	10	10
CSL405	OPEN SOURCE TECH LAB	ORAL+TW	02	O	02	10	20
	Total		27	-	27	97	267

Result : PASSES

SGPI : 9.89

Principal

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