

VIDYALANKAR INSTITUTE OF TECHNOLOGY

Vidyalankar Marg, Wadala (E), Mumbai 400 037
(Affiliated to University of Mumbai)

GRADE CARD

Name : MISHRA TANMAY RISHIKESH SUNITA Seat No. : 256149
Examination : Third Year COMPUTER ENGINEERING (Semester 5) of
syllabus Revised (2016) held in December-2018

Scheme Code	Course Title	Head/s of Passing	Course Credits	Grade Obtained	Credits Earned (C)	Grade Point (G)	C x G
CSC501	MICROPROCESSOR	ESE+IA	04	O	04	10	40
CSC502	DATABASE MANAGEMENT SYSTEM	ESE+IA	04	O	04	10	40
CSC503	COMPUTER NETWORK	ESE+IA	04	O	04	10	40
CSC504	THEORY OF COMPUTER SCIENCE	ESE+IA	04	O	04	10	40
CSDLO5013	ADVANCE ALGORITHM	ESE+IA	04	O	04	10	40
CSL501	MICROPROCESSOR LAB	PRACTS+TW	01	O	01	10	10
CSL502	COMPUTER NETWORK LAB	PRACTS+TW	01	O	01	10	10
CSL503	DATABASE & INFO. SYSTEM LAB	PRACTS+TW	01	O	01	10	10
CSL504	WEB DESIGN LAB	PRACTS+TW	02	O	02	10	20
CSL505	BUSINESS COMM. & ETHICS	TW	02	O	02	10	20
Total			27	-	27	100	270

Result : PASSES

SGPI : 10

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Principal

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