```
module update_frog (clk, new_game, reset, gameover, player_input, frog_x, frog_y);
         input logic clk, new_game, reset, gameover;
input logic [3:0] player_input;
output logic [3:0] frog_x, frog_y; // 4 bits bc we need range of 0-16 for each coord
 3
 4
5
6
7
          always_ff @(posedge clk) begin
 8
              if (reset || new_game) begin
                 frog_y <= 8;
10
                 frog_x \ll 0;
11
              end
12
             else if (gameover == 0) begin
13
                 if (player_input[3]) // move up
                 if (frog_y < 15)
    frog_y <= frog_y + 1;
if (player_input[2]) // move down</pre>
14
15
16
                 if (frog_y > 0)
    frog_y <= frog_y - 1;
if (player_input[1]) // move left</pre>
17
18
19
20
                         (frog_x > 0)
21
                         frog_x <= frog_x - 1;
22
                 if (player_input[0]) // move right
23
                     if (frog_x < 15)
24
                         frog_x <= frog_x + 1;
25
             end
26
27
             if (frog_x == 15)
                 frog_x \ll 0;
28
29
30
          end
31
32
      endmodule
33
34
      module update_frog_testbench();
35
          logic clk, new_game, reset, gameover;
logic [3:0] player_input;
36
37
          logic [3:0] frog_x, frog_y;
38
39
          update_frog dut(clk, new_game, reset, gameover, player_input, frog_x, frog_y);
40
41
          // Set up the clock.
42
          parameter CLOCK_PERIOD=100;
43
          initial begin
          c1k \ll 0;
45
          forever #(CLOCK_PERIOD/2) clk <= ~clk;</pre>
46
47
48
          initial begin
49
                                                    @(posedge clk);
50
              reset \leftarrow 1;
                                                    @(posedge clk);
             reset \leftarrow 0;
51
                                                   @(posedge clk);
             player_input <= 0;</pre>
52
                                                   @(posedge clk);
53
             gameover \leftarrow 0;
                                                   @(posedge clk);
54
              7/ pass through some basic player inputs, during game and
      gameover
                                                   @(posedge clk);
56
                                                   @(posedge clk);
57
              player_input[0] <= 1;
                                                   @(posedge clk);
             player_input[0] <= 0;</pre>
                                                   @(posedge clk);
59
                                                   @(posedge clk);
             player_input[0] <= 1;
60
             player_input[0] <= 0;</pre>
                                                   @(posedge clk)
61
                                                   @(posedge clk);
62
                                                   @(posedge clk)
63
                                                   @(posedge c]k)
             player_input[1] <= 1;</pre>
             player_input[1] <= 0;
player_input[1] <= 1;</pre>
64
                                                   @(posedge clk);
65
                                                   @(posedge clk);
             player_input [1] <= 0;
                                                   @(posedge clk);
66
67
                                                   @(posedge clk):
68
                                                   @(posedge clk);
69
70
71
72
                                                   @(posedge clk)
             player_input[2] <= 1;</pre>
             player_input[2] <= 0;
                                                   @(posedge clk):
             player_input[2] <= 1;</pre>
                                                   @(posedge clk);
             player_input[2] <= 0;</pre>
                                                   @(posedge clk);
73
                                                   @(posedge c]k);
                                                   @(posedge clk);
             player_input[3] <= 1;</pre>
                                                   @(posedge clk);
```

114

115

endmodule

Project: DE1_SoC