Tanvi Jivtode

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Driven and goal-oriented Computer Engineering student seeking full-time opportunities in the embedded systems space.

EDUCATION

University of Michigan (Computer Engineering) Bachelor of Science in Engineering, College of Engineering; Cumulative GPA: 3.60	Ann Arbor, MI Expected 2022
Skills Extensive knowledge of C, C++, MATLAB Familiar with Javascript, HTML, Python, Bash, Flask, ARM, Angular, Simulink, Verilog (FPGA) Platforms and Applications: Windows 10, Linux, Git, EAGLE, OpenSCAD, Arduino	Enpoceed 2022
Curriculum Highlights Adv. Embedded Systems (EECS 473), Embedded Control Systems (EECS 461), Signals and Systems (EECS 216), Intr. Embedded Systems (EECS 373), Intr. Circuits (EECS 215), Data Structures and Algorithms (EECS 281), Logic Design (EECS 270), Computer Architecture (EECS 370)	
Organizations Society of Women Engineers (SWE), Women in Electrical/Computer Engineering (WECE), Michigan Sahana	
Fred J. Page High School <i>Honors Diploma, Scholars Diploma, Salutatorian for Class of 2018;</i> Cumulative GPA: 4.00	Franklin, TN 2014 – 2018
EXPERIENCE	
 Qualcomm Interim Engineering Intern, Modem SW Team Tasked with porting GDB (GNU Project Debugger) to a proprietary external target, Integer Unit (IU) as part of a modem subsystem Developed an 'IU Manager' in C to handle GDB requests on the target side Implemented a Remote Serial Protocol server in Python to interact with GDB on the PC side 	San Diego, CA May 2021- Aug 2021
 Mcity - Connected and Automated Vehicle Test Facility Software Engineering Intern Produced two wireless trigger devices, a button-based and LiDAR-based trigger (from printed circuit board design in EAGLE and firmware in C++ to server connection in Flask) used to set off facility events Created IoT devices that add user-friendly and environmental elements to computerized tests to help improve testing of autonomous/connected vehicles 	Ann Arbor, MI May 2020- April 2021
 Michigan Electric Racing Electric Vehicle Powertrain Team Member Developed a schematic and printed circuit board for high voltage circuits, particularly a shutdown circuit (safety loop) for the powertrain Prototyping PCBs in Altium and building circuit boards to be used in the powertrain 	Ann Arbor, MI Sept 2019 - Dec 2020
DaVita Inc. Application Development Intern RESEARCH	Brentwood, TN June 2019 – Aug 2019

UofM Multidisciplinary Program

Researcher, Mapleseed: Sensor Network Laboratory

Jan 2020 -Embedded programming of a set of CC1310 development boards to employ their wireless Present

Ann Arbor, MI

communication capabilities

Member of the Mapleseed research project under Professor Xiaogan Liang