Project 3 - Digital Multimeter

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Device Behavior Description

This project designs the MSP432P401R microcontroller in conjunction with a 14-bit analog-to-digital converter (ADC) to act as a digital multimeter. The digital multimeter is capable of measuring DC voltages from 0V to 3.3V as well as AC measurements including peak-to-peak voltage, true RMS voltage, and frequencies from 2Hz to 1kHz for any AC wave. The digital multimeter measurements are readable via a computer terminal.

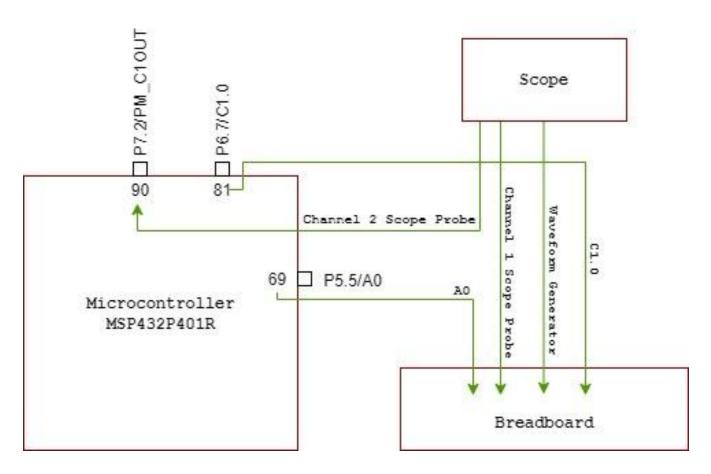
System Specification

Table 1: Digital Multimeter Parameters and Respective Values [1], [2]

System Parameter	Value
ADC Bit Resolution	14-Bit
Measurable Range of Frequencies	2Hz - 1kHz
Measurable Range of Voltages	0V - 3.3V
Clock Frequency	24MHz
Baud Rate	115200

System Schematic

Figure 1: Schematic of Digital Multimeter Hardware Configuration



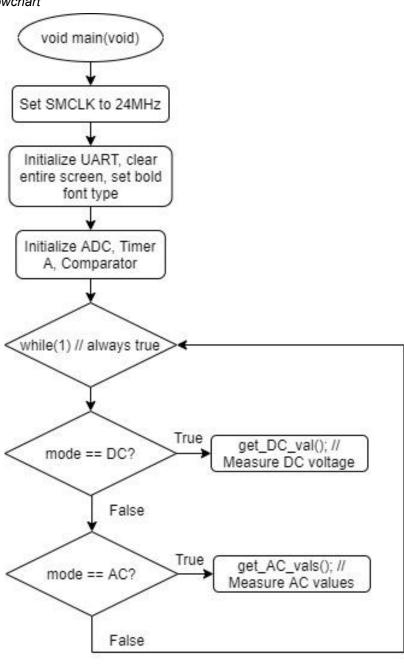
^{*}Waveform Generator, Channel 1 Scope Probe, A0, and C1.0 are all connected to the same breadboard terminal

Software Architecture

The digital multimeter is controlled by a while loop which checks for keyboard presses of either 'A' or 'D'. If 'A' is pressed, the digital multimeter will display AC measurements (frequency, peak-to-peak voltage, and true RMS voltage) whereas, if 'D' is pressed, the digital multimeter will display DC measurements (DC voltage).

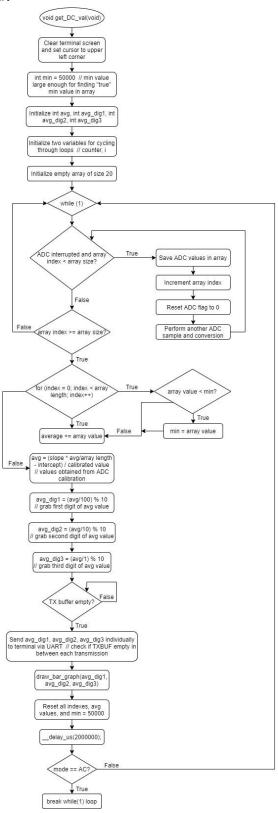
Main Diagram

Figure 2: main flowchart



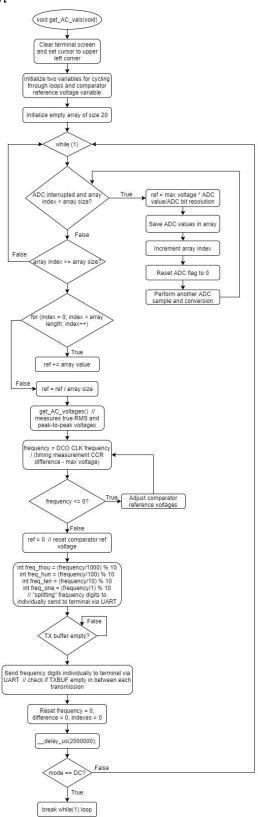
DC Voltage Diagram

Figure 3: get_DC_val flowchart



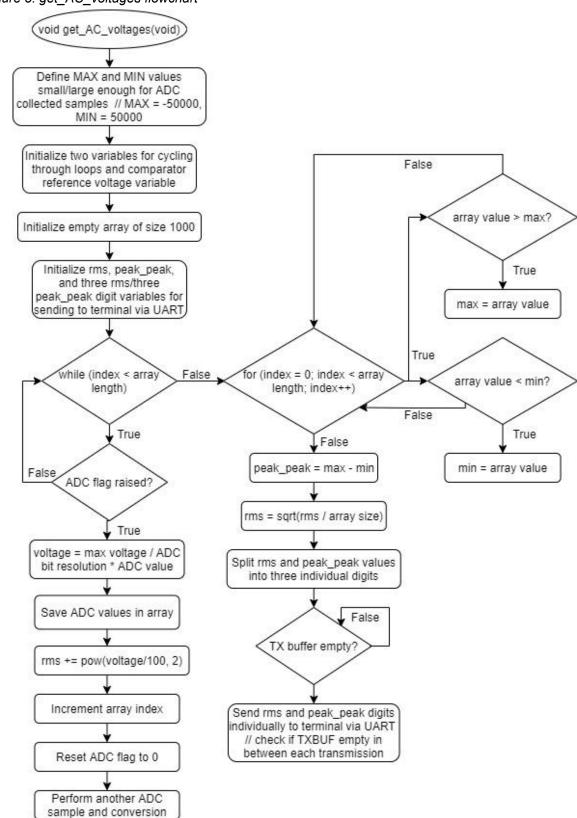
AC Values Diagram

Figure 4: get_AC_vals flowchart



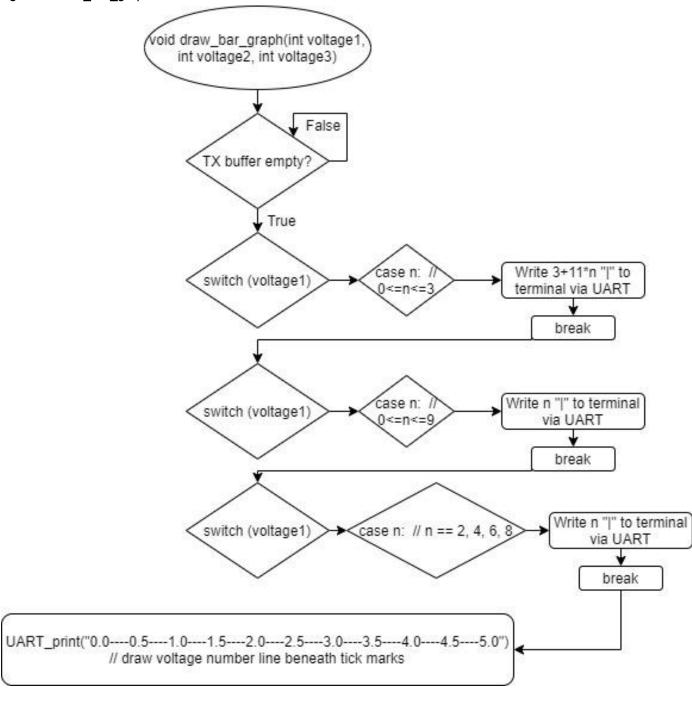
AC Voltages Diagram

Figure 5: get_AC_voltages flowchart



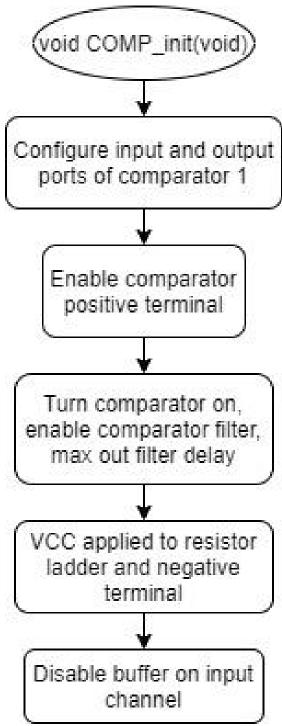
Bar Graph Diagram

Figure 6: draw_bar_graph flowchart



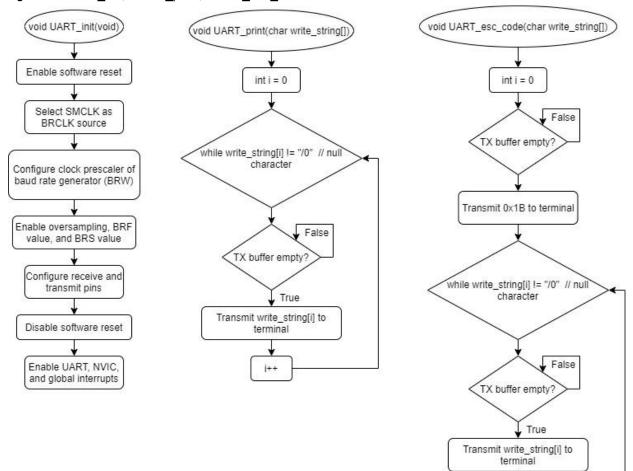
Comparator Diagram

Figure 7: COMP_init flowchart



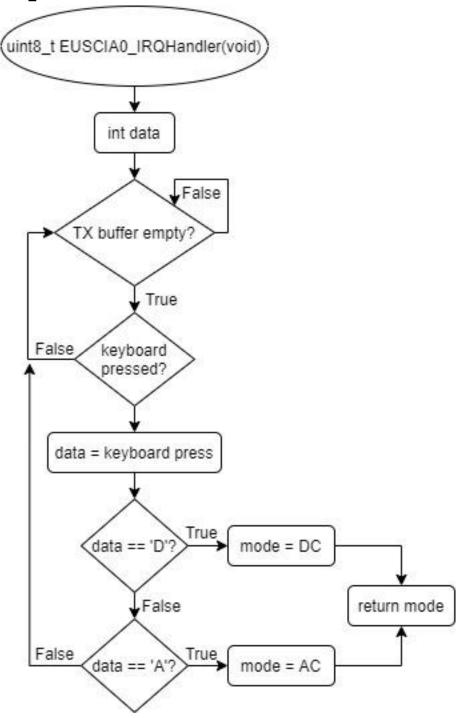
UART Diagrams

Figure 8: UART_init, UART_print, UART_esc_code flowcharts



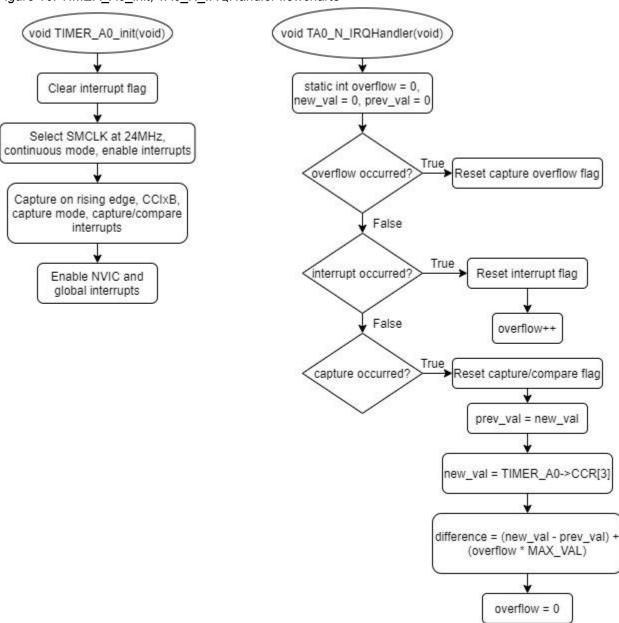
EUSCI ISR Diagram

Figure 9: EUSCIA0_IRQHandler flowchart



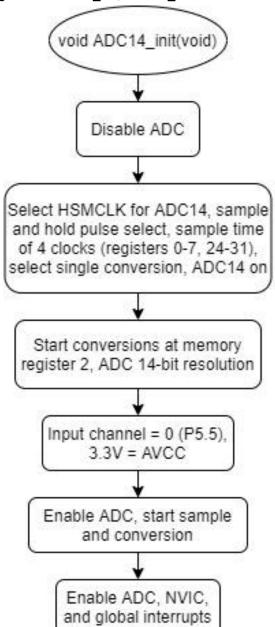
Timer A Diagrams

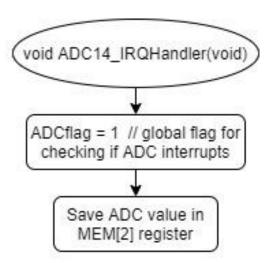
Figure 10: TIMER_A0_init, TA0_N_IRQHandler flowcharts



ADC Diagrams

Figure 11: ADC14_init, ADC14_IRQHandler flowcharts





Sample DC Measurement Terminal Display

Figure 12: DC Measurement of 1V DC Offset

Sample AC Measurements Terminal Display

Figure 13: AC Measurements of 500Hz, 2V_{peak-to-peak}, 1V DC Offset Sine Wave

```
AC Measurements

Peak-to-Peak Voltage: 2.02 V

RMS Voltage: 1.25 V

!!!!!!!!!!!!!!!

0.0----0.5----1.0----1.5----2.0----2.5----3.0----3.5----4.0----4.5----5.0

Frequency: 0501 Hz
```

Appendices

main.c

```
______
#include "msp.h"
#include "DCO.h"
#include "UART.h"
#include "ADC.h"
#include "math.h"
#define CPU FREQ 3000000
                         // Microcontroller frequency
#define SCLK FREQ 24000000
#define WRITE DELAY 2000000
#define __delay_us(t_us) (__delay_cycles((((uint64_t)t_us)*CPU_FREQ) / 1000000))
#define DC 1
              // DC or AC depending on measurements to be taken
#define AC 2
#define C1 IN PORT P6 // Initializing comparator ports
#define C1 IN BIT7  // Input port: P6.7
#define C1 OUT PORT P7 // Output port: P7.2
#define C1 OUT BIT2
#define MAX VAL 0xFFFF
#define MAX VOLTAGE 33000
#define ADC RES 16384 // Max resolution of ADC 14-bit: 2^14 = 16384
#define REF CONV 31
#define FREQ CONV 300
#define ANALOG ARRAY LENGTH 1000
int val; // Global for saving MEM[2] values
{\tt uint8\_t} ADCflag = 0; // Global flag for checking when to populate ADC array
uint8_t mode; // Mode controlled by user (keyboard press) for deciding whether to take AC or DC measurements
int difference = 0; // Global for calculating difference between CCR Timer A measurements
void COMP init(void); // Initializing functions
void TIMER A0 init(void);
void draw bar graph(int voltage1, int voltage2, int voltage3);
void TA0 N IRQHandler(void);
void ADC14 IRQHandler(void);
uint8 t EUSCIAO IRQHandler(void);
void get DC val(void);
void get AC vals(void);
void get AC voltages(void);
void main(void)
   WDT A->CTL = WDT A CTL PW | WDT A CTL HOLD; // stop watchdog timer
   set_DCO(FREQ_24_MHz);
   UART init();  // Initialize UART
   UART_esc_code(CLEARSCREEN); // Clear terminal screen
   UART esc code(BOLD); // All terminal text is bold print for ease of reading
   ADC14 init(); // Initialize ADC
   TIMER A0 init(); // Initialize Timer A
   COMP init(); // Initialize Comparator E1
   while (1)
```

```
if (mode == DC) // If key 'D' pressed on keyboard, take DC measurements
            UART esc code(TOPLEFT); // Moves cursor to the top left of the screen, essentially "rewriting" screen
            get DC val(); // Calculates DC voltage value
        else if (mode == AC) // If key 'A' pressed on keyboard, take AC measurements
           UART_esc_code(TOPLEFT);
           get AC vals(); // Calculates AC true RMS voltage, peak-to-peak voltage, and frequency
    }
}
void get DC val(void)
    UART print (CLEARSCREEN);
    UART print(TOPLEFT);
    int min = DEF MIN;
    int avg = 0;
    int avg dig1, avg dig2, avg dig3; // Variables for individually sending average DC voltage value to terminal via
UART
    uint8 t counter, i = 0; // Variables for cycling through loops
    int digital conversion[ARRAY LENGTH] = EMPTY ARRAY; // Initialize empty array for saving ADC values
    while(1) // Infinite loop to check for ADC global flag
        if (ADCflag == 1 && i < ARRAY LENGTH) // ADC interrupt triggered
           digital conversion[i] = val;  // Save ADC value in array
           i++; // Increment array index
            ADCflag = 0; // Reset ADC interrupt flag
            ADC14->CTL0 \mid= (ADC14 CTL0 SC); // Perform another sample and conversion
        else if (i >= ARRAY LENGTH) // Array is filled with samples
            for (counter = 0; counter < ARRAY LENGTH; counter++)</pre>
                if (digital conversion[counter] < min) // Find min value in array
                   min = digital conversion[counter];
               avg += digital_conversion[counter]; // Add all values in array to calculate average
            }
            avg = (SLOPE * (avg/ARRAY LENGTH) - INTERCEPT) / CAL; // Calculate calibrated average of array
            min = (SLOPE * min - INTERCEPT) / CAL; // Calculate calibrated min of array
            if (min <= 0) // To avoid negative integers
                avg = ZERO;
            else if (min > 0 && min < = 69) // Add 3 for OV to 69mV, calibration
            else if (min \geq 70 && min \leq 169) // Add 2 for 70mV to 169mV, calibration
                avg += PLUS2;
```

```
avg = MAX;
                                           // "Splitting" average voltage into three individual values...
            avg dig1 = (avg / 100) % 10;
            avg dig2 = (avg / 10) % 10;
                                            // ...to send to terminal via UART
            avg dig3 = (avg / 1) % 10;
            while(!(EUSCI_A0->IFG & EUSCI_A_IFG_TXIFG));
                                                         // Wait for TXBUF to be empty in order to transmit...
                                                           // ... to terminal via UART
                                   // Moves cursor to the left of the terminal to keep text level
            UART esc code(LEFT);
            UART_esc_code(GREEN);  // Header title is green
            UART print("DC Voltage: ");
            UART esc code (WHITE); // Change font color to white
            while(!(EUSCI A0->IFG & EUSCI A IFG TXIFG));
            EUSCI_A0->TXBUF = avg_dig1 + '0';  // Print digits one by one
            UART print(".");
            while(!(EUSCI A0->IFG & EUSCI A IFG TXIFG));
            EUSCI_A0->TXBUF = avg_dig2 + '0';
            while (! (EUSCI A0->IFG & EUSCI A IFG TXIFG));
            EUSCI A0->TXBUF = avg dig3 + '0';
            while (! (EUSCI A0->IFG & EUSCI A IFG TXIFG));
            UART print(" V");
            UART esc code(LEFT);
            UART esc code (NEWLINE); // Skips a line on terminal
            UART esc code (NEWLINE);
            draw_bar_graph(avg_dig1, avg_dig2, avg_dig3); // Function for drawing voltage bar graph
            i = 0; // Reset array index
            min = DEF MIN;
            avg = 0;
            avg dig1 = 0;
            avg dig2 = 0;
           avg dig3 = 0;
            ADCflag = 1;
                           // Raise interrupt flag
            delay us(WRITE DELAY); // Delay for allowing enough time for terminal to display all values
            UART esc code (CLEARSCREEN);
            UART esc code (TOPLEFT);
        if (mode == AC) // If 'A' press detected, switch to AC measurements mode
}
void get_AC_vals(void)
   UART_print(CLEARSCREEN);
   UART print(TOPLEFT);
   uint8 t i = 0, counter = 0; // Variables for cycling through loops
   int analog_conversion[ARRAY_LENGTH] = EMPTY_ARRAY; // Initialize empty array for saving MEM[2] values
   int REF = 0, frequency = 0, freq thou = 0, freq hun = 0, freq ten = 0, freq one = 0; // REF = reference voltage
value
   while (1)
        if (ADCflag == 1 && i < ARRAY LENGTH)
            REF = REF CONV * val/ADC RES; // Converts ADC value to equivalent AC voltage
            analog conversion[i] = REF; // Save REF value in array
            i++; // Increment array index
```

else if (min > 325) // Max voltage = 3.3V

```
ADCflag = 0; // Reset interrupt flag
           ADC14->CTL0 |= (ADC14 CTL0 SC); // Perform another sample and conversion
       else if (i >= ARRAY LENGTH) // Array is filled with samples
           for (counter = 0; counter < ARRAY_LENGTH; counter++) // Add all REF values in array
              REF += analog_conversion[counter];
           REF = REF/ARRAY LENGTH; // Divide all REF values by length of array to find average ref voltage value
           get AC voltages(); // Calculates true RMS and peak-to-peak AC voltages
           frequency = SCLK FREQ/(difference - FREQ CONV); // Converts ADC reading to equivalent AC frequency
using...
                                                    // ... difference between timing measurements captured by Timer
           if (frequency \le 0) // Different reference voltage value implementations to test if frequency \le 0
               COMP E1->CTL2 |= ((REF+1) << COMP E CTL2 REF0 OFS
                           | (REF) << COMP E CTL2 REF1 OFS);
           frequency = SCLK_FREQ/(difference - FREQ_CONV); // Check frequency in between each ref voltage
implementation
           if (frequency <= 0)
               COMP E1->CTL2 |= ((REF-1) << COMP E CTL2 REF0 OFS
                           (REF-1) << COMP E CTL2 REF1 OFS);
           frequency = SCLK FREQ/(difference - FREQ CONV);
           if (frequency <= 0)
               COMP E1->CTL2 |= ((REF+1) << COMP E CTL2 REF0 OFS
                          (REF+1) << COMP E CTL2 REF1 OFS);
           frequency = SCLK FREQ/(difference - FREQ CONV);
           if (frequency <= 0)
           {
              COMP E1->CTL2 |= ((REF) << COMP E CTL2 REF0 OFS
                          (REF-1) << COMP_E_CTL2_REF1_OFS);</pre>
           frequency = SCLK FREQ/(difference - FREQ CONV);
           if (frequency <= 0)
           {
               COMP E1->CTL2 |= ((REF+1) << COMP E CTL2 REF0 OFS
                           (REF-1) << COMP E CTL2 REF1 OFS);
           REF = 0;  // Reset reference voltage value
           if (frequency < 0) // To avoid negative frequencies
```

```
frequency = 0;
           freq thou = (frequency/1000) % 10; // "Splitting" frequency into four individual values to send to...
           freq hun = (frequency/100) % 10;
                                             // ... terminal via UART
           freq_ten = (frequency/10) % 10;
           freq_one = (frequency/1) % 10;
           while(!(EUSCI_AO -> IFG & EUSCI_A_IFG_TXIFG)); // Wait for TXBUF to be empty in order to transmit to...
                                                         // ...terminal via UART
           UART_print("Frequency: ");
           while(!(EUSCI A0 -> IFG & EUSCI_A_IFG_TXIFG));
           EUSCI_A0 -> TXBUF = freq_thou + '0';  // Prints digits one by one
           while(!(EUSCI A0 -> IFG & EUSCI A IFG TXIFG));
           EUSCI A0 -> TXBUF = freq hun + '0';
           while(!(EUSCI A0 -> IFG & EUSCI_A_IFG_TXIFG));
           EUSCI A0 -> TXBUF = freq ten + '0';
           while(!(EUSCI_A0 -> IFG & EUSCI_A_IFG_TXIFG));
           EUSCI_A0 -> TXBUF = freq_one + '0';
           while(!(EUSCI A0 -> IFG & EUSCI A IFG TXIFG));
           UART print(" Hz");
           UART esc code (NEWLINE);
           UART esc code (NEWLINE);
           difference = 0;
           frequency = 0;
           i = 0;
            _delay_us(WRITE_DELAY);
           UART_esc_code(CLEARSCREEN);
           UART_esc_code(TOPLEFT);
       if (mode == DC) // If 'D' press detected, switch to DC measurements mode
           break;
   }
void get_AC_voltages(void)
   int max = DEF MAX; // Default values for maximum and minimum of array
   int min = DEF MIN;
   int rms = 0, new val = 0;
   uint8 t rms dig1, rms dig2, rms dig3; // Variables for sending individual RMS digits to terminal via UART
   int peak peak = 0;
   uint8 t peak dig1, peak dig2, peak dig3;
   int analog vals[ANALOG ARRAY LENGTH] = 0; // Initialize empty array for saving ADC values
   uint16_t i = 0, counter = 0; // Variables for cycling through arrays
   while(i < ANALOG ARRAY LENGTH) // Keep running until all array values are filled
   {
       if (ADCflag == 1)
       {
           analog_vals[i] = new_val;  // Save converted voltage value in array
           rms += pow(new val/100, 2); // Adding square of all samples to calculate true RMS voltage
           i++; // Increment array index
           ADCflag = 0; // Reset interrupt flag
           ADC14->CTL0 \mid= (ADC14 CTL0 SC); // Perform another sample and conversion
   }
   if (i \geq ANALOG ARRAY LENGTH) // Array is filled
```

```
for (counter = 0; counter < ANALOG ARRAY LENGTH; counter++)
       if (analog vals[counter] > max) // Find max value in array
           max = analog vals[counter];
       }
       if (analog vals[counter] < min) // Find min value in array</pre>
           min = analog_vals[counter];
       }
   }
}
   peak_peak = max - min; // Calculate peak-to-peak voltage as max voltage - min voltage
   rms = sqrt(rms/1000); // Calculate final true RMS voltage value
   rms\_dig1 = (rms/100) % 10; // "Splitting" RMS voltage into individual values to send to terminal...
   rms dig2 = (rms/10) % 10;
                             // ... via UART
   rms dig3 = (rms/1) % 10;
   peak_dig1 = (peak_peak/10000) % 10;
   peak dig2 = (peak peak/1000) % 10;
   peak dig3 = (peak peak/100) % 10;
   UART esc code(GREEN); // Header text is green
   while(!(EUSCI A0 -> IFG & EUSCI A IFG TXIFG)); // Wait for TXBUF to be empty before writing to terminal
   UART print("AC Measurements");
   UART_esc_code(WHITE);    // Change font color to white
   UART_esc_code(LEFT); // Move cursor to the left of the screen to create level text lines
   UART_esc_code(NEWLINE); // Skip a line on the terminal
   UART_esc_code(NEWLINE);
   while(!(EUSCI_A0 -> IFG & EUSCI_A_IFG_TXIFG));
   UART_print("Peak-to-Peak Voltage: ");
   while(!(EUSCI A0 -> IFG & EUSCI A IFG TXIFG));
   EUSCI AO -> TXBUF = peak dig1 + '0'; // Individually send peak-to-peak voltage value digits
   while(!(EUSCI A0 -> IFG & EUSCI A IFG TXIFG));
   UART print(".");
   while(!(EUSCI A0 -> IFG & EUSCI A IFG TXIFG));
   EUSCI A0 -> TXBUF = peak dig2 + '0';
   while(!(EUSCI A0 -> IFG & EUSCI A IFG TXIFG));
   EUSCI A0 -> TXBUF = peak dig3 + '0';
   while(!(EUSCI A0 -> IFG & EUSCI A IFG TXIFG));
   UART print(" V");
   UART esc code (NEWLINE);
   UART esc code (NEWLINE);
   UART esc code (LEFT);
   while(!(EUSCI A0 -> IFG & EUSCI A IFG TXIFG));
   UART_print("RMS Voltage: ");
   while(!(EUSCI_A0 -> IFG & EUSCI_A_IFG_TXIFG));
   while(!(EUSCI_A0 -> IFG & EUSCI_A_IFG_TXIFG));
   UART print(".");
   while(!(EUSCI A0 -> IFG & EUSCI A IFG TXIFG));
   EUSCI A0 -> TXBUF = rms dig2 + '0';
   while(!(EUSCI_A0 -> IFG & EUSCI_A_IFG_TXIFG));
   EUSCI A0 -> TXBUF = rms dig3 + '0';
   while(!(EUSCI A0 -> IFG & EUSCI A IFG TXIFG));
   UART print(" V");
   UART_esc_code(LEFT);
   UART esc code (NEWLINE);
   UART esc code (NEWLINE);
   draw bar graph(rms dig1, rms dig2, rms dig3); // Function for drawing voltage bar graph
   UART esc code (NEWLINE);
   UART esc code (NEWLINE);
```

```
UART esc code (NEWLINE);
       UART esc code(LEFT);
       UART esc code(CLEARDOWN); // Clear screen from cursor down
void TIMER A0 init(void)
   TIMER A0->CCTL[3] &= ~TIMER A CCTLN CCIFG; // Clear interrupt flag
   | TIMER_A_CTL_MC__CONTINUOUS // Select CONTINUOUS mode
                 | TIMER A CTL IE); // Enable interrupts
   TIMER_A0->CCTL[3] = (TIMER_A_CCTLN_CM_1
                                            // Capture on rising edge
                    | TIMER_A_CCTLN_CCIS_1 // Select CCIxB
                    | TIMER_A_CCTLN_CAP
                                            // Capture mode
                    | TIMER A CCTLN CCIE); // Capture/compare interrupt
   NVIC \rightarrow ISER[0] = 1 \ll (TA0_N_IRQn);
   __enable_irq();
void COMP init(void)
   C1_IN_PORT->SELO |= C1_IN; // C1.0 = P6.7
   C1_IN_PORT->SEL1 |= C1_IN;
   C1_OUT_PORT->SELO |= C1_OUT;
                               // C10UT = P7.2
   C1 OUT PORT->SEL1 &= ~C1 OUT;
   C1 OUT PORT->DIR |= C1 OUT;
   COMP E1->CTL0 = 0; // Initialize registers to 0 to make sure the correct settings are implemented
   COMP E1->CTL1 = 0;
   COMP E1->CTL2 = 0;
   COMP E1->CTL0 = (COMP E CTL0 IPEN // Enable comparator positive terminal
                | COMP E CTLO IPSEL 0);// Positive terminal = Ch0, C1.0 = P6.7
   COMP_E1->CTL1 = (COMP_E_CTL1_ON // Turn comparator on
                | COMP E CTL1 F
                                     // Enable comparator filter
                 | COMP E CTL1 FDLY 3); // Max out filter delay (~3000ns)
   COMP E1->CTL2 = (COMP E CTL2 RS 1
                                      // VCC applied to resistor ladder
                | COMP_E_CTL2_RSEL); // VCC applied to negative terminal
   COMP E1->CTL3 = COMP E CTL3 PD0; // Disable buffer on Port Ch0
}
void TA0 N IRQHandler(void)
   static int overflow = 0, new val = 0, prev val = 0;
   if (TIMER A0->CCTL[3] & TIMER_A_CCTLN_COV) // If overflow occurs
       TIMER A0->CCTL[3] &= ~TIMER A CCTLN COV; // Reset capture overflow
   if (TIMER A0->CTL & TIMER A CTL IFG) // If interrupt occurs
       TIMER A0->CTL &= ~TIMER A CTL IFG; // Clear interrupt flag
```

```
overflow++; // Increment overflow for frequency calculations
   }
   if (TIMER A0->CCTL[3] & TIMER A CCTLN CCIFG)
                                               // If capture occurs
       TIMER A0->CCTL[3] &= ~TIMER_A_CCTLN_CCIFG; // Reset CC flag
       prev val = new val; // Store first value in CC register
       new_val = TIMER_A0->CCR[3]; // Store second value in CC register
       difference = (new_val - prev_val) + (overflow * MAX_VAL); // Calculate difference for freq calculation
       overflow = 0;  // Reset overflow for next freq calculation
   }
}
void ADC14 IRQHandler(void)
   ADCflag = 1;
                // Raise ADC interrupt flag
   val = ADC14->MEM[2]; // Store value in memory register 2 in global array
void draw bar graph(int voltage1, int voltage2, int voltage3)
   while(!(EUSCI A0->IFG & EUSCI A IFG TXIFG));
                                               // Wait for TXBUF to be empty
   switch (voltage1) // Switch cases to decide how many "|" to print depending on...
                      // ... voltage value
       case 0:
          UART_print("|||");
           break;
       case 1:
           UART print("||||||||||");
           break;
       case 2:
           UART_print("||||||||||);
       case 3:
           UART print("|||||||||||);
   }
   while(!(EUSCI A0->IFG & EUSCI A IFG TXIFG));
   switch (voltage2)
       case 0:
          UART print("");
          break;
       case 1:
          UART print("|");
           break;
       case 2:
           UART print("||");
           break;
       case 3:
           UART print("|||");
           break;
       case 4:
          UART print("|||");
           break;
       case 5:
```

```
UART print("|||||");
           break;
       case 6:
           UART print("||||||");
           break;
       case 7:
           UART print("||||||");
           break:
       case 8:
           UART_print("|||||||");
           break;
       case 9:
           UART print("|||||||");
           break;
    }
    while(!(EUSCI A0->IFG & EUSCI A IFG TXIFG));
    switch (voltage3)
       case 2:
           UART print("|");
           break;
       case 4:
           UART print("||");
           break;
       case 6:
           UART_print("|||");
           break;
       case 8:
           UART_print("|||");
           break;
    }
    UART esc code (NEWLINE);
    UART esc code (NEWLINE);
    UART esc code(LEFT);
    while(!(EUSCI A0->IFG & EUSCI A IFG TXIFG));
    UART print("0.0----0.5----1.5----2.0----2.5----3.0----3.5----4.0---4.5----5.0"); // Draw number line
uint8 t EUSCIAO IRQHandler(void)
    int data;
   while(!(EUSCI A0->IFG & EUSCI A IFG TXIFG));
    if (EUSCI_A0->IFG & EUSCI_A_IFG_RXIFG) // Check if computer keyboard pressed
       data = EUSCI_A0->RXBUF; // Receive data from keyboard press
        if (data == 'D')
           mode = DC;  // DC mode measurements terminal display
        }
       else if (data == 'A')
           mode = AC;  // AC mode measurements terminal display
    return mode;
```

DCO.h

```
// DCO Code
#ifndef DCO_H_
#define DCO_H_
#define CS_KEY_VAL 0x695A

#define CYCLES 3000000
#define FREQ_1_5_MHz CS_CTL0_DCORSEL_0
#define FREQ_3_MHz CS_CTL0_DCORSEL_1
#define FREQ_6_MHz CS_CTL0_DCORSEL_2
#define FREQ_12_MHz CS_CTL0_DCORSEL_3
#define FREQ_24_MHz CS_CTL0_DCORSEL_4

// function prototypes
void set_DCO(int);

#endif /* DCO_H_ */
```

```
#include "msp.h"
#include "DCO.h"
void set_DCO(int frequency)
   CS->KEY = CS KEY VAL; // Unlock CS Registers
   CS->CTLO = 0;
   if(frequency == FREQ_1_5_MHz)
           CS->CTL0 = CS CTL0 DCORSEL 0; // Set DCO to 1.5MHz
   else if(frequency == FREQ 3 MHz)
           CS->CTL0 = CS CTL0 DCORSEL 1; // Set DCO to 3MHz
   else if(frequency == FREQ_6_MHz)
          CS->CTL0 = CS_CTL0_DCORSEL_2; // Set DCO to 6MHz
   else if(frequency == FREQ 12 MHz)
          CS->CTLO = CS CTLO DCORSEL 3; // Set DCO to 12MHz
       }
   else if(frequency == FREQ 24 MHz)
          CS->CTL0 = CS CTL0 DCORSEL 4; // Set DCO to 24MHz
```

UART.h

#endif /* UART_H_ */

// UART Code #ifndef UART H #define UART H #define BRW VAL 13 // Calculated values for SMCLK = 24MHz and 115200 baud rate #define BRF VAL 0 #define BRS VAL 0x25 #define PUART P1 // UART port: P1 #define RX BIT2 // RX: P1.2 #define TX BIT3 // TX: P1.3 #define ESC 0x1B $\,$ // Escape code to enable escape commands #define BOLD "[1m" $\,$ // Bold font #define TOPLEFT "[H" // Moves cursor to the top left of the screen #define GREEN "[32m" // Green font #define LEFT "[100D" // Make value large enough such that cursor will always... $\ensuremath{//}$...move to the very left of the terminal screen #define NEWLINE "[1B" // Skips line on terminal #define WHITE "[37m" // White font #define CLEARSCREEN "[2J" // Clears the entire terminal screen #define CLEARDOWN "[0J" // Clears the terminal screen from the cursor down void UART_init(void); // Define UART.c functions void UART_print(char write_string[]); void UART_esc_code(char write_string[]);

```
#include "msp.h"
#include "DCO.h"
#include "UART.h"
void UART init(void)
    EUSCI A0->CTLW0 |= EUSCI A CTLW0 SWRST; // Initialize software reset
    EUSCI AO->CTLWO = (EUSCI A CTLWO SWRST | EUSCI A CTLWO SSEL SMCLK); // Select SMCLK as BRCLK source
    EUSCI_AO->BRW = BRW_VAL; // Configure clock prescaler of baud rate generator
    EUSCI A0->MCTLW = (EUSCI A MCTLW OS16 // Enable oversampling
                    | (BRF VAL<<EUSCI A MCTLW BRF OFS) // 1st modulation
                    | (BRS_VAL<<EUSCI_A_MCTLW_BRS_OFS)); // 2nd modulation
    PUART->SELO |= (RX|TX); // Configure UART pins
    PUART->SEL1 &= \sim (RX \mid TX);
    EUSCI A0->CTLW0 &= ~EUSCI A CTLW0 SWRST; // Clear software reset
    EUSCI_A0->IE |= EUSCI_A_IE_RXIE;
                                     // Enable RX to trigger interrupt
    NVIC->ISER[0] = 1 << (EUSCIA0_IRQn);  // Enable UART interrupt</pre>
    enable irq();  // Enable global interrupts
void UART print(char write string[])
    int i = 0; // Integer for checking characters in string
    while (write_string[i] != NULL) // Write string until null character reached
       while(!(EUSCI A0->IFG & EUSCI A IFG TXIFG));
       EUSCI A0->TXBUF = write string[i];
       i++;
void UART_esc_code(char write_string[])
   int i = 0; // Integer for checking characters in string
   while(!(EUSCI_A0->IFG & EUSCI_A_IFG_TXIFG));
    EUSCI A0->TXBUF = 0x1B; // Send '0x1B' to enable escape commands
    while (write string[i] != NULL) // Write string until null character reached
       while(!(EUSCI A0->IFG & EUSCI A IFG TXIFG));
       EUSCI A0->TXBUF = write_string[i];
       i++;
```

ADC.h

// ADC Code

```
#ifndef ADC H
#define ADC_H_
#define PORT ADC P5
                   // Analog pin = P5.5
#define ANALOG PIN BIT5
#define MEM REG 2 // Memory register 2 used for ADC14
\#define DEF_MAX -50000 // Max comparator value in for loop to calculate max of array
\#define DEF_MIN 50000 // Min comparator value in for loop to calculate min of array
#define ARRAY LENGTH 20 // 20 samples in array
#define SLOPE 203 // Calculated from array values
#define INTERCEPT 75031 // Calculated from array values
#define CAL 10000 // Value used to calibrate voltages
#define ZERO 0
#define MAX 330
#define PLUS2 2 // Calibration value
#define PLUS3 3 // Calibration value
void ADC14_init(void);
#endif /* ADC_H_ */
```

```
#include "msp.h"
#include "DCO.h"
#include "UART.h"
#include "ADC.h"
void ADC14 init(void)
   ADC14->CTL0 &= ~ADC14 CTL0 ENC; // ADC14 disabled
   ADC14->CTL0 = ADC14_CTL0_SSEL__HSMCLK // Select HSMCLK for ADC14
              | ADC14 CTL0 SHT0 0 // Sample time of 4 clocks, registers 0-7, 24-31
              | ADC14 CTL0 CONSEQ 0 // Select single conversion
              | ADC14 CTL0 ON; // ADC14 on
   ADC14->CTL1 = (MEM REG<<ADC14 CTL1 CSTARTADD OFS) // Start conversions at memory register 2;
               | ADC14 CTL1 RES 14BIT // ADC 14-bit resolution
               | ADC14_CTL1_BATMAP;
                                       // Input channel = 0, P5.5
   ADC14->MCTL[2] = ADC14_MCTLN_INCH_0
                 | ADC14_MCTLN_VRSEL_0; // Select 3.3V = AVCC
   ADC14->CTL0 |= ADC14 CTL0 ENC | ADC14 CTL0 SC; // ADC enabled, start sample and conversion
   PORT ADC->SELO |= ANALOG PIN; // Configure analog pin, P5.5
   PORT ADC->SEL1 |= ANALOG PIN;
   ADC14->IERO = ADC14_IERO_IE2; // Enable interrupt for memory control register 2
   NVIC \rightarrow ISER[0] = 1 \ll (ADC14 IRQn);
   __enable_irq();
```

External References

- [1] *MSP432P4xx SimpleLink™ Microcontrollers Technical Reference Manual*, Texas Instruments, Mar. 2015 [Revised June 2019].
- [2] Texas Instruments, "MSP432P401R, MSP432P401M SimpleLink™ Mixed-Signal Microcontrollers," MSP432P401R datasheet, Mar. 2015 [Revised June 2019].