



Daffodil *International* **University**

Lab report no three

Arithmetic Operations in Assembly Language.

Course code: CSE232

Course Title: Microprocessor, Embedded Systems and IoT

Submitted To

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Date of Submission

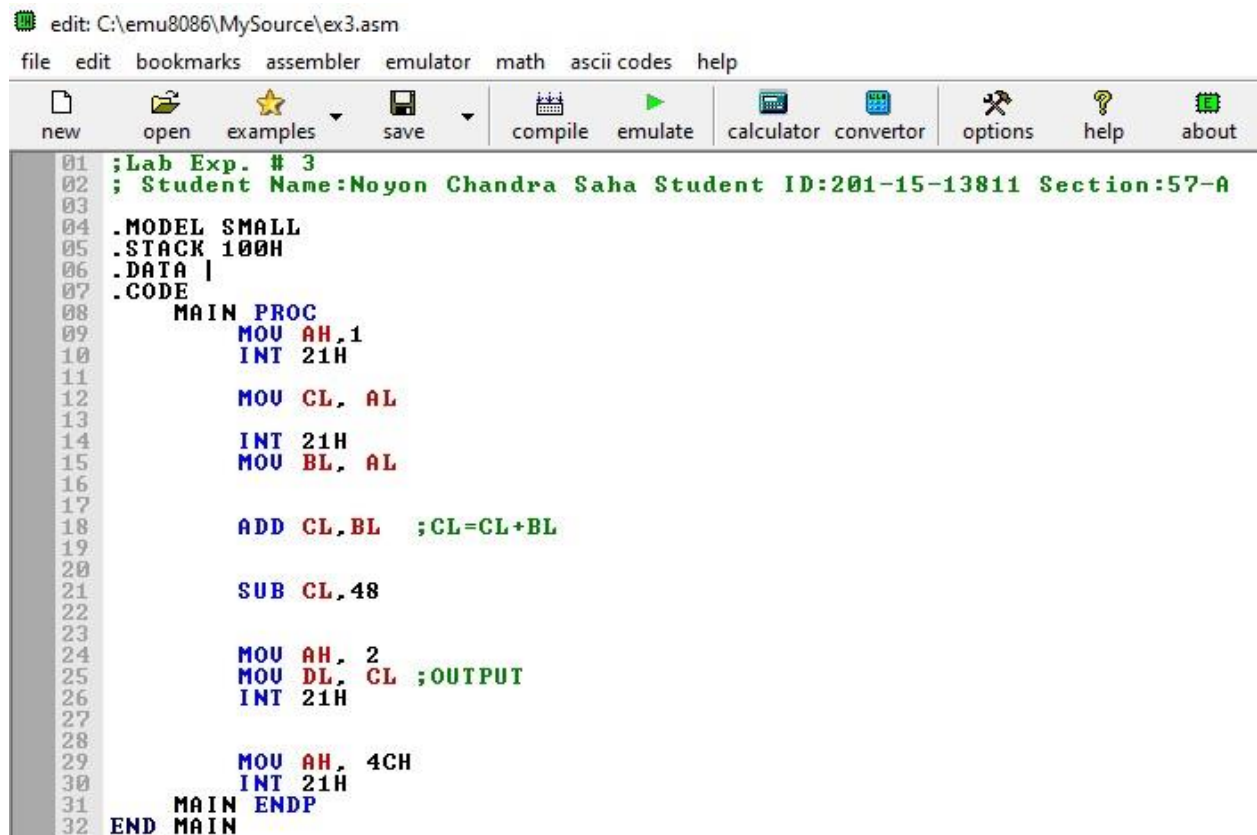
15 November 2022

Experiment name: Arithmetic Operations in Assembly Language.

Procedure:

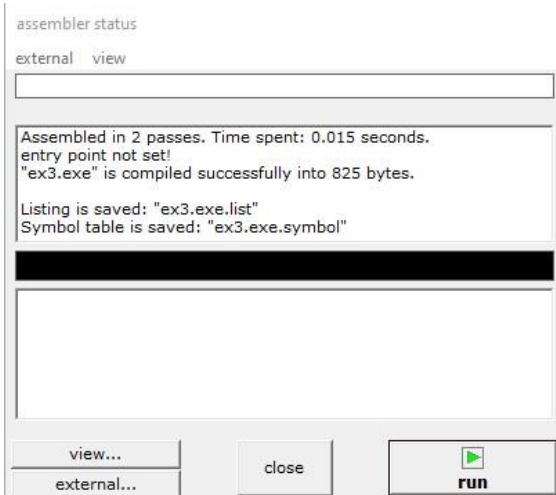
Today we learn about Arithmetic Operations in Assembly Language.

EMU8086 Source Editor:



```
edit: C:\emu8086\MySource\ex3.asm
file  edit  bookmarks  assembler  emulator  math  ascii codes  help
new  open  examples  save  compile  emulate  calculator  convertor  options  help  about
01 ;Lab Exp. # 3
02 ; Student Name:Noyon Chandra Saha Student ID:201-15-13811 Section:57-A
03
04 .MODEL SMALL
05 .STACK 100H
06 .DATA |
07 .CODE
08     MAIN PROC
09         MOV AH,1
10         INT 21H
11
12         MOV CL, AL
13
14         INT 21H
15         MOV BL, AL
16
17
18         ADD CL,BL ;CL=CL+BL
19
20
21         SUB CL,48
22
23
24         MOV AH, 2
25         MOV DL, CL ;OUTPUT
26         INT 21H
27
28
29         MOV AH, 4CH
30         INT 21H
31     MAIN ENDP
32 END MAIN
```

Assembler Status:



Outcome:

emulator: ex1.exe_

file math debug view external virtual devices virtual drive help

Load reload step back single step run step delay ms: 0

registers

	H	L
AX	4C	39
BX	00	33
CX	01	39
DX	00	39
CS	F400	
IP	0204	
SS	0710	
SP	00FA	
BP	0000	
SI	0000	
DI	0000	
DS	0700	
ES	0700	

F400:0204

F4200:	FF	255	RES
F4201:	FF	255	RES
F4202:	CD	205	=
F4203:	21	033	!
F4204:	CF	207	!
F4205:	00	000	NULL
F4206:	00	000	NULL
F4207:	00	000	NULL
F4208:	00	000	NULL
F4209:	00	000	NULL
F420A:	00	000	NULL
F420B:	00	000	NULL
F420C:	00	000	NULL
F420D:	00	000	NULL
F420E:	00	000	NULL
F420F:	00	000	NULL
F4210:	00	000	NULL
F4211:	00	000	NULL
F4212:	00	000	NULL
F4213:	00	000	NULL
F4214:	00	000	NULL
F4215:	00	000	NULL

F400:0204

BIOS DI
INT 021h
IRET
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
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ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
...

screen source reset aux vars debug stack flags

emulator screen (80x25 chars)

639

clear screen change font 0/16