				Camlin Page Date / /		
	PROGRAM	NO: 2	TRAFFIC	CONTROL		
5	AIM: Traffic signal simulation					
161	HARDWARE -	REQUIR	ENENTS: A	rduino Boa	necting wines	
10	CIRCUIT DIAGRAMS					
		318	5V	77	+	
15		RST	D12 ·			
	_	AREP	DII			
			D10			
			D8			
20			D7			
	7		De De	4.7		
	- 1	A 0	D4			
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25		A 2	02	the to		
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		Camlin Page Date / /
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	CODE:	
	void setup ()	
	pin Mode (13,0 UTPUT);	
	pinMode (12, OUTPUT);	
5	pin Mode (11,00TPUT);	
	3	
	void 100p()	
	'	Α
10	digital Write (13, HI)	зн), <u> </u>
10	digital Write (13, HIC digital Write (12, Low digital Write (11, Low delay (5000); delay (5000); delay (13, Low digital Write (12, HIC digital Write (12, HIC digital Write (11, Low delay (1000);),
	digital Write (11, LOV	y)'
	delay (5000);	
	ai gital Write (13, LOI	N)'
15	digital Write (12, HIC	14)
	digital Winte (11, LO	w) ;
	delay (1000);	•
	digital Write (12, LG) digital Write (12, LG) digital Write (11, HI) delay (5000);	ν) <u>'</u>
	oligital Write (12, L	οω) <u>'</u>
20	digital Write (11, HI	CH).
	delay (5000);	
	3	
25		
30		