

Magi Language Compiler

Zhijian LIU

Shanghai Jiao Tong University

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The Gift of the Magi

The Gift of the Magi is a short story, written by O. Henry, about a young married couple and how they deal with the challenge of buying secret Christmas gifts for each other with very little money. As a sentimental story with a moral lesson about gift-giving, it has been a popular one for adaptation, especially for presentation at Christmas time. The plot and its "twist ending" are well-known, and the ending is generally considered an example of comic irony.

Project Overview

- Time period: from **Mar 22** to **May 13**
- Git commits: **172** commits
- Java code length: **13485** lines

Highlights

- Use a well-organized framework with good extensibility
- Feed back a user-friendly compilation error message
- Support most features of **OOP** including member function, private modifier, class inheritance, member initialization and constructor function
- Achieve an outstanding compilation quality
- Implement the **SSA** form and do some optimizations on it including the useless code elimination and dominator-based value numbering technique
- Do not use any peephole and data-oriented optimizations
- Use the **global** register allocation and improve the algorithm
- Make good use of the version control system

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Demonstration: Compilation Error Message

- 1:31: the function "angry" cannot have two parameters named "haha"!
- 2:4: "haha" is not a symbol name!
- 6:10: the number of parameters in the function-call expression is wrong!
- 7:12: two int/string-type expressions are expected in the addition expression!

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Demonstration: Object Oriented Programming

```
class Pair {
    public int first = 0;
    public int second = 0;
    public bool equal = true;

    public Pair() {
        this.first = this.second = 0;
        this.equal = true;
    }

    public Pair(int first, int second) {
        this.first = first;
        this.second = second;
        this.equal = (first == second);
    }

    public void print() {
        println(toString(this.first) + " " + toString(this.second));
    }
}

class Triple extends Pair {
    public int third = 0;
    public bool equal = true;

    public Triple() {
        this.first = this.second = this.third = 0;
        this.equal = true;
    }

    public Triple(int first, int second, int third) {
        this.first = first;
        this.second = second;
        this.third = third;
        this.equal = (first == second && second == third);
    }

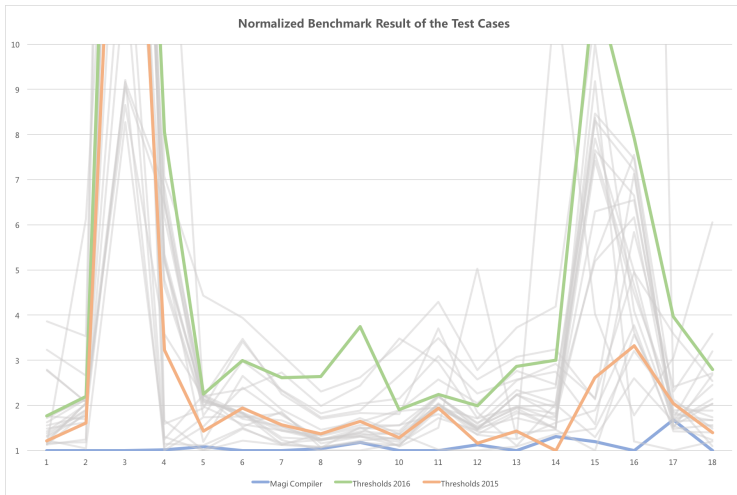
    public void print() {
        println(toString(this.first) + " " + toString(this.second) + " " + toString(this.third));
    }
}

int main() {
    Pair pair = new Pair(1, 2);
    pair.print();
    Triple triple = new Triple(1, 2, 3);
    triple.print();
    return 0;
}
```


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Demonstration: Normalized Benchmark Result



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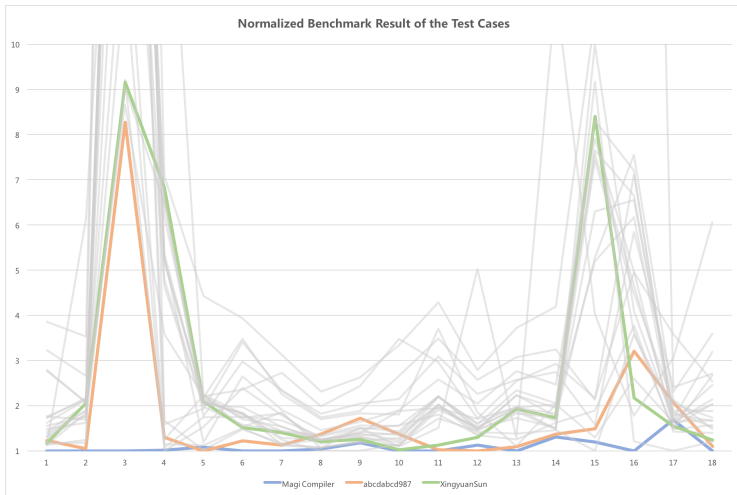
Demonstration: Single Static Assignment

```
func min $p0 $p1 {  
  %min.0.enter:  
    $t4 = move $p1  
    $t5 = move $p0  
    jump %min.1.entry  
  
  %min.1.entry:  
    $t3 = slt $t5 $t4  
    br $t3 %min.2.if_true %min.3.if_false  
  
  %min.2.if_true:  
    $t2 = move $t5  
    jump %min.4.if_merge  
  
  %min.4.if_merge:  
    ret $t2  
    jump %min.5.exit  
  
  %min.3.if_false:  
    $t2 = move $t4  
    jump %min.4.if_merge  
  
  %min.5.exit:  
    $p1 = move $t4  
    $p0 = move $t5  
}
```

Demonstration: Single Static Assignment

```
func min $p0.0 $p1.0 {  
    %min.0.enter:  
        $t4.0 = move $p1.0  
        $t5.0 = move $p0.0  
        jump %min.1.entry  
  
    %min.1.entry:  
        $t3.0 = slt $t5.0 $t4.0  
        br $t3.0 %min.2.if_true %min.3.if_false  
  
    %min.2.if_true:  
        $t2.0 = move $t5.0  
        jump %min.4.if_merge  
  
    %min.4.if_merge:  
        $t2.1 = phi %min.2.if_true $t2.0 %min.3.if_false $t2.2  
        ret $t2.1  
        jump %min.5.exit  
  
    %min.3.if_false:  
        $t2.2 = move $t4.0  
        jump %min.4.if_merge  
  
    %min.5.exit:  
        $p1.1 = move $t4.0  
        $p0.1 = move $t5.0  
}
```

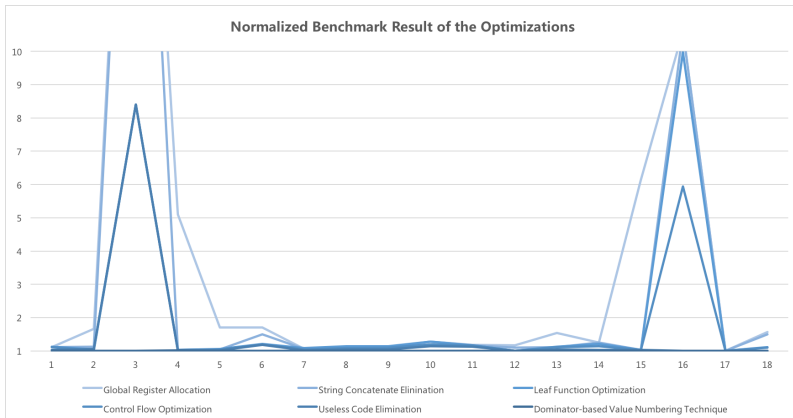
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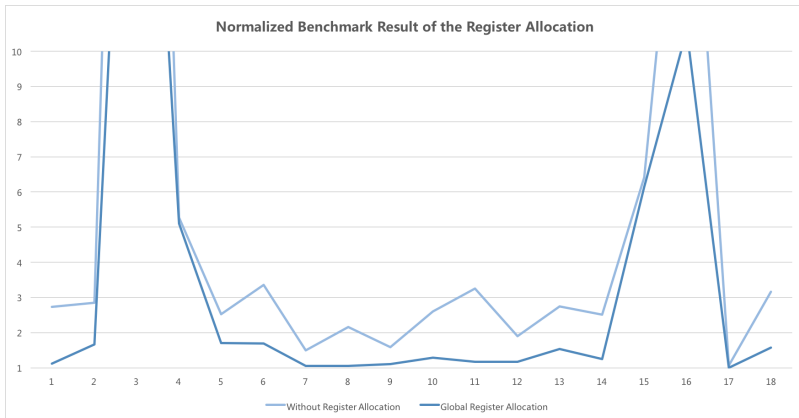
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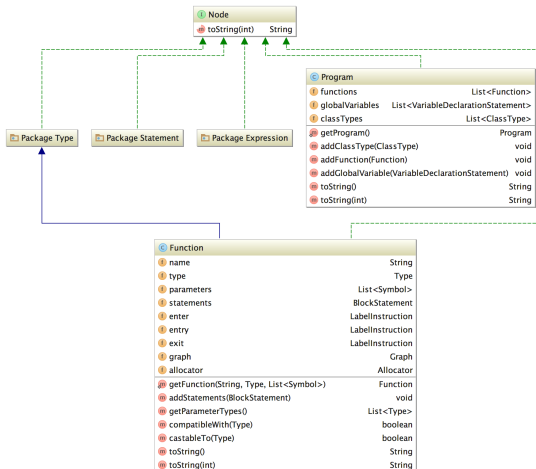
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Front End

- 1 Use **AN**other **T**ool for **L**anguage **R**ecognition to generate the lexer and parser
- 2 Convert the **C**oncrete **S**yntax **T**ree to the **A**bstract **S**yntax **T**ree by the listener mode of **ANTLR**

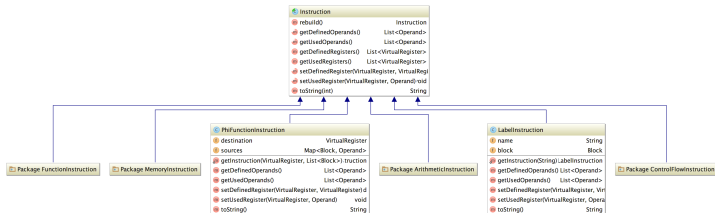
Abstract Syntax Tree Framework



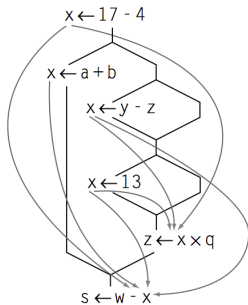
Back End

- 1 Convert the **A**bstract **S**yntax **T**ree to the linear **I**ntermediate **R**epresentation
- 2 Build the **C**ontrol **F**low **G**raph by scanning the linear **I**ntermediate **R**epresentation

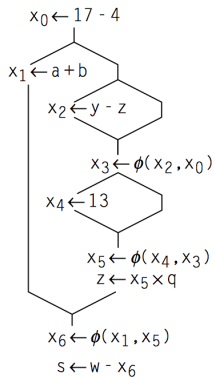
Intermediate Representation Framework



Single Static Assignment



(a) Original Code Fragment



(b) With x in SSA Form

Single Static Assignment

- 1 Compute the immediate dominator and the dominance frontier
- 2 Insert ϕ -functions
- 3 Rename the variables and the temporaries
- 4 Do some optimizations based on SSA form
- 5 Translate out of SSA form

Single Static Assignment: Analyze the Dominance

Definition

In a flow graph with entry node s , node u **dominates** node v iff u lies on every path from s to v

Data flow equation

$$\text{dom}_v = \{v\} \cup \left(\bigcap_{p \in \text{pred}_v} \text{dom}_p \right)$$

Single Static Assignment: Analyze the Dominance

Definition

In a flow graph with entry node s , node u **dominates** node v iff u lies on every path from s to v

Data flow equation

$$\text{dom}_v = \{v\} \cup \left(\bigcap_{p \in \text{pred}_v} \text{dom}_p \right)$$

Single Static Assignment: Analyze the Dominance

Definitions

- A node u **strictly dominates** a node v if u dominates v and u does not equal v .
- The **immediate dominator** of a node u is the unique node that strictly dominates u but does not strictly dominate any other node that strictly dominates u .
- The **dominance frontier** of a node u is the set of all nodes v such that u dominates an immediate **predecessor** of v , but u does not strictly dominate v . It is the set of nodes where u 's dominance stops.

Single Static Assignment: Immediate Dominator

```
for all nodes, b    // initialize the dominators array
    IDoms[b]  $\leftarrow$  Undefined
IDoms[b0]  $\leftarrow$  b0
Changed  $\leftarrow$  true
while (Changed)
    Changed  $\leftarrow$  false
    for all nodes, b, in reverse postorder (except root)
        NewIDom  $\leftarrow$  first (processed) predecessor of b // pick one
        for all other predecessors, p, of b
            if IDoms[p]  $\neq$  Undefined // i.e., Doms[p] already calculated
                then NewIDom  $\leftarrow$  Intersect(p, NewIDom)
        if IDoms[b]  $\neq$  NewIDom then
            IDoms[b]  $\leftarrow$  NewIDom
            Changed  $\leftarrow$  true

Intersect(i, j)
    finger1  $\leftarrow$  i
    finger2  $\leftarrow$  j
    while (finger1  $\neq$  finger2)
        while (RPO(finger1) > RPO(finger2))
            finger1 = IDoms[finger1]
        while (RPO(finger2) > RPO(finger1))
            finger2 = IDoms[finger2]
    return finger1
```

Single Static Assignment: Insert ϕ -functions

```
Globals  $\leftarrow \emptyset$   
Initialize all the Blocks sets to  $\emptyset$   
for each block  $b$   
  VARKILL  $\leftarrow \emptyset$   
  for each operation  $i$  in  $b$ , in order  
    assume that  $op_i$  is " $x \leftarrow y \text{ op } z$ "  
    if  $y \notin \text{VARKILL}$  then  
      Globals  $\leftarrow \text{Globals} \cup \{y\}$   
    if  $z \notin \text{VARKILL}$  then  
      Globals  $\leftarrow \text{Globals} \cup \{z\}$   
  VARKILL  $\leftarrow \text{VARKILL} \cup \{x\}$   
  Blocks( $x$ )  $\leftarrow \text{Blocks}(x) \cup \{b\}$ 
```

(a) Finding Global Names

```
for each name  $x \in \text{Globals}$   
  WorkList  $\leftarrow \text{Blocks}(x)$   
  for each block  $b \in \text{WorkList}$   
    for each block  $d$  in  $\text{DF}(b)$   
      if  $d$  has no  $\phi$ -function for  $x$  then  
        insert a  $\phi$ -function for  $x$  in  $d$   
        WorkList  $\leftarrow \text{WorkList} \cup \{d\}$ 
```

(b) Rewriting the Code

Single Static Assignment: Rename the Variables

```
for each global name  $i$ 
  counter[ $i$ ]  $\leftarrow 0$ 
  stack[ $i$ ]  $\leftarrow \emptyset$ 
Rename( $n_0$ )
```

```
 newName( $n$ )
   $i \leftarrow \text{counter}[n]$ 
  counter[ $n$ ]  $\leftarrow \text{counter}[n] + 1$ 
  push  $i$  onto stack[ $n$ ]
  return " $n_i$ "
```

```
Rename( $b$ )
  for each  $\phi$ -function in  $b$ , " $x \leftarrow \phi(\dots)$ "
    rewrite  $x$  as newName( $x$ )

  for each operation " $x \leftarrow y \text{ op } z$ " in  $b$ 
    rewrite  $y$  with subscript top(stack[ $y$ ])
    rewrite  $z$  with subscript top(stack[ $z$ ])
    rewrite  $x$  as newName( $x$ )

  for each successor of  $b$  in the CFG
    fill in  $\phi$ -function parameters

  for each successor  $s$  of  $b$  in the dominator tree
    Rename( $s$ )

  for each operation " $x \leftarrow y \text{ op } z$ " in  $b$ 
    and each  $\phi$ -function " $x \leftarrow \phi(\dots)$ "
      pop(stack[ $x$ ])
```

Optimizations

Regular optimizations

- 1 Leaf function optimization
- 2 Control flow optimization

Optimizations based on SSA form

- 1 Useless code elimination
- 2 Dominator-based value numbering technique

Optimization: Control Flow Optimization

```
Clean( )  
    while the CFG keeps changing  
        compute postorder  
        OnePass( )  
  
OnePass( )  
    for each block i, in postorder  
        if i ends in a conditional branch then  
            if both targets are identical then  
                replace the branch with a jump  
        if i ends in a jump to j then  
            if i is empty then  
                replace transfers to i with transfers to j  
            if j has only one predecessor then  
                combine i and j  
            if j is empty and ends in a conditional branch then  
                overwrite i's jump with a copy of j's branch
```

Optimization: Useless Code Elimination

```
Mark( )
  WorkList  $\leftarrow \emptyset$ 
  for each operation  $i$ 
    clear  $i$ 's mark
    if  $i$  is critical then
      mark  $i$ 
      WorkList  $\leftarrow$  WorkList  $\cup \{i\}$ 
  while (WorkList  $\neq \emptyset$ )
    remove  $i$  from WorkList
    (assume  $i$  is  $x \leftarrow y \text{ op } z$ )
    if  $\text{def}(y)$  is not marked then
      mark  $\text{def}(y)$ 
      WorkList  $\leftarrow$  WorkList  $\cup \{\text{def}(y)\}$ 
    if  $\text{def}(z)$  is not marked then
      mark  $\text{def}(z)$ 
      WorkList  $\leftarrow$  WorkList  $\cup \{\text{def}(z)\}$ 
  for each block  $b \in \text{RDF}(\text{block}(i))$ 
    let  $j$  be the branch that ends  $b$ 
    if  $j$  is unmarked then
      mark  $j$ 
      WorkList  $\leftarrow$  WorkList  $\cup \{j\}$ 
```

(a) The Mark Routine

```
Sweep( )
  for each operation  $i$ 
    if  $i$  is unmarked then
      if  $i$  is a branch then
        rewrite  $i$  with a jump
          to  $i$ 's nearest marked
          postdominator
      if  $i$  is not a jump then
        delete  $i$ 
```

(b) The Sweep Routine

Optimization: Dominator-based Value Numbering

```
procedure DVNT(B)
  allocate a new scope for B
  for each  $\phi$ -function of the form " $n \leftarrow \phi(\dots)$ " in B
    if  $p$  is meaningless or redundant then
       $VN[n] \leftarrow$  the value number for  $p$ 
      remove  $p$ 
    else
       $VN[n] \leftarrow n$ 
      Add  $p$  to the hash table
  for each assignment  $a$  of the form " $x \leftarrow y \text{ op } z$ " in B
    overwrite  $y$  with  $VN[y]$ 
    overwrite  $z$  with  $VN[z]$ 
    let  $\text{expr} \leftarrow "y \text{ op } z"$ 
    if  $\text{expr}$  can be simplified to  $\text{expr}'$  then
      replace  $a$  with " $x \leftarrow \text{expr}'$ "
       $\text{expr} \leftarrow \text{expr}'$ 
    if  $\text{expr}$  has a value number  $v$  in the hash table then
       $VN[x] \leftarrow v$ 
      remove statement  $a$ 
    else
       $VN[x] \leftarrow x$ 
      add  $\text{expr}$  to the hash table with value number  $x$ 
  for each successor  $s$  of  $B$ 
    adjust the  $\phi$ -function inputs in  $s$ 
  for each child  $c$  of  $B$  in the dominator tree
    DVNT( $c$ )
  deallocate the scope for  $B$ 
```

Global Register Allocation

- 1 Analyze the liveness
- 2 Build the interference graph
- 3 Use the bottom-up coloring

Global Register Allocation: Liveliness Analysis

Definitions

- v **live** at e iff there is a path p from e to i such that
 - instruction i uses v
 - no instructions defining v on the path p
- v **live-in** at i iff v **live** at e , where $e \in \text{in-edge}_i$
- v **live-out** at i iff v **live** at e , where $e \in \text{out-edge}_i$

Data flow equation

$$\begin{aligned} \text{live-in}_i &= \text{use}_i \cup (\text{live-out}_i \setminus \text{def}_i) \\ \text{live-out}_i &= \bigcup_{s \in \text{succ}_i} \text{live-in}_s \end{aligned}$$

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Global Register Allocation: Liveness Analysis

- 1 Calculate the *def* and *use* for each basic block
- 2 Do the liveness analysis on each basic block by using **the fix-point algorithm**
- 3 Calculate the liveness information for each instruction in each basic block by **the one-pass backward calculation**

Global Register Allocation: Interference Graph

- For a **move** instruction i ,
 - add **forbidden** edges between def_i and live-out; \ use_i
 - add **recommend** edges between def_i and use_i
- For a **non-move** instruction i ,
 - add **forbidden** edges between def_i and live-out;

Global Register Allocation: Bottom-up Coloring

Algorithm 1 Computing the coloring order for $G = (V, E)$

```
1: initialize stack to empty
2: while  $V$  is not empty do
3:   if  $\exists v \in V$  with  $\deg_v < k$  then
4:      $candidate \leftarrow v$ 
5:   else
6:      $candidate \leftarrow v$  picked from  $V$ 
7:   end if
8:   remove  $candidate$  and its edges in the graph  $G$ 
9:   push  $candidate$  onto stack
10: end while
```

Global Register Allocation: Bottom-up Coloring

Algorithm 2 Coloring bottom-up for $G = (V, E)$

```
1: while stack is not empty do  
2:    $v \leftarrow pop(stack)$   
3:   insert  $v$  and its edges into the graph  $G$   
4:   color  $v$   
5: end while
```

Other work

- Give some of my classmates advice on the framework and optimization
- Provide some **IR** test data for my classmates
- Provide a unit test file for my classmates
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Summary

- I am ashamed to do only a little bit of the work
- Thank all of you, my **TAs** and my classmates