

# Reductions for NP-Complete Problems

Tao Hou

## 1 Circuit Satisfiability: A First NP-Complete Problem

**Definition 1.** Define a *circuit*  $K$  to be a labeled, directed acyclic graph such as the one shown in Figure 1:

- The *sources* in  $K$  are the nodes with no incoming edges. They are either labeled a fixed value 0 or 1, or need to be assigned a value (thus called *free sources*)
- Every other node is labeled with one of the Boolean operators  $\wedge$ ,  $\vee$ , or  $\neg$ ; nodes labeled with  $\wedge$  or  $\vee$  have two incoming edges, and nodes labeled with  $\neg$  have one incoming edge.
- There is a single node with no outgoing edges, and it represents the output: the result computed by the circuit.

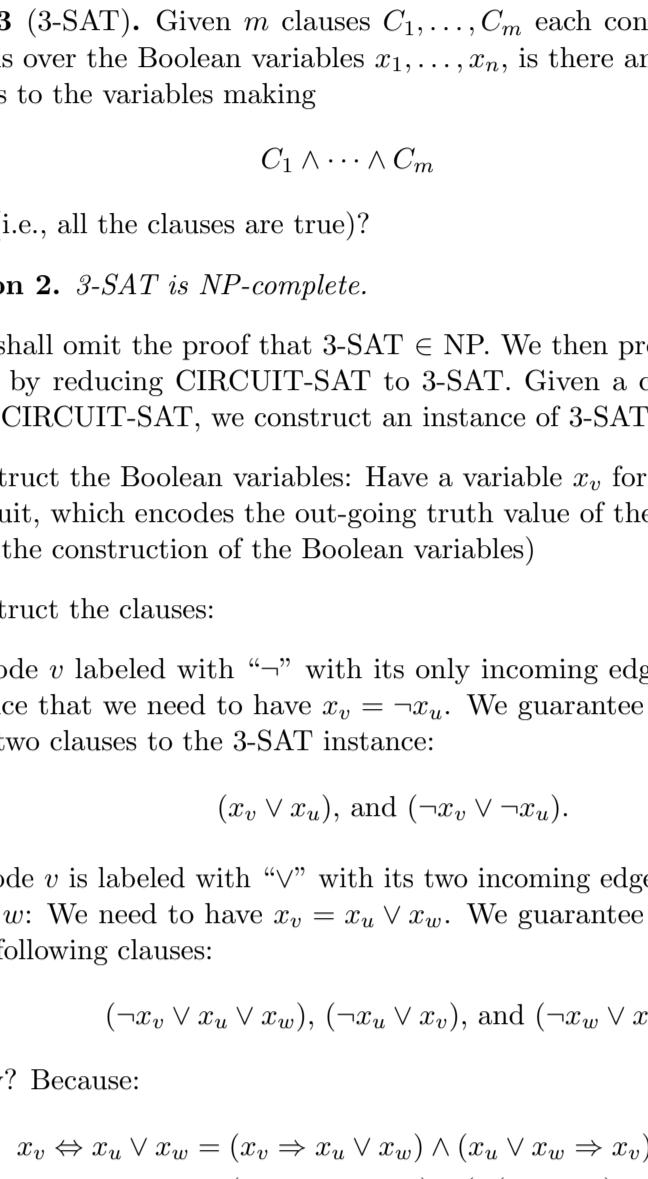


Figure 1: A circuit with 5 sources (two of them have fixed truth values and three are free sources) and one output. [Figure from [1]]

**Problem 1 (CIRCUIT-SAT).** Given a circuit  $K$ , is there an assignment of 0-1 values to the free sources that causes the output to have the value 1?

**Proposition 1.** CIRCUIT-SAT is NP-complete.

*Sketch of proof.*

- Any algorithm that takes an input of  $n$  bits and produces a yes/no answer can be represented by a circuit having  $n$  sources
- Moreover, if the algorithm takes poly-time, then circuit is of poly-size w.r.t  $n$ .
- Consider some problem  $X$  in NP, which has a poly-time certifier  $C(s, t)$ .
- An instance  $s$  of  $X$  is an “yes” instance if and only if there exists a certificate  $t$  of length  $p(|s|)$  such that  $C(s, t) = \text{“yes”}$ .
- Notice  $C(s, t)$  is an algorithm taking an input of  $|s| + |t|$  bits
- So we convert it into a poly-size circuit  $K$  with  $|s| + |t|$  sources:
  - First  $|s|$  sources are *hard-coded* with values of  $s$
  - Remaining  $|t|$  sources are *free* and represent bits of the certificate  $t$
- Circuit  $K$  is satisfiable there iff exists a certificate  $t$  making  $C(s, t) = \text{“yes”}$ .  $\square$

## 2 SAT and 3-SAT

**Definition 2.** A Boolean formula is made up of the Boolean variables  $x_1, \dots, x_n$ , operators including  $\wedge$  (AND),  $\vee$  (OR),  $\neg$  (NOT),  $\Rightarrow$  (implication),  $\Leftrightarrow$  (if and only if), and composite (combinations) of them possibly with parenthesis. E.g.:

$$(x_1 \vee x_2) \vee \neg((\neg x_1 \Leftrightarrow x_3) \vee x_4)) \wedge \neg x_2.$$

**Problem 2 (SAT).** Given a Boolean formula with  $n$  Boolean variables  $x_1, \dots, x_n$ , is there an assignment (of true/false values) to  $x_1, \dots, x_n$  making the whole formula true?

**Definition 3.** Given  $n$  Boolean variables  $x_1, \dots, x_n$ :

- *literal*: one of the variables  $x_i$  or its negation  $\neg x_i$
- *clause*: a disjunction of distinct literals, e.g.,  $x_1 \vee \neg x_2 \vee \neg x_1 \vee x_3$

**Problem 3 (3-SAT).** Given  $m$  clauses  $C_1, \dots, C_m$  each containing exactly three literals over the Boolean variables  $x_1, \dots, x_n$ , is there an assignment of truth values to the variables making

$$C_1 \wedge \dots \wedge C_m$$

to be true (i.e., all the clauses are true)?

**Proposition 2.** 3-SAT is NP-complete.

*Proof.* We shall omit the proof that 3-SAT  $\in$  NP. We then prove that 3-SAT is NP-hard by reducing CIRCUIT-SAT to 3-SAT. Given a circuit  $K$  as an instance of CIRCUIT-SAT, we construct an instance of 3-SAT as follows:

- To construct the Boolean variables: Have a variable  $x_v$  for each node  $v$  of the circuit, which encodes the out-going truth value of the node  $v$ . (This finishes the construction of the Boolean variables)

- To construct the clauses:
  - A node  $v$  labeled with “ $\neg$ ” with its only incoming edge from node  $u$ : Notice that we need to have  $x_v = \neg x_u$ . We guarantee this by adding the two clauses to the 3-SAT instance:

$$(x_v \vee x_u), \text{ and } (\neg x_v \vee \neg x_u).$$

- A node  $v$  is labeled with “ $\vee$ ” with its two incoming edges from nodes  $u$  and  $w$ : We need to have  $x_v = x_u \vee x_w$ . We guarantee this by adding the following clauses:

$$(\neg x_v \vee x_u \vee x_w), (\neg x_u \vee x_v), \text{ and } (\neg x_w \vee x_v).$$

Why? Because:

$$\begin{aligned} x_v \Leftrightarrow x_u \vee x_w &= (x_v \Rightarrow x_u \vee x_w) \wedge (x_u \vee x_w \Rightarrow x_v) \\ &= (\neg x_v \vee x_u \vee x_w) \wedge (\neg(x_u \vee x_w) \vee x_v) \\ &= (\neg x_v \vee x_u \vee x_w) \wedge ((\neg x_u \wedge \neg x_w) \vee x_v) \\ &= (\neg x_v \vee x_u \vee x_w) \wedge (\neg x_u \vee x_v) \wedge (\neg x_w \vee x_v) \end{aligned}$$

- A node  $v$  labeled with “ $\wedge$ ” with two incoming edges from nodes  $u$  and  $w$ : We need to have  $x_v = x_u \wedge x_w$ . We add the following clauses:

$$(\neg x_v \vee x_u), (\neg x_v \vee x_w), \text{ and } (x_v \vee \neg x_u \vee \neg x_w).$$

- A source  $v$  labeled with a fixed value 1: Add a clause  $(x_v)$  with a single literal, forcing the variable  $x_v$  to take the designated value 1.
- A source  $v$  labeled with a fixed value 0: Add a clause  $(\neg x_v)$  with a single literal, forcing the variable  $x_v$  to take the designated value 0.
- The output node  $v$ : Add the single-literal clause  $(x_o)$ , which requires that  $v$  take the value 1.

We then claim that all the clauses we have constructed for 3-SAT are satisfiable iff the circuit  $K$  can be satisfied:

- Since free sources in  $K$  correspond to Boolean variables but do not correspond to any clauses, we are free to choose values for the Boolean variables corresponding to these free sources

- The clauses constructed for each internal node  $v$  guarantee that the Boolean variable  $x_v$  has the same value as the outgoing edge in  $K$  given a certain assignment on the free sources

- The output value also has to be 1 by the single-literal clause  $(x_o)$

Notice that our goal was to create an instance of 3-SAT where all clauses have exactly 3 literals, while in the instance we constructed, some clauses have lengths of 1 or 2. So we need to convert this instance of SAT to an equivalent instance in which each clause has exactly three literals.

To do this:

- Create four new variables:  
$$z_1, z_2, z_3, z_4,$$

- And the four clauses:

$$(\neg z_i \vee z_1 \vee z_2), (\neg z_i \vee z_1 \vee z_3), (\neg z_i \vee z_2 \vee z_4), (\neg z_i \vee z_3 \vee z_4)$$

for each of  $i = 1$  and  $i = 2$ . In order for all four clauses to be true, we must have  $z_i = 0$  for  $i = 1$  and  $i = 2$ .

Then:

- $(t) \Rightarrow (t \vee z_1 \vee z_2)$
- $(t \vee t') \Rightarrow (t \vee t' \vee z_1)$   $\square$

**Proposition 3.** SAT is NP-complete.

*Proof.* This is easy because each instance of 3-SAT is an instance of SAT.  $\square$

## 3 Independent set

**Definition 4.** In a graph  $G = (V, E)$ , we say a set of vertices  $S \subseteq V$  is an *independent set* if no two vertices in  $S$  form an edge in  $G$ .

**Remark 4.** By default, graphs are *undirected* in this topic.

**Remark 5.** It is easy to find small independent sets in a graph (for example, a single node forms an independent set); the hard part is to find a large independent set, since when you add more and more points into a set, it becomes more probable that two vertices from the set are connected by an edge.

**Problem 4 (IND-SET).** Given a graph  $G$  and a number  $k$ , does  $G$  contain an independent set of size at least  $k$ ?

**Remark 6.** The “optimization” version of the above problem is to find the independent set with the maximum size.

**Proposition 7.** IND-SET is NP-complete.

*Proof.* IND-SET is in NP. To design a certifier, given a certificate  $t$ , you try to decode  $k$  vertices from the bits in  $t$ . If the decoding cannot be done, you simply return “no”. Otherwise, you check whether the  $k$  vertices form an independent set (which is easy), and return “yes” or “no” accordingly.

**IND-SET is NP-complete.** We show that 3-SAT reduces to IND-SET. Suppose we are given an instance  $\Phi$  of 3-SAT:

- Variables  $x_1, \dots, x_n$  and clauses  $C_1, \dots, C_k$ .

We construct a graph  $G = (V, E)$  consisting of  $3k$  vertices with two types of edges:

- $G$  contains 3 vertices for each clause, one for each literal.

- (Triangle edges) Connect the 3 literals in a clause in a triangle.

- (Cross edges) Connect literals to their negations.

Example:

- For each variable  $x_i$ , we define nodes  $v_i$  and  $\bar{v}_i$  corresponding to  $x_i$  and its negation  $\neg x_i$ .

- We also let  $T$ ,  $F$ , and  $B$  form a triangle (see figure below)

$$\Phi = (\neg z_i \vee z_1 \vee z_2), (\neg z_i \vee z_1 \vee z_3), (\neg z_i \vee z_2 \vee z_4), (\neg z_i \vee z_3 \vee z_4)$$

- A node  $v$  is labeled with “ $\neg$ ” with its two incoming edges from nodes  $u$  and  $w$ : We need to have  $v = x_u \vee x_w$ . We add the following clauses:

$$(\neg x_v \vee x_u \vee x_w), (\neg x_u \vee x_v), \text{ and } (\neg x_w \vee x_v).$$

- A source  $v$  labeled with a fixed value 1: Add a clause  $(x_v)$  with a single literal, forcing the variable  $x_v$  to take the designated value 1.

- A source  $v$  labeled with a fixed value 0: Add a clause  $(\neg x_v)$  with a single literal, forcing the variable  $x_v$  to take the designated value 0.

- The output node  $v$ : Add the single-literal clause  $(x_o)$ , which requires that  $v$  take the value 1.

We then claim that all the clauses we have constructed for 3-SAT are satisfiable iff the circuit  $K$  can be satisfied:

- Since free sources in  $K$  correspond to Boolean variables but do not correspond to any clauses, we are free to choose values for the Boolean variables corresponding to these free sources

- The clauses constructed for each internal node  $v$  guarantee that the Boolean variable  $x_v$  has the same value as the outgoing edge in  $K$  given a certain assignment on the free sources

- The output value also has to be 1 by the single-literal clause  $(x_o)$

Notice that our goal was to create an instance of 3-SAT where all clauses have exactly 3 literals, while in the instance we constructed, some clauses have lengths of 1 or 2. So we need to convert this instance of SAT to an equivalent instance in which each clause has exactly three literals.

To do this:

- Create four new variables:  
$$z_1, z_2, z_3, z_4,$$

- And the four clauses:

$$(\neg z_i \vee z_1 \vee z_2), (\neg z_i \vee z_1 \vee z_3), (\neg z_i \vee z_2 \vee z_4), (\neg z_i \vee z_3 \vee z_4)$$

for each of  $i = 1$  and  $i = 2$ . In order for all four clauses to be true, we must have  $z_i = 0$  for  $i = 1$  and  $i = 2$ .

Then:

- $(t) \Rightarrow (t \vee z_1 \vee z_2)$
- $(t \vee t') \Rightarrow (t \vee t' \vee z_1)$   $\square$

**Proposition 3.** SAT is NP-complete.

*Proof.* This is easy because each instance of 3-SAT is an instance of SAT.  $\square$

## 4 Vertex cover

**Definition 5.** Given a graph  $G = (V, E)$ , we say that a set of vertices  $S \subseteq V$  is a *vertex cover* of  $G$  if for every edge  $e \in E$ , at least one vertex of  $e$  is in  $S$ .

**Remark 4.** By default, graphs are *undirected* in this topic.

**Remark 5.** It is easy to find large vertex covers in a graph (for example, the whole vertex set is one); the hard part is to find small ones.

**Problem 5 (VERTEX-COVER).** Given a graph  $G$  and an integer  $k$ , does  $G$  contain a vertex cover of size at most  $k$ ?

**Remark 6.** The “optimization” version of the above problem is to find the vertex cover with the minimum size.

**Proposition 7.** IND-SET is NP-complete.

*Proof.* Proof of VERTEX-COVER  $\in$  NP is omitted. To prove that it's NP-complete, we reduce IND-SET to VERTEX-COVER:

- Given an instance  $(G, k)$  of IND-SET, we construct an instance  $(G, n - k)$  of IND-SET, where  $n$  is the number of vertices of  $G$ .

- Then, by Proposition 10,  $G$  has an independent set of size  $\geq k$  if and only if  $G$  has a vertex cover of size  $\leq n - k$ .  $\square$

**Proposition 8.** It is easy to find large vertex covers in a graph (for example, the whole vertex set is one); the hard part is to find small ones.

**Problem 5 (VERTEX-COVER).** Given a graph  $G$  and an integer  $k$ , does  $G$  contain a vertex cover of size at most  $k$ ?

**Remark 9.** The “optimization” version of the problem is to find a vertex cover of the