Instructor: 蔡文錦

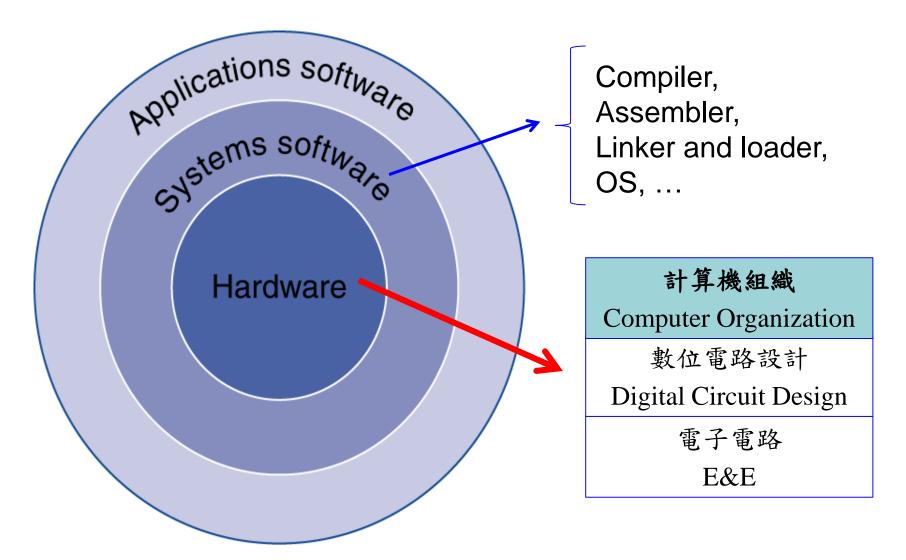
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Course outline

Ch 1: Computer Abstractions and Technology

Ch 2: Instructions: Language of the Computer

Ch 3: Arithmetic for Computers

Ch 4: The processor

Ch 5: Memory Hierarchy

Ch 6: Multiprocessor

Programming Homeworks

- 1 hw (using MIPS assembly language)
- 1 term project with 3 stages (Using Verilog)
- 1 member per team.

Exam/Quiz schedule

July

Mon	Tue	Wed	Thu	Fri
	7/4	5		
10 (Q1)	11	12(Q2)		
17 (M)	18	19		
24(Q3)	25	26(Q4)		
31 (M)	8/1	2		
7(Q5)	8 (F)	9		

Aug.

Course material

- Text Book: "Computer Organization & Design, The Hardware/Software Interface," David A. Patterson and John L. Hennessy, MORGAN KAUFMANN, 5th edition.
- Lecture handout (including this course outline)
 - Download from new E3 (http://e3new.nctu.edu.tw)
- Project assignment & announcement
 - on new E3 platform

Grading1

- 3 exams: 60%

– Quizzes: 20%

Programming homework: 20%

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