

Mario J. Badr

CONTACT INFORMATION	The Edward S. Rogers Department of Electrical and Computer Engineering Room 1107, Sandford Fleming Bldg. 10 King's College Road, Toronto, ON M5S 3G4 Canada University of Toronto	
RESEARCH INTERESTS	Many/multi-core architectures, cache coherence, interconnection networks, application modelling, design space exploration.	
INDUSTRY EXPERIENCE	Qualcomm Research Silicon Valley <i>Intern (C++, OpenCL, Hexagon, CMake)</i> Used the Multicore Asynchronous Runtime Environment (MARE) to develop a heterogeneous application with multiple domain-specific kernels. Provided feedback to the MARE team on performance issues and bottlenecks.	Santa Clara, California, USA <i>May, 2015 - August, 2015</i>
	Environment Canada <i>Intern (Java, XML)</i> Implemented new features and bug fixes for the NinJo workstation, a tool for meteorologists. Notable projects include upgrading the visualization framework for lightning strikes to be faster and use significantly less memory, incorporating storm cell data for Canada, and helping to create a configurable view of weather data for a given storm cell.	Toronto, Ontario, Canada <i>June, 2009 - August, 2010</i>
AWARDS	Roberto Padovani Intern Scholarship - \$5,000 Awarded to seven Qualcomm Research interns across the globe for outstanding technical contributions made during their internship.	<i>2015</i>
	Thomas Noakes & Queen Elizabeth II Graduate Scholarship - \$15,000 Awarded for excellence in science and technology.	<i>2015</i>
	TA Teaching Excellence Award - \$200 Awarded to four recipients across the three University of Toronto campuses based on nominations, references, and teaching philosophy.	<i>2015</i>
PEER-REVIEWED PUBLICATIONS	Joshua San Miguel, Mario Badr , and Natalie Enright Jerger. <i>Load Value Approximation</i> . In Proceedings of the International Symposium on Microarchitecture (MICRO), to appear, December 2014. (acceptance rate: 19%)	
	Mario Badr and Natalie Enright Jerger. <i>SynFull: Synthetic Traffic Models Capturing a Full Range of Cache Coherence Behaviour</i> . In Proceedings of the International Symposium on Computer Architecture (ISCA), June 2014. (acceptance rate: 18%)	
WORKSHOPS, PRESENTATIONS, AND POSTERS	Workshop Paper Ajaykumar Kannan, Mario Badr , Parisa Khadem Hamedani and Natalie Enright Jerger. <i>Offloading to the GPU: An Objective Approach</i> . In Proceedings of the International Workshop on Parallelism in Mobile Platforms (PRISM), June 2015.	
	Poster Joshua San Miguel, Mario Badr , and Natalie Enright Jerger. <i>Load Value Approximation</i> . At the International Symposium on Microarchitecture (MICRO), December 2014.	

Presentation

Mario Badr and Natalie Enright Jerger. *SynFull: Traffic Models Capturing Cache Coherence Behaviour*. At the International Symposium on Computer Architecture (ISCA), June 2014.

Presentation

Mario Badr and Natalie Enright Jerger. *SynFull: Synthetic Traffic Models That Capture Cache Coherent Behaviour*. At the University of Toronto ECE Graduate Symposium (Connections), May 2014. Awarded *Best Oral Presentation*.

Presentation

Mario Badr and Natalie Enright Jerger. *Realistic Synthetic Traffic* At the University of Toronto ECE Graduate Symposium (Connections), May 2012.

TEACHING EXPERIENCE

Engineering Strategies and Practice

1st Year Undergraduates

Teaching Assistant

Tutorials

Supervised students through the design process for a given project and provided feedback on written design documents, with a focus on stimulating an engineering methodology to design.

Computer Fundamentals

1st Year Undergraduates

Teaching Assistant

Tutorials, Computer Lab

Taught the fundamentals of computer programming in C, including data structures, recursion, and sorting algorithms.

Programming Fundamentals

2nd Year Undergraduates

Teaching Assistant

Tutorials

Taught the fundamentals of C++ and object-oriented programming, including value versus reference semantics and memory management on the stack and heap.

Communication and Design

2nd Year Undergraduates

Teaching Assistant

Tutorials, Computer Lab

Mentored student groups in the implementation of an application that visualizes and finds routes in a city map. Developed six new tutorials in 2017 to help students with software engineering.

Computer Organization

3rd Year Undergraduates

Teaching Assistant

Computer Lab

Supervised and helped students implement a simple von Neumann processor using verilog.

Computer Architecture

4th Year Undergraduates & Graduates

Teaching Assistant

Computer Lab

Taught computer architecture concepts, including caches, pipelining, out-of-order cores, and cache coherence.

UNIVERSITY SERVICE

Appointed Graduate Representative

2015-2016

Executive Committee of Faculty Council - Faculty of Applied Science & Engineering

Graduate Representative

2015-2016

Faculty Council - Faculty of Applied Science & Engineering

President

2015-2016

Electrical and Computer Engineering Graduate Students' Society

Bargaining Support Committee Member

January - April 2014

Canadian Union of Public Employees

PROFESSIONAL DEVELOPMENT	Department Steward Canadian Union of Public Employees	2013-2014
	Social Event Coordinator Electrical and Computer Engineering Graduate Students' Society	2012-2013
	Teaching in Higher Education One semester course Learned more about teaching theories and styles and was observed by peers while teaching a tutorial on Computer Architecture. Also developed a course syllabus for a fictitious course.	2015
	Prospective Professors in Training <i>Thirteen Seminars, One semester course</i> Began preparations for becoming a professor in academia, attending several seminars and preparing a preliminary teaching dossier and research statement. Also completed a course on Teaching Engineering in Higher Education.	2014-2015
	Mini-MBA <i>Ten Classes</i> Gained an understanding of fundamental business concepts and applied what I learned in a group-based case competition.	2015
	Oral Presentation Skills <i>Five Classes</i> Examined presentation structure and the use of visual aids. Exchanged feedback with peers and the instructor on individual oral presentations.	2014
	Prewriting Strategies for Developing and Organizing Your Ideas <i>Four Classes</i> Learned several new strategies for developing and organizing ideas before (and during) the writing process.	2014
	NSERC Proposal Workshop <i>Three Classes</i> Examined features of good and bad proposal writing, and exchanged feedback with peers on our own NSERC proposals.	2014
	Teaching Fundamentals Certificate <i>Six Workshops</i> Improved my teaching skills with workshops on pedagogy, research, academic integrity, and students in difficulty.	2012-2013
	Ph.D., Computer Engineering <i>University of Toronto</i> Dissertation: "Novel Evaluation Methodologies for Future Architectures" Advisor: Natalie Enright Jerger	September, 2013 - Present <i>Toronto, Ontario, Canada</i>
EDUCATION	M.A.Sc, Computer Engineering <i>University of Toronto</i> Thesis: "Synthetic Traffic Models that Capture Cache Coherence Behaviour" Advisor: Natalie Enright Jerger	January 2014 <i>Toronto, Ontario, Canada</i>
	B.A.Sc, Electrical Engineering <i>University of Toronto</i>	May 2011 <i>Toronto, Ontario, Canada</i>