

8-bit PIC® Microcontroller Peripheral Integration

Quick Reference Guide

Product Family	Pin Count	Program Flash Memory (KB)	RAM (B)	Data EEPROM (B)	Peripheral Function Focus																Intelligent Analog		Waveform Control				Timing and Measurements ⁽¹⁾				Logic and Math		Safety and Monitoring		Communications		User Interface	Low Power and System Flexibility					
					ADC (# of bits)	Comp	HSComp	DAC (# of bits)	OPA	PRG	ShapeComp	ZCD	CCP/ECCP	10-bit PWM	16-bit PWM	COG	CWG	NCO	DSM	AngTMR	HLT (8-bit)	16-bit PWM (16-bit)	NCO (20-bit)	SMT (24-bit)	RTCC	TEMP/TS	CLC	MULT	Math/ACC	CRC/SCAN	HLT	WWDT	EUSART/AUSART	UART with Protocols	I²C/SPI	USB with ACT	LIN Capable	mTouch® Sensing	HCD	LCD	PPS	IDLE/DOZE/PMD	DMA/AVI
PIC10(L)F3XX	6	384–896	64	HEF	8									✓																													
PIC12LF1552	8	3.5	256	HEF	10																																						
PIC16LF155X/6X	14–20	7–14	1024	HEF	10 ⁽²⁾										✓																												
PIC16(L)F145X	14–20	14	1024	HEF	10	✓																																					
PIC1X(L)F157X	8–20	1.75–14	1024	HEF	10	✓		5																																			
PIC16(L)F153XX	8–48	3.5–28	2048	HEF	10	✓		5						✓	✓	4																											
PIC1X(H)V752/53	8–14	1.75–3.5	128	–	10		✓	5/9	✓		✓			✓																													
PIC1X(L)F1612/3	8–14	3.5	256	HEF	10	✓		8						✓	✓																												
PIC16(L)F161X	14–20	7–14	1024	HEF	10	✓		8						✓	✓																												
PIC16(L)F170X	14–20	3.5–14	1024	HEF	10		✓	5/8	✓					✓	✓	✓	✓																										
PIC16(L)F171X	28–40	7–28	2048	HEF	10		✓	5/8	✓					✓	✓	✓	✓																										
PIC16(L)F176X/7X	14–40	7–28	2048	HEF	10		✓	5/10	✓	✓	✓			✓	✓	✓	✓	✓																									
PIC16(L)F183XX	8–20	3.5–14	2048	256	10	✓		5						✓	✓																												
PIC16(L)F184XX	14–28	7–28	2048	256	12 ⁽³⁾	✓		5						✓	✓	✓																											
PIC16(L)F188XX	28–40	7–56	4096	256	10 ⁽³⁾	✓		5						✓	✓	✓																											
PIC16(L)F191XX	28–64	14–56	4096	256	12 ⁽³⁾	✓		5						✓	✓	✓																											
PIC18(L)FXXXK40	28–64	16–128	3728	256–1K	10 ⁽³⁾	✓		5						✓	✓	✓																											
PIC18(L)FXXXK42	28–48	16–128	8192	256–1K	12 ⁽³⁾	✓		5						✓	✓	4																											
PIC18(L)FXXJ94	64–100	32–128	4096	–	12	✓								✓																													
PIC18(L)FXXK83	28	32–64	4096	1K	12 ⁽³⁾	✓		5						✓	✓	✓																											
PIC18FXXQ10	28–40	128	3728	1024	10 ⁽³⁾	✓		5						✓	✓	✓																											

INTELLIGENT ANALOG: Sensor Interfacing and Signal Conditioning		LOGIC AND MATH: Customizable Logic and Math Functions
ADC: Analog-to-Digital Converter	General purpose 8-/10-/12-bit ADC	CLC: Configurable Logic Cell 1. Integrated combinational and sequential logic 2. Customer interconnection and re-routing of digital peripherals
ADC³/ADCC: Analog-to-Digital Converter with Computation	General purpose 10-/12-bit ADC with automated analog signal analysis (ex. oversampling, averaging, etc.)	MULT: Hardware Multiplier MULTIPLY function of two 8-bit values with 16-bit result
Comp: Comparator	General purpose rail-to-rail comparator	MathACC: Math Accelerator 1. MULTIPLY, ADD, ACCUMULATE functions of 8-/16-bit values with 35-bit result 2. Calculates a 16-bit PID function based on configurable K _P , K _I , K _D constants with a 34-bit result
DAC: Digital-to-Analog Converter	Programmable voltage reference with multiple internal and external connections	SAFETY AND MONITORING: Hardware Monitoring and Fault Detection
HC I/O: High-Current I/O	Up to 50 mA or 100 mA current drive on select I/O pins	CRC/SCAN: Cyclical Redundancy Check with Memory Scan 1. Automatically calculates CRC checksum of Program/DataEE memory for NVM integrity 2. General purpose 16-bit CRC for use with memory and communications data
HSComp: High-Speed Comparator	General purpose rail-to-rail comparator with < 50 ns response time	HLT: Hardware Limit Timer and 8-bit Timer/Counter 1. Hardware monitoring for missed periodic events and fault detection of external hardware 2. General purpose 8-bit timer/counter with external reset capabilities
OPA: Operational Amplifier	General purpose op amp for internal and external signal source conditioning	WWDT: Windowed Watch Dog Timer System supervisory circuit that generates a reset when software timing anomalies are detected within a configurable critical window
PRG: Programmable Ramp Generator	Analog ramp generator (with slope compensation) for current/voltage mode power supplies	COMMUNICATIONS: General, Industrial, Lighting and Automotive
SlopeComp: Slope Compensation	Slope compensation for Peak Current Mode power supplies	ACT: Active Clock Tuning for Crystal-Free USB 1. Auto-tuning of internal oscillator when connected to USB host (eliminates need for external crystal) 2. Tunes internal oscillator to match accuracy of external clock source
VREF: Voltage Reference	Stable fixed voltage reference for use with integrated analog peripherals	CAN: Controller Area Network Industrial- and automotive-centric communication bus
ZCD: Zero Cross Detect	AC high-voltage zero-crossing detection for simplifying TRIAC control, synchronized switching control and timing	LIN: Local Interconnect Network 1. Industrial- and automotive-centric communication bus 2. Support for LIN when using the EUSART
WAVEFORM CONTROL: PWM Drive and Waveform Generation		EUSART/AUSART: Enhanced/Addressable Universal Asynchronous Receiver Transceiver 1. General purpose serial communications 2. Support for LIN when using the EUSART
CCP/ECCP: (Enhanced) Capture Compare PWM	1. CCP/ECCP: 10-bit PWM control with 16-bit capture and compare 2. ECCP: Addition of auto shutdown control	I²C: Inter-Integrated Circuit General purpose 2-wire serial communications
COG: Complementary Output Generator	Automated complementary output with control of key parameters such as programmable rising/falling edge events, polarity, phase, precision dead-band, blanking and auto shutdown	SPI: Serial Peripheral Interface General purpose 4-wire serial communications
CWG: Complementary Waveform Generator	Automated complementary output with control of key parameters such as dead-band and auto shutdown	UART: Universal Asynchronous Receiver Transmitter Supports LIN master and slave, DMX, DALI and device protocols
DSM: Data Signal Modulator	1. Modulates up to two carrier signals with digital data to create custom carrier synchronized output waveforms 2. LED dimming engine functionality via interconnection with 10-/16-bit PWM, DSM and op amp	USB: Universal Serial Bus Support for full-speed USB 2.0 device profiles
NCO: Numerically Controlled Oscillator and 16-/20-bit Timer/Counter	1. Precision linear frequency generator (@ 50% duty cycle) with 0.0001% step size of source input clock frequency 2. General purpose 16-/20-bit timer/counter	USER INTERFACE: Capacitive Touch Sensing and LCD Control
PWM: Pulse Width Modulation	General purpose 10-bit PWM control	HCVD: Hardware Capacitive Voltage Divider Simplifies implementation and reduces overhead of mTouch sensing applications
16-bit PWM: Standalone 16-bit PWM and 16-bit Timer/Counter	1. High-resolution 16-bit PWM with edge- and center-aligned modes 2. General purpose 16-bit timer/counter	LCD: Liquid Crystal Display Highly integrated segmented LCD controller
TIMING AND MEASUREMENTS: Signal Measurement with Timing and Counter Control		mTouch: Microchip Proprietary Capacitive Touch Technology 1. Capacitive sensing for touch buttons and sliders 2. Capacitive sensing for system measurements and detection (ex. water level, intrusion detection, etc.)
AngTMR: Angular Timer	Phase angle timer for measurement and control of rotational and periodic events (ex. motor, AC mains, TRIAC, etc.)	LOW POWER AND SYSTEM FLEXIBILITY: XLP Low-Power Technology, Peripheral and Interconnects
HLT: Hardware Limit Timer and 8-bit Timer/Counter	1. Hardware monitoring for missed periodic events and fault detection 2. General purpose 8-bit timer/counter with external reset capabilities	DIA: Device Information Area Dedicated memory area for data storage of temp sensor factory calibration values, factory ID and FVR values for ADC and COMP
NCO: Numerically Controller Oscillator and 16-/20-bit Timer/Counter	1. Precision linear frequency generator (@ 50% duty cycle) with 0.0001% step size of source input clock frequency 2. General purpose 16-/20-bit timer/counter	DMA: Direct Memory Access Moves data between memories and peripherals without CPU overhead, improving overall system performance and efficiency
RTCC: Real-Time Clock/Calendar	Maintains accurate clock and calendar timing with external 32.768 kHz crystal	DOZE: Power Saving Mode Ability to run the CPU core slower than the system clock used by the internal peripherals
SMT: 24-bit Signal Measurement Timer and 24-bit Timer/Counter	1. Accurate measurement of any digital signal including period, duty cycle, time of flight; instantaneous vs. average measurements 2. General purpose 24-bit timer/counter	HEF: High-Endurance Flash 128B Non-volatile data storage with high-endurance 100k E/W cycles
TEMP: Temperature Indicator	Provides relative temperature measurements utilizing the ADC	IDLE: Power Saving Mode Ability to put the CPU core to sleep while the internal peripherals continue to operate from the system clock
TS: Temperature Sensor	Provides linear relative temperature measurements utilizing the ADC with two factory-calibrated reference values	MAP: Memory Access Partition Customizable Flash partitioning with bootloader write protection option
8-/16-bit Timer	General purpose 8-/16-bit timer/counter	PMD: Peripheral Module Disable Peripheral power disable hardware to minimize power consumption of unused peripherals
16-bit PWM: Standalone 16-bit PWM and 16-bit Timer/Counter	1. High-resolution 16-bit PWM with edge- and center-aligned modes 2. General purpose 16-bit timer/counter	PPS: Peripheral Pin Select I/O pin remapping of digital peripherals for greater design flexibility and optimized board layout
Learn more about 8-bit PIC Microcontrollers at www.microchip.com/8bit .		VI: Vectored Interrupts Offers faster and more predictable interrupt response times, with lower software overhead
Learn more about Core Independent Peripherals (CIP) at www.microchip.com/CIP .		XLP: eXtreme Low Power Technology XLP technology devices with extreme low-power operation modes for battery/low-power applications



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