

# SN54LS386A, SN74LS386A QUADRUPLE 2-INPUT EXCLUSIVE-OR GATES

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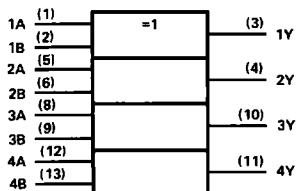
- Electrically Identical to SN54LS86A/SN74LS86A
- Mechanically Identical to SN54L86/SN74L86
- Total Average Propagation Delay Times . . . 10 ns
- Typical Total Power Dissipation . . . 30.5 mW

FUNCTION TABLE  
(EACH GATE)

INPUTS		OUTPUT
A	B	
L	L	L
L	H	H
H	L	H
H	H	L

H = high level  
L = low level

logic symbol†

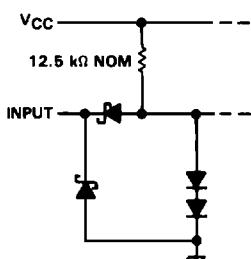


†This symbol is in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, N, and W packages

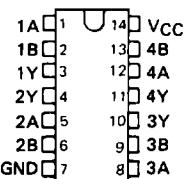
schematics of inputs and outputs

EQUIVALENT OF EACH INPUT



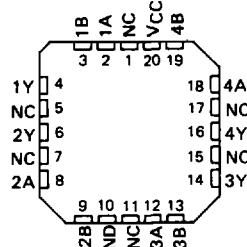
SN54LS386A . . . J OR W PACKAGE  
SN74LS386A . . . D OR N PACKAGE

(TOP VIEW)



SN54LS386A . . . FK PACKAGE

(TOP VIEW)



NC ~ No internal connection

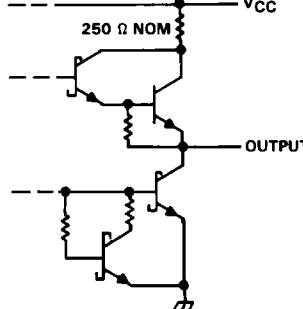
logic diagram (each gate)



positive logic

$$Y = A \oplus B = \bar{A}B + A\bar{B}$$

TYPICAL OF ALL OUTPUTS



PRODUCTION DATA documents contain information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

TEXAS  
INSTRUMENTS

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## **SN54LS386A, SN74LS386A QUADRUPLE 2-INPUT EXCLUSIVE-OR GATES**

**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)**

NOTE 1: Voltage values are with respect to network ground terminal.

#### **recommended operating conditions**

	SN54LS386A			SN74LS386A			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
Supply voltage, V <sub>CC</sub>	4.5	5	5.5	4.75	5	5.25	V
High-level output current, I <sub>OH</sub>			-400			-400	μA
Low-level output current, I <sub>OL</sub>			4			8	mA
Operating free-air temperature, T <sub>A</sub>	-55	125	0	70			°C

2

ttl Devices

**electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)**

PARAMETER	TEST CONDITIONS <sup>†</sup>	SN54LS386A			SN74LS386A			UNIT	
		MIN	TYP <sup>‡</sup>	MAX	MIN	TYP <sup>‡</sup>	MAX		
V <sub>IH</sub>	High-level input voltage			2		2		V	
V <sub>IL</sub>	Low-level input voltage			0.7		0.8		V	
V <sub>IK</sub>	Input clamp voltage	V <sub>CC</sub> = MIN,	I <sub>I</sub> = -18 mA		-1.5		-1.5	V	
V <sub>OH</sub>	High-level output voltage	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V, V <sub>IL</sub> = V <sub>IL</sub> max, I <sub>OH</sub> = -400 $\mu$ A	2.5	3.4	2.7	3.4	V	
V <sub>OL</sub>	Low-level output voltage	V <sub>CC</sub> = MIN V <sub>IH</sub> = 2 V, V <sub>IL</sub> = V <sub>IL</sub> max	I <sub>OL</sub> = 4 mA	0.25	0.4	0.25	0.4	V	
			I <sub>OL</sub> = 8 mA			0.35	0.5		
I <sub>I</sub>	Input current at maximum input voltage	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 7 V		0.2		0.2	mA	
I <sub>IH</sub>	High-level input current	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 2.7 V		40		40	$\mu$ A	
I <sub>IL</sub>	Low-level input current	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 0.4 V		-0.8		-0.8	mA	
I <sub>OS</sub>	Short-circuit output current <sup>§</sup>	V <sub>CC</sub> = MAX		-20	-100	-20	-100	mA	
I <sub>CC</sub>	Supply current	V <sub>CC</sub> = MAX,	See Note 2		6.1	10	6.1	10	mA

<sup>f</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

<sup>†</sup>All typical values are at  $V_{DD} = 5\text{ V}$ ,  $T_A = 25^\circ\text{C}$ .

<sup>†</sup>All typical values are at  $V_{CC} = 5$  V,  $|I_A| = 25$  C.

NOTE 3:  $I_{OL}$  is measured with the inputs grounded and the outputs open.

switching characteristics,  $V_{CC} = 5$  V,  $T_A = 25^\circ\text{C}$

PARAMETER	FROM (INPUT)	TEST CONDITIONS		MIN	TYP	MAX	UNIT
t <sub>PLH</sub>	A or B	Other input low	C <sub>L</sub> = 15 pF, R <sub>L</sub> = 2 kΩ,	12	23		ns
t <sub>PHL</sub>				10	17		
t <sub>PLH</sub>	A or B	Other input high	See Note 3	20	30		ns
t <sub>PHL</sub>				13	22		

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.