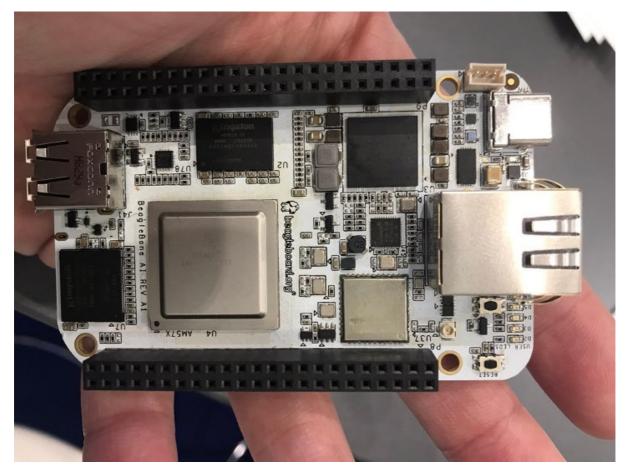
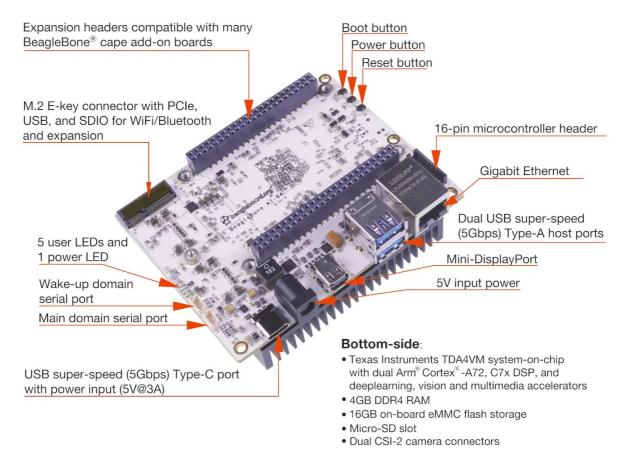
# **BeagleBoard.org BeagleBone AI Devices**



BeagleBoard AI models are <u>certified Open Source Hardware</u> (OSHW). There are two available models of the <u>BeagleBone AI</u>:

- **BeagleBone AI-64:** This small single-board computer has a Texas Instruments TDA4VM chipset (64-bit ARM <u>arm64</u> architecture).
- **BeagleBone AI (RevA2):** This smaller single-board computer has a Texas Instruments AM5729 chipset (ARMv7 <u>armhf</u> architecture).

## **BeagleBone AI-64**



BeagleBone Al-64 is a single-board computer (SBC) that provides a complete system with development tools for Al/ML. It utilizes the 64-bit Texas Instruments AM5729 SoC and can be expanded with the BeagleBone cape headers, via hundreds of open-source hardware examples and dozens of CoTS expansion options.

#### **Hardware**

- SoC: Texas Instruments Jacinto TDA4VM, 2x ARM Cortex-A72
- Max CPU clock: 2 GHz
- Co-processors: C7x+MMA, 2xC66x, 12xPRU, 6xARM Cortex-R5, PowerVR Rogue 8XE GE8430, H.264 encode, H.264/H.265 decode
- Analog Pins: 7 (3.3V)
- **Digital Pins:** 72 (including 7 analog pins which are digital capable)
- Memory: 4GB LPDDR4
- Storage: 16GB eMMC flash, on-board microSD card slot
- USB: USB 3.0 Type-C 5Gbps, 2x USB 3.0 Type-A Host Ports
- Network: 1 Gigabit Ethernet M.2, E-key w/ PCle/USB/SDIO
- Video: miniDP, MIPI DSI (w/ I2C), Cape add-on boards
- Audio: miniDP, cape/USB add-ons, MikroBUS Shuttle connector
- Camera: 2x MIPI CSI (4 lane)
- Supported Expansion Interfaces: 4x UART, 16-bit LCD interface, 2x I2C, 2x SPI, 8x PWM, 4x Timer, A/D (3.3V), CAN, Pulse Capture, Quadrature Encoder, PRU GPIO, PRU UART, PRU Pulse Capture
- On-board Sensors: On-die processor temperature sensors

#### **Documentation**

#### Vendor

BeagleBoard.org publishes a detailed <u>git repository</u> with all CAD design files as well as a documentation <u>wiki</u>.

## **Community**

The hardware community around all BeagleBone models is very active on the <u>BeagleBoard.org forums</u>. The BeagleBone Google Groups are now <u>archived</u>.

#### **Software**

#### **Bootloader**

BeagleBone models use the <u>U-Boot</u> bootloader.

#### **Operating System**

BeagleBone models run a <u>Debian GNU/Linux</u> derivative. Instructions for flashing and updating the OS <u>are available here</u>.

#### **Connecting to the Device**

The system starts the XFCE desktop environment when it boots. The easiest way to access it is by connecting the device via USB-C.

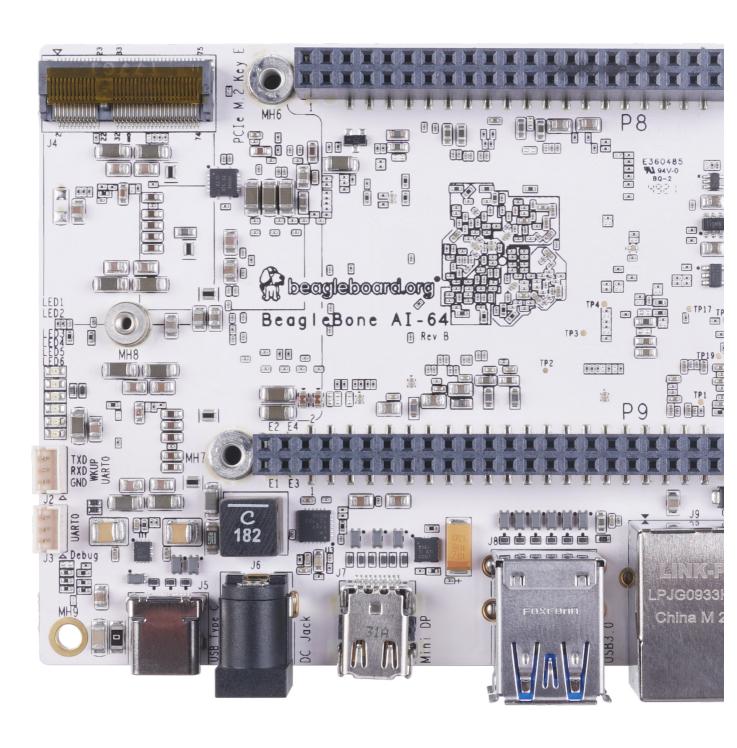
## **Tari Testing & Compatibility**

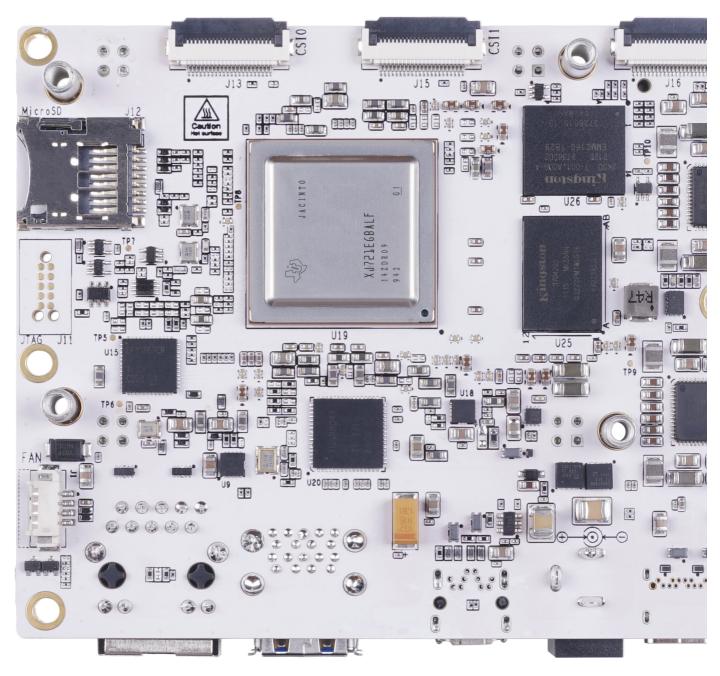
The Tari suite arm64 builds for **Ubuntu** will run on the BeagleBoard Al-64.

Some Debian dependencies must be installed: \* Tor must be installed and the control port 9051 will need to be open and configured in /etc/tor/torrc. \* unzip is required to extract the Tari binaries. \* NOTE The Tari suite start\_all script requires the GNOME desktop environment. This DE is not recommended on this device so the script should be ignored or modified for XFCE.

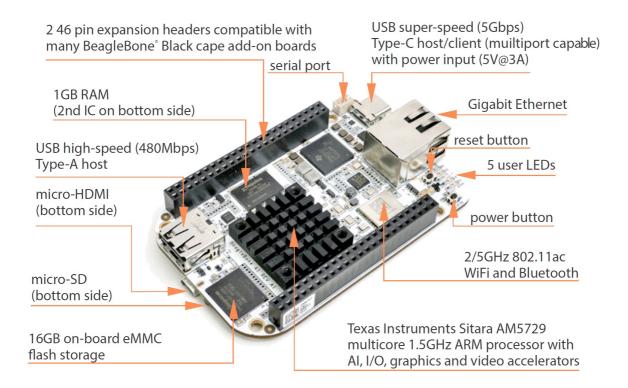
Instructions: \* Download the arm64 build of tari\_suite from the <u>Tari downloads page</u>. \* Verify the SHA256 checksum for the file. \* Copy the ZIP archive to the device and extract the files with unzip. \* Change the permissions on the Tari binaries to be executable. \* Run the Tari binaries via the CLI.

Additional installation instructions are available in the <u>Tari project repositories</u>.





**BeagleBone Al** 



BeagleBone AI is a miniature single-board computer (SBC) based on the Texas Instruments AM5729. The board is compatible with the BeagleBone Black header and most cape accessories. BeagleBone AI also has an SoC with TI C66x digital-signal-processor (DSP) and embedded-vision-engine (EVE) cores supported through an optimized TIDL machine learning OpenCL API with pre-installed tools.

## **Hardware**

- SoC: Texas Instruments Sitara AM5729, 2x ARM Cortex-A15
- Max CPU clock: 1.5 GHz
- Co-processors: 2xC66x, 4xPRU, 4xARM Cortex-M4, 4xEVE, 2xPowerVR SGX544, IVA-HD
- **Analog Pins:** 7 (4-wire touchscreen capable) (3.3V)
- **Digital Pins:** 72 (including 7 analog pins which are digital capable)
- Memory: 1GB DDR3L
- Storage: 16GB eMMC flash, on-board microSD card slot
- USB: USB 3.0 Type-C 5Gbps, USB 2.0 Type-A Host Port
- Network: 1 Gigabit Ethernet, 2.4/5GHz WiFi, Bluetooth 4.2/BLE
- Video: micro HDMI, Cape add-on boards
- Audio: micro HDMI, cape/USB add-ons, Bluetooth
- Camera: None
- Supported Expansion Interfaces: 4x UART, 16-bit LCD interface, 2x I2C, 2x SPI, 8x PWM, 4x Timer, A/D (3.3V), CAN, Pulse Capture, Quadrature Encoder, PRU GPIO, PRU UART, PRU Pulse Capture
- On-board Sensors: On-die processor temperature sensors

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#### **Software**

#### **Bootloader**

BeagleBone models use the <u>U-Boot</u> bootloader.

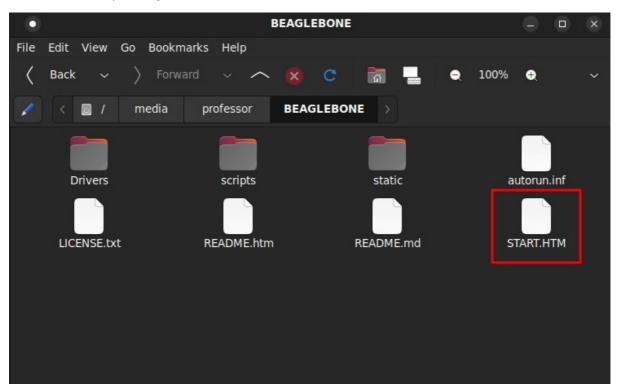
## **Operating System**

BeagleBone models run a <u>Debian GNU/Linux</u> derivative. Instructions for flashing and updating the OS <u>are available here</u>.

## **Connecting to the Device**

The system starts a Web configuration interface when it boots. The easiest way to access it is by <u>connecting the device</u> via USB-C, opening a Web browser, and following the <u>instructions here</u>.

When you connect to the device, you will be presented with the on-board filesystem as in the screenshot below. Open START.HTM in your browser to view instructions for updating the firmware, updating Debian, and other maintenance of the device.



The on-board filesystem for the BeagleBoard Al-64 is very small and you will need to format a microSD card (recommended at least 32GB) to copy and run the Tari binaries.

#### Tari Testing & Compatibility

The Tari project does not provide builds for the armhf architecture, so the Tari suite must be compiled from source on the BeagleBoard Al.

Instructions for compiling from source are available in the <u>Tari project repositories</u>.

Some Debian dependencies also must to be installed: \* Tor must be installed and the control port 9051 will need to be open and configured in /etc/tor/torrc. \* unzip is required to extract the Tari binaries. \* NOTE: The Tari suite start\_all script requires the GNOME desktop environment. A GUI is not recommended on this device so the script

## should be ignored.

