MAVEN SILICON
Centre of Excellence in VLSI

INTERNSHIP PROGRESS REPORT

Grading Index

>90%	>70%	>50%	>30%	>10%	<10%
● "O"	● "A+"	● "A"	● "B+"	● "B"	● "C"
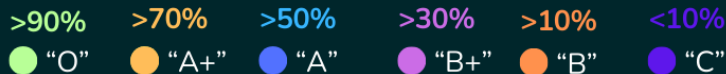
Name

TARUN SAI GAMPALA

Course Name

RISC V RV 32 I RTL design

Date

30/10/2023

Project Test Score (in %)	100
Project Submission Report Score (in %)	100
Grade	O

Note: Feel free to mail us at training_support@maven-silicon.com for any queries