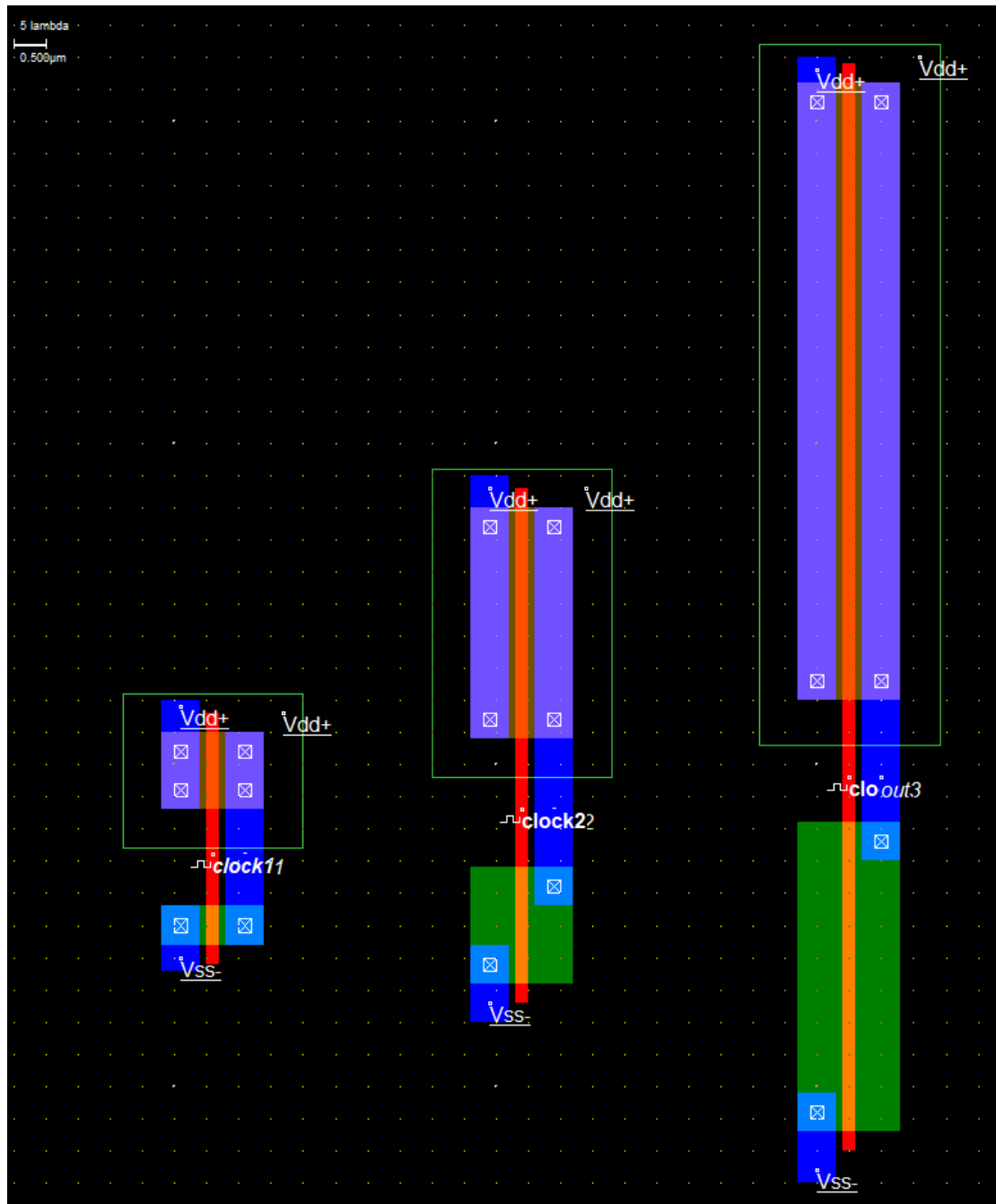
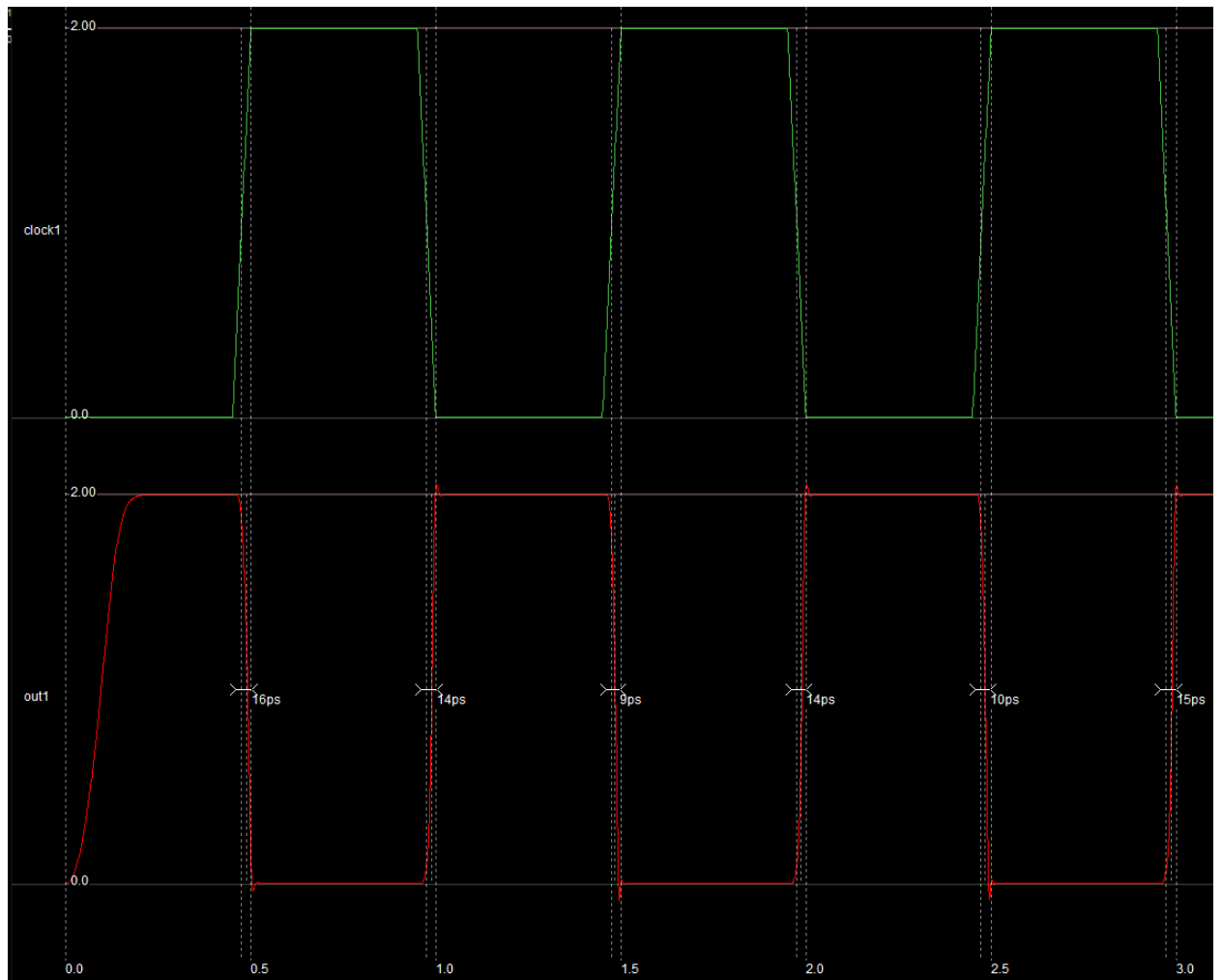


LABORATORY EXERCISE 3B VLSI

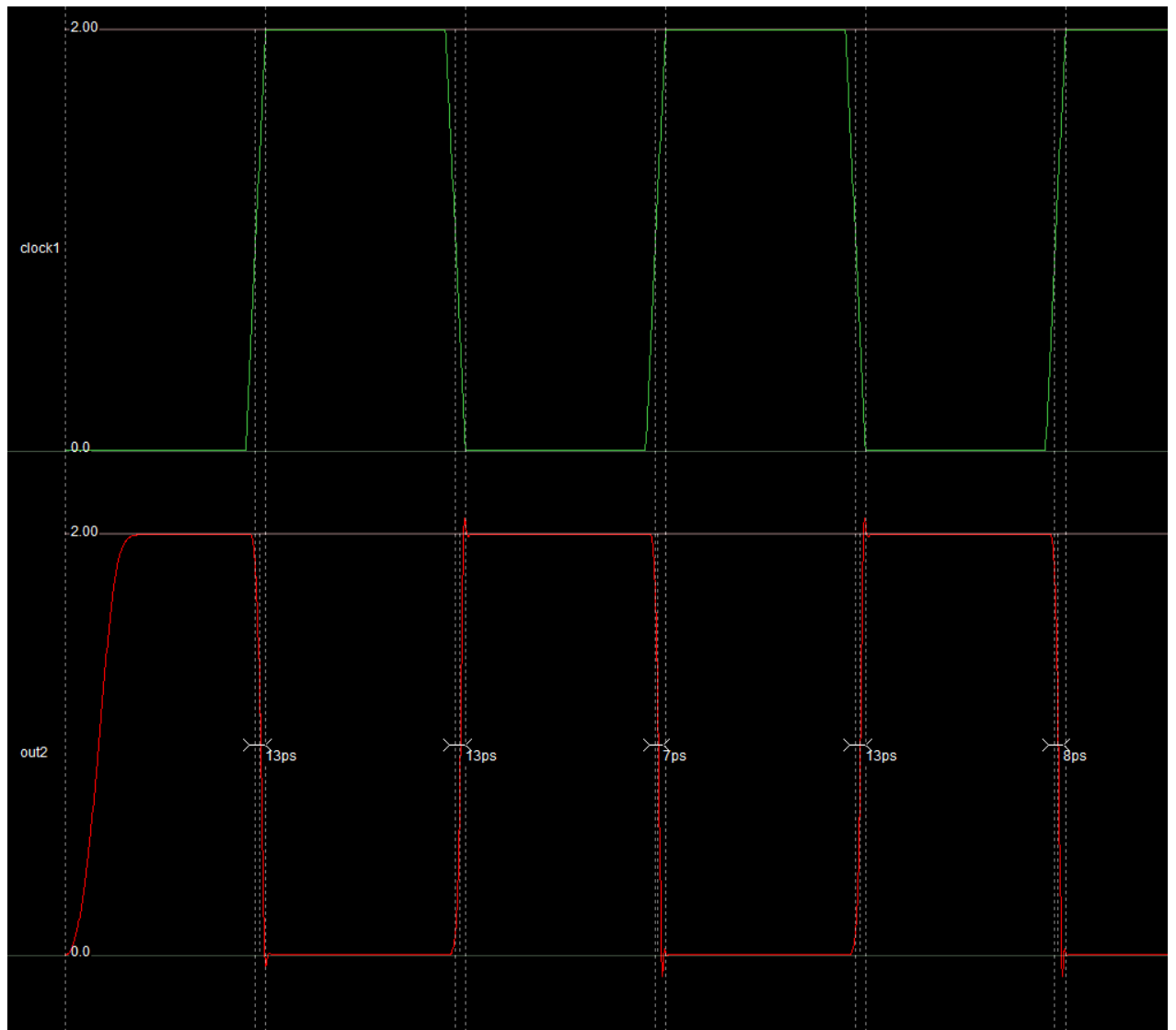
Layout with the requested sizes



k=1



k=3



k=8

