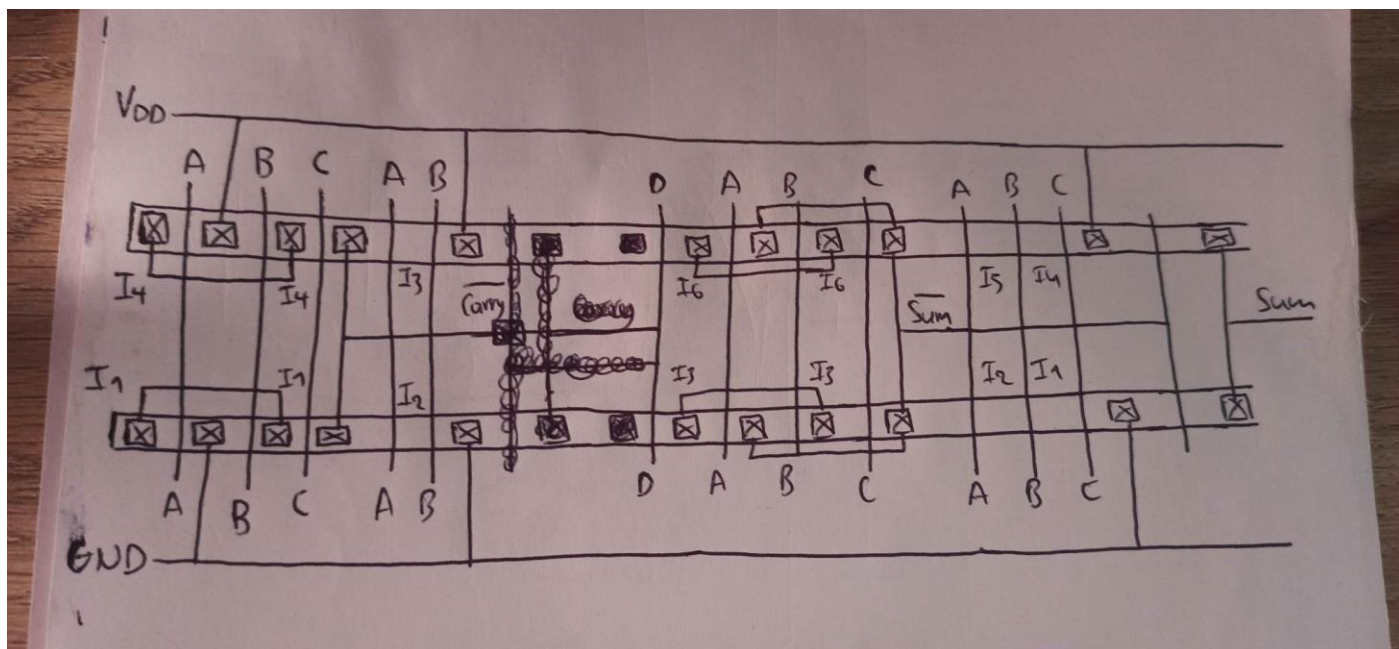
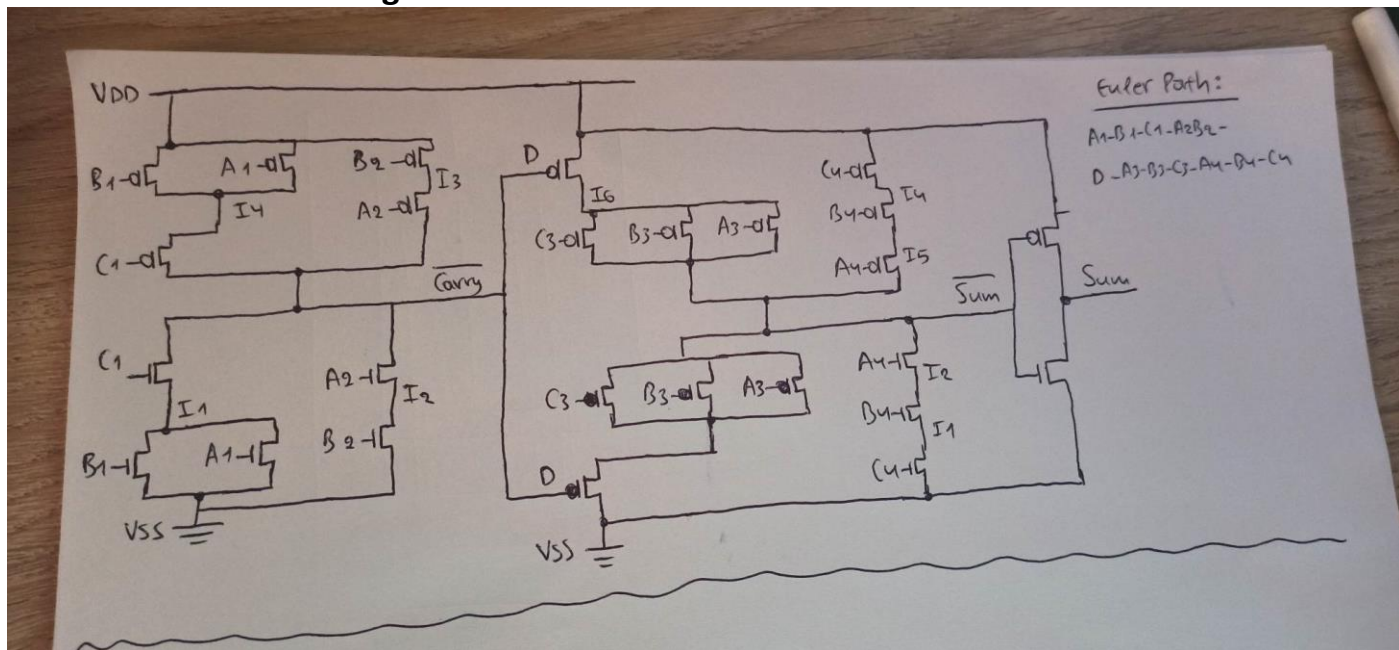


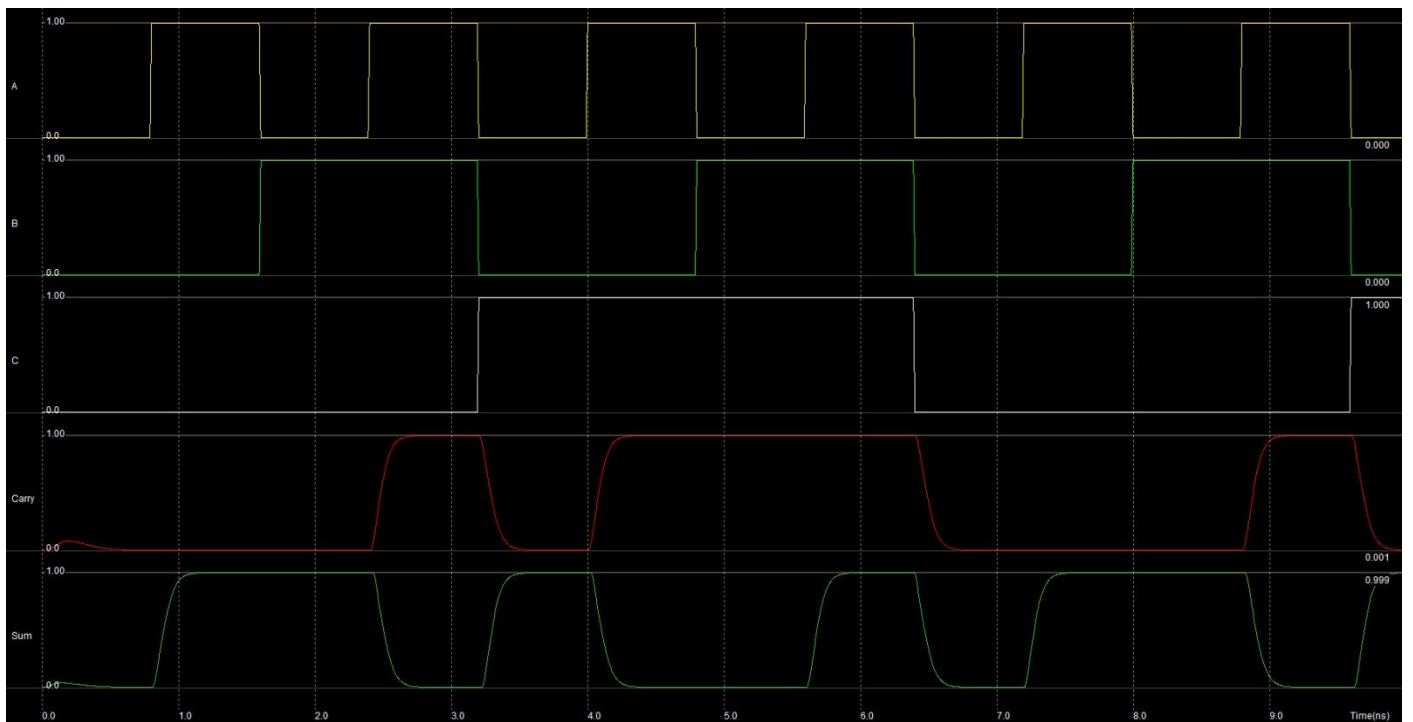
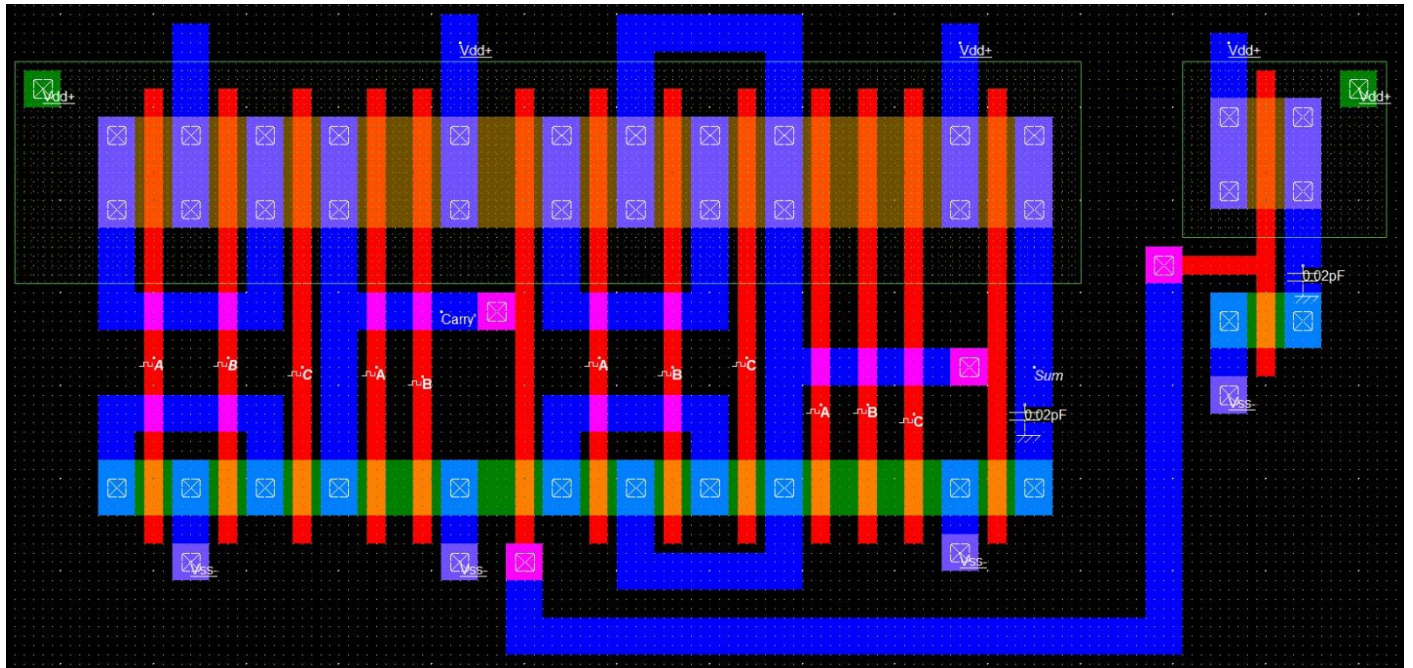
LABORATORY EXERCISE 3 VLSI

1.

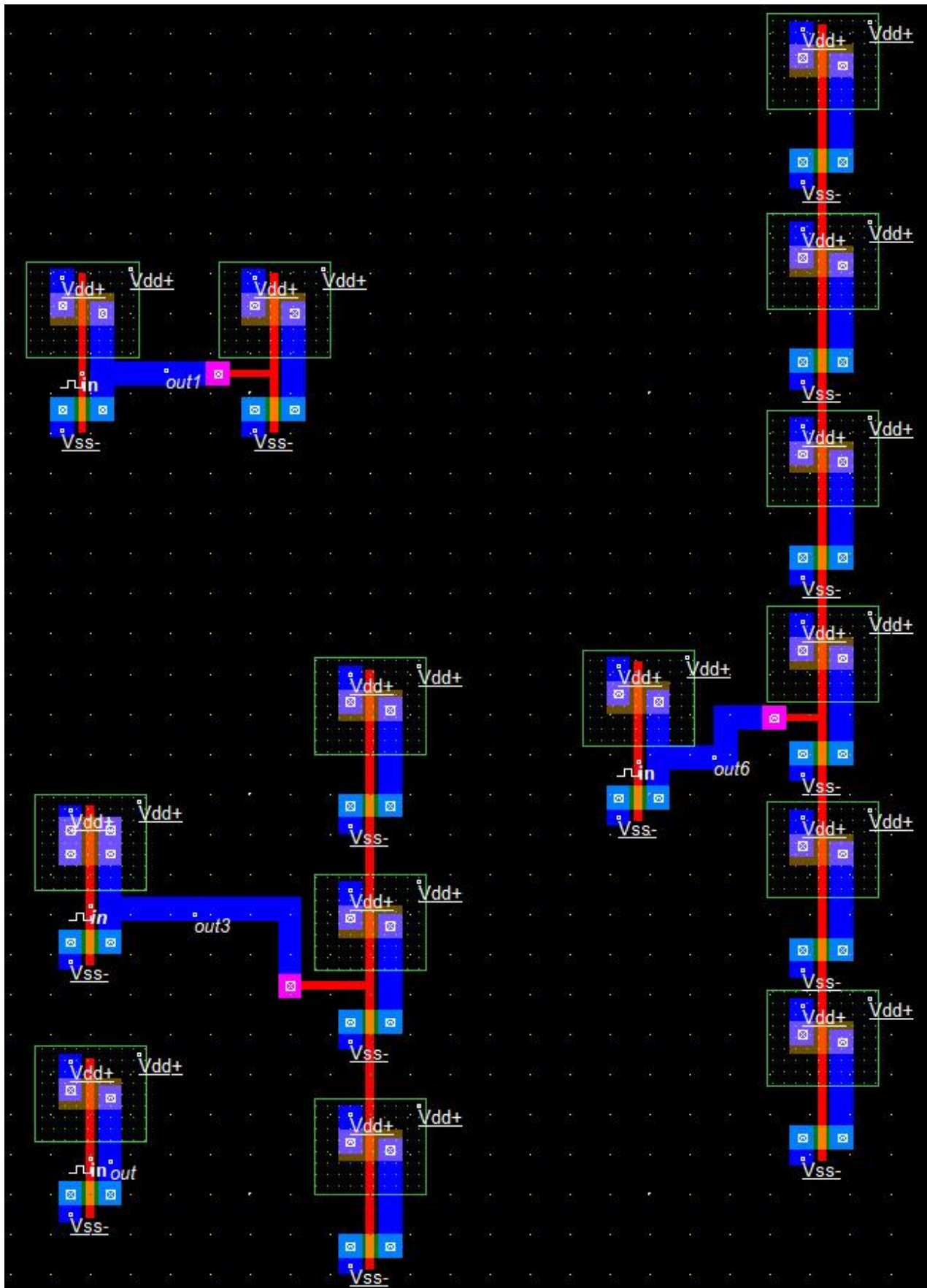
Euler Paths και Stick Diagrams:

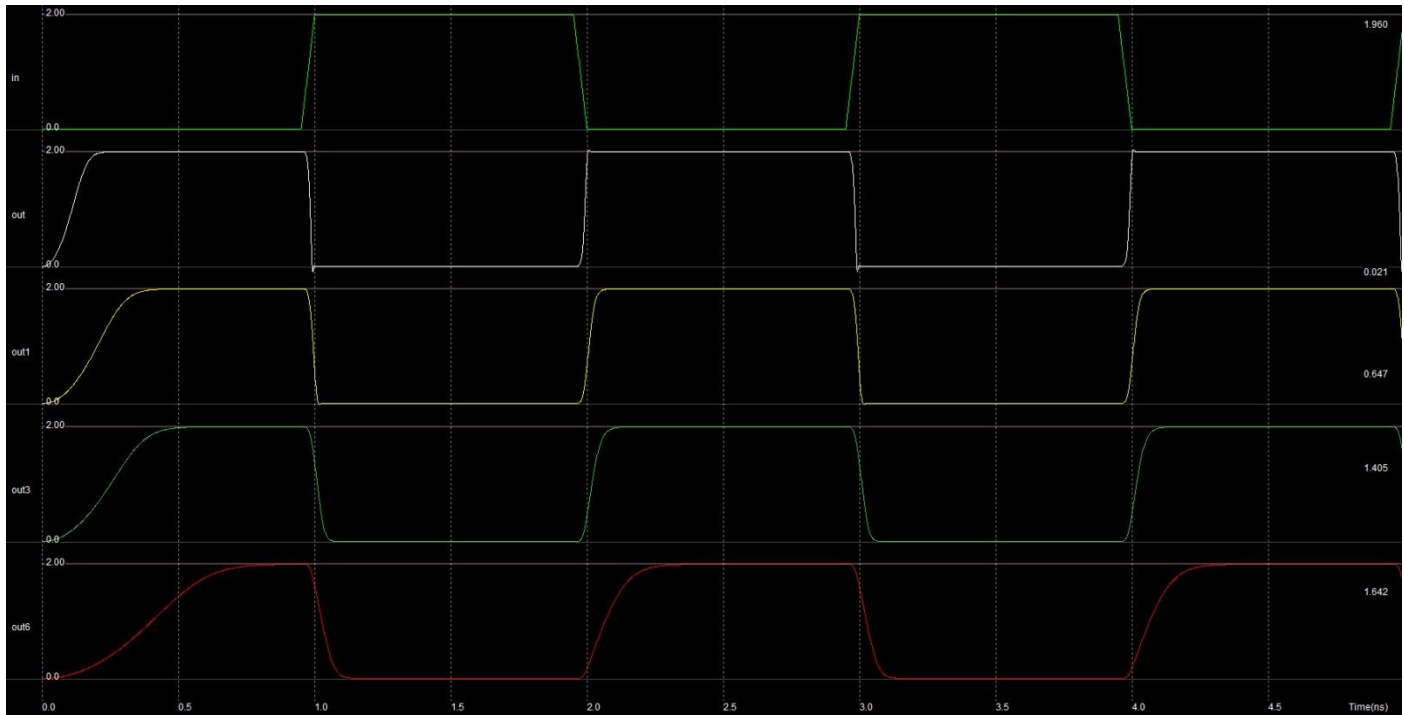


Implementation F-A (cmos65n)



2. Minimum Inverters (cmos018)





Minimum Inverter which leads n minimum inverters	↑(ps)	↓(ps)
n=0	8	17
n=1	20	32
n=3	34	42
n=6	51	91

Minimum Inverter which leads n minimum inverters	Delay Calculation
n=1	2,5
n=3	4,25
n=6	6,37