Total No. of Pages 2	Roll No
VI-SEMESTER END SEMESTER EXAM(Supplementary)	<i>B.Tech.(COE)</i> Aug- 2018
CO-302 Compiler Design	•
Time: 3:00 Hours Note: Attempt any Five questions	Max. Marks: 40
Q.No. 1	
 A. Construct DFA accepting strings of binary digits which a B. Compute FIRST and FOLLOW sets and Construct a pretable for the following grammar, where S is the start sym S→aBDh 	. [3] edictive parsing
B→cC є C→bC є D→EF E→g є F→f є (where 'є' denotes epsilon)	•
Q.No. 2	
A. What are the different error recovery strategies adop ?explain	ted in complier [3]
 B. Construct parsing table for the following grammar, whe symbol S→AaAb S→BbBa A→ є B→ є 	re S is the start [5]
Q.No. 3	
 A. Discuss algorithm for computation of the sets of LR(1) ite the following grammar is LR(1) but not LALR(1) S→Aa bAc Bc bBa A→d B→ d 	ems. Shows that [3]

B. Consider following grammar [5] S→aAb| bB $A \rightarrow Aa \mid \epsilon$ $B \rightarrow Bb \mid \epsilon$ and test whether the grammar is LL(1) or not? Q.No. 4 A. What is the advantage of left recursive grammar over right recursive grammar in LR parsing. Explain with suitable example. [3] B. Consider the program fragment and Generate three address code for it [5] sum=0 for(i=1;i<=20; i++) sum=sum+a[i]+b[i]; Q.No. 5 A. What is the use of FA in lexical analysis? Design a DFA for strings over {0, 1} having an even number of 0's and any no. of 1's. [3] B. Explain importance of PDA in compiler design and Design a PDA for [5] Language L= $\{0^n 1^n 1 \text{ for } n>=0\}$ Q.No. 6 A. What is loop jamming? Explain code optimization by eliminating induction variables and code motion with suitable examples. [4]B. Write SDD for generating three address code for Boolean expressions with &&, \parallel (OR) and ! Operators. [4]Q.No. 7 A. What are the various approaches for symbol table organization? Explain [3] with examples.

B. Give syntax tree, Directed acyclic graph (DAG) And three address code

[5]

for expression if (a>0) then a=3*(b+1) else b=b+1.