

Toshfan

Thread/Process share resources

22-1-2021

NUCKS, EAST, PAWSTAN.

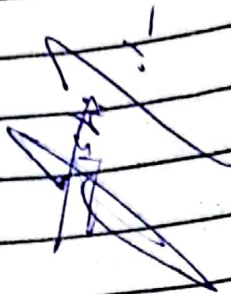
Process Synchronization

Processes

independent processes.

Cooperative Processes

(Kuch beh
Common share
too kst ki
Execution at any
pr kuch effect
dual krta h)



Examples-

int shared = 8 6 4. • but wrong.

• Race Condition

P ₁	P ₂
int X = shared	int y = shared 5
X++;	y--;
sleep(1);	sleep(1);
shared = X	shared = y;
6	4
terminated	

• Producer - Consumer Problem (bounded buffer Problem)

- Multi process
- Both processes are parallel and share sth.

- Count is set to 0 initially.

Producer - increment count

Consumer - decrement count

Problem:- • Increment (count++) & decrement (count--)

- It might oversight as in above example.

- Synchronization between producer & consumer
- use three separate semaphores.

int count = 0

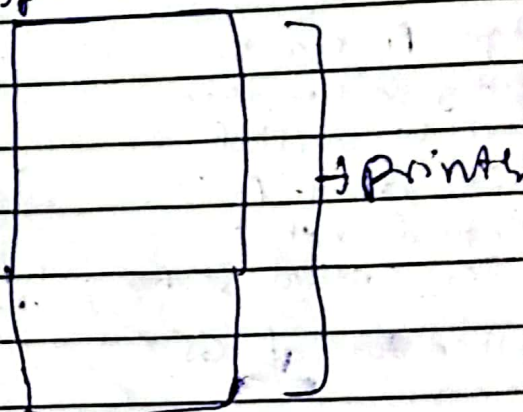
```
void consumer (void)
{
    int itemC;
    while (true)
    {
        while (count == 0);
        itemC = Buffer[out];
        out = (out + 1) % n;
        count = count - 1;
        process item (itemC)
    }
}
```

```
void producer (void)
{
    int temp;
    while (true)
    {
        produce item (temp);
        while (count == n);
        Buffer[in] = temp;
        in = (in + 1) % n;
        count = count + 1;
    }
}
```

• Print - Spooler Problem.

- 1) Load $R_i, m[in]$
- 2) Store $SD[R_i], "F-N"$
- 3) INCR R_i
- 4) Store $m[in], R_i$

Spooler directory



Problems:-

- Process wishes to print a file
so, it adds name in spooler directory.

IN

2 process are in same slot
data loss.

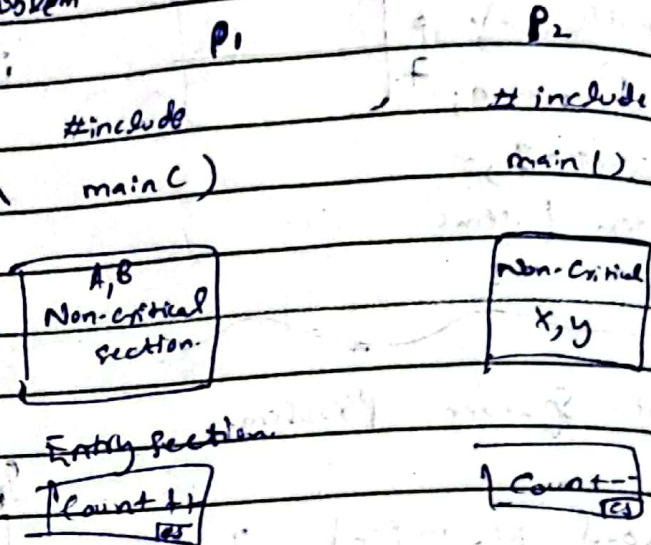
At any time, one process in critical section (Mutual Exclusion)
 Critical region → Part of program where shared memory is accessed

-- Race Condition

- Several process access and manipulate same directory or data.
- These can be avoided if no two processes are in critical region at same time.

-- Critical Section Problem

If P_1 & P_2 want to use critical section, before it, it should execute entry section, then move to critical section.



Ag P_1 executes entry section, then move to critical section, then P_2 should not enter critical section.

When out of CS exits

-- Synchronization mechanism:-

Condition / Rules.

- 1) Mutual exclusive → P_1 is in critical section. P_2 can not enter critical section.
- 2) Progress → P_1 wanna enter critical section. P_2 Roke rahi hai. No process should block other.
- 3) Bounded Wait → P_1 Critical section se bhar aye. P_2 Critical section ke access kr rahi hai. P_2 ki turn hai.
- 4) No assumption related to H/W speed →

Before entering critical section, a process should know if any other is already in critical section or not

Lock variable (for solving critical section problem)

do { acquire lock or release lock }	1. while (lock == 1); 2. lock = 1; 3. Critical section 4. lock = 0.	Entry code Exit code.
---	--	--

- execute in user mode
- Multiprocess solution
- No mutual exclusion.

Problem :- If P₁ executes line 1 and then there is emergency. P₁ chadhi gya kahun. P₂ aye and critical section enter krigh. Phir P₁ jh aye it will executes 2nd line aur wo bhi critical section mai. Mutual exclusion is finished.

Turn Variable (strict Alteration) → turn wise.

- 2 process solution (Condy for 2 process)

- execute in user mode.
- Mutual execution ✓

- Progress X

b/c ~~P₀~~ P₀ ko rok rahi
and P₁ P₀ ko.
after critical section.

Process P ₀	Process P ₁
while (turn != 0);	while (turn != 1);
[critical section]	[critical section]
turn = 1;	turn = 0;

- Bounded wait ✓

- taking turns is not good idea if one process is slow.
- Process 1 turns but maybe it is not interested.

↓ Solution .

P₀ while (true).

interested[0] = TRUE;

while (interested[1] != FALSE);

Deadlock.

P₁ while (True)

{ interested[1] = TRUE;

while (interested[0] != FALSE;

- Peterson & Bakery algo solves critical section problem.
- Are not used in practice.

— Peterson Method. (2 process)

$P_0 = 0$
 $P_1 = 1$

Interested 0 1 Entry Section (Process)
F F {
 Turn 0

$P=0$
 $0=1$
 $P=1$
 $1=0$

1. int other;
2. other = 1 - process;
3. interested [process] = TRUE;
4. turn = process
5. while (interested [other] = TRUE
 && turn = process);

Critical Section

Exit Section (process)

6. interested [process] = FALSE;

— Lamport's Algo | Bakery Algo. (n process)

- 1) n numbers of process.
- 2) Token number to processes
- 3) Token number in same enumeration
- 4) Token number. if not assigned and when finishes execution zero/false is assigned.

- Boolean Choosing [N]
- Initially it is false.
- When P_i is choiced, token is set to TRUE.

- int Number [N]
- Initially 0.
- if it want enter number should be non-zero.

- 5) Token id left will enter critical section.

- 6) 2 process can have same token number. Then see id.

Software - Based Solution of Critical Section Problem.

Peterson & Bakery.

Mutual Exclusion using hardware support

- interrupt disabling \rightarrow process disables interrupt while executing its critical section.

```
while(true)
{
    // disable interrupts
    // critical section
    // re-enable interrupts
    // remaining section
}
```

- disabling interrupts guarantees mutual exclusion.

- Disadvantages:-
 - Processor can't switch b/w tasks when interrupt is disabled.

- If we have more than one processor, disabling interrupt on one processor won't stop the others. So, Mutual exclusion is not fully achieved.
- restricted for user programs.

- These instructions perform reading & writing in one atomic (indivisible) step. No other process can interrupt.

- Uniprocessor System: These instructions run without being interrupted.
- Multiprocessor System: Executed with lock system bus.

Test & Test Instruction (solution to critical section solution)

```
while(test_set(lock))
{
    // true yes
    // false no loop
    // main here plus jee go.
}
```

Mutual progress

all to be done

lock main to solve.

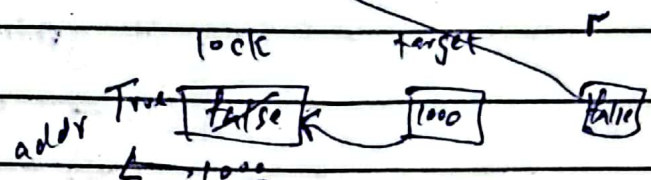
problem 400

```
while (lock == 1)
{
    lock = 1;
}
```

3 Critical Section

```
lock = 0
```

```
boolean test_set (boolean x, target)
{
    boolean r = !x & target;
    x & target = True;
    return r;
}
```



• - Mutual Exclusion using machine Instructions

Advantages →

- Works on any system
- Simple to implement

Disadvantages →

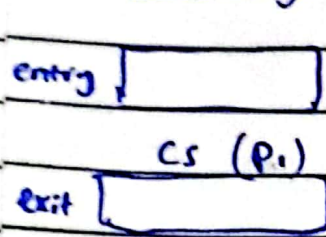
- Busy waiting
- Starvation
- Deadlock in priority scheduling

(If low-priority process is in critical section, high priority process will wait.)

- Medium-priority can block low priority from finishing, causing high priority to wait indefinitely.

• - Priority Inversion Problem

(Spinlock or deadlock)

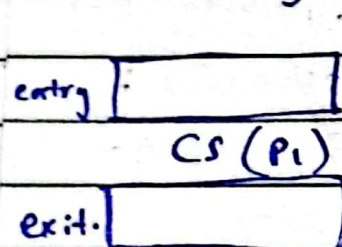


But $P_0 \gg P_1$
 \downarrow
 priority of P_0 much higher than P_1

Then we take CPU from P_1 but P_1 remains in critical section. So, P_0 can not come in critical section

• - Priority Inheritance

(solution to Priority inversion)



$P_2 \gg P_1$

→ for sometime, P_1 gets priority high by its

• when P_1 comes out, means exit, it gets original priority which is low.

philosopher

think → left fork → right fork → eat

the philosopher spins left fork utters, phr right utters say phr prompt at right fork that bel murtu (dead lock in this case).

Dining Philosopher's Problem

void philosopher(void)

{ while (true)

{ Thinking();

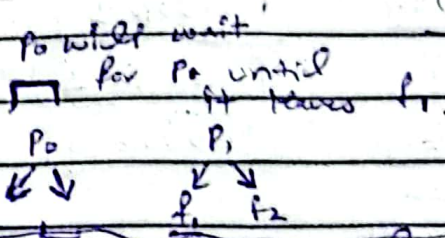
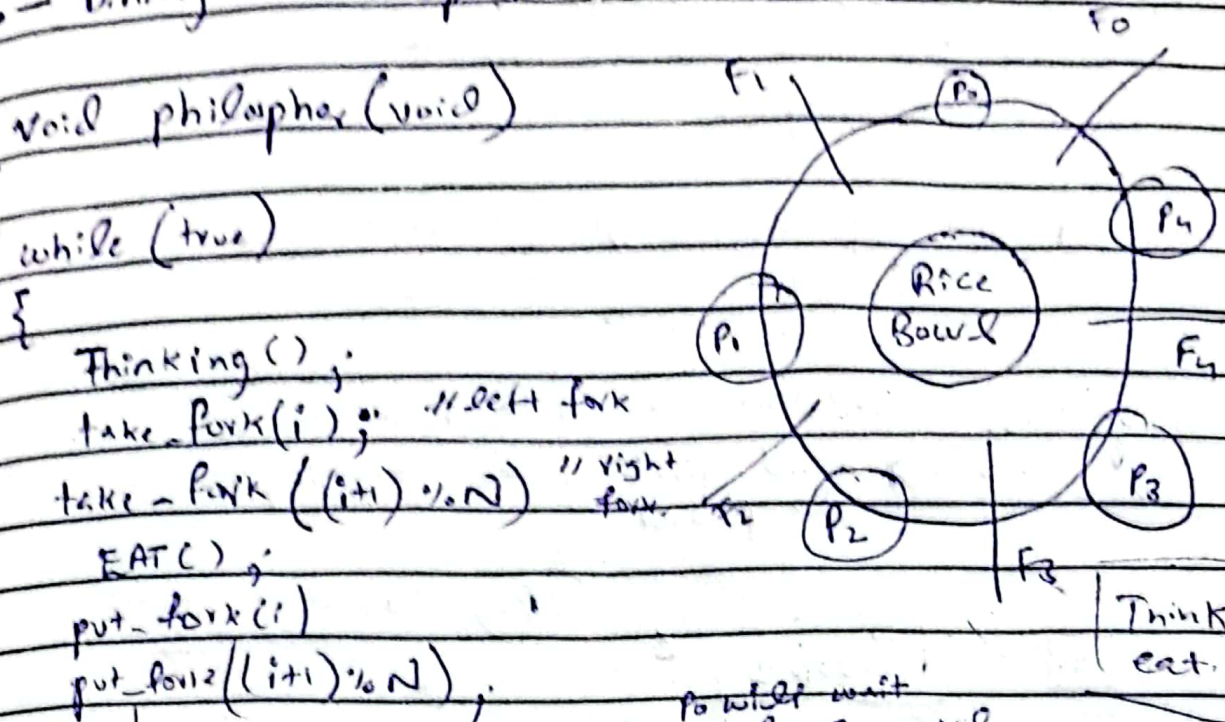
take_fork(i); // left fork

take_fork((i+1)%N); // right fork

EAT();

put_fork(i);

put_fork((i+1)%N);



We will use binary semaphores. 5 binary forks

Utter has semaphores S_0, S_1, S_2, S_3, S_4

Same code as above just some changes

Critical Section
main 2 bet the forks
or independent fork

wait (take_fork(i))
wait (take_fork((i+1)%N))

EAT()

signal (put_fork(i))

signal (put_fork((i+1)%N))

}

P0 :	S_0	S_1
P1 :	S_1	S_2
P2 :	S_2	S_3
P3 :	S_3	S_4
P4 :	S_4	S_0

• P0 says S_0 and S_1
K0 down K1 down. Like
values 1 to 0 of
both S_0 and S_1 .

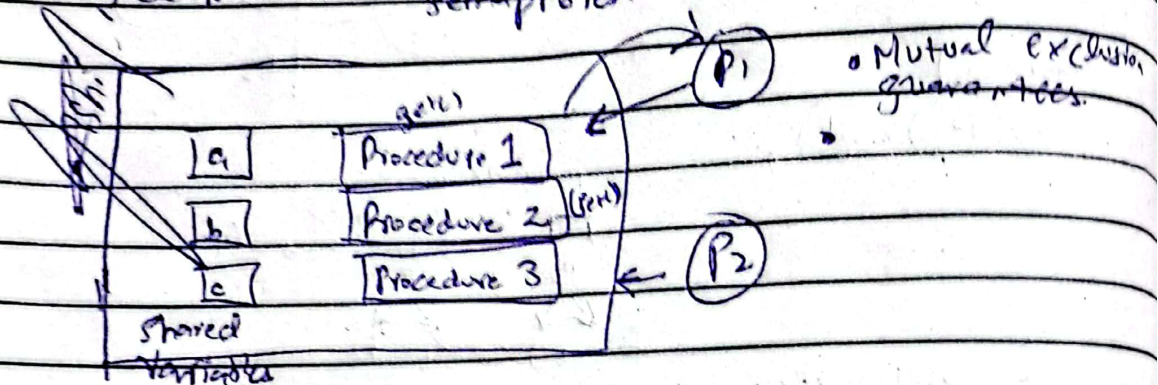
• P1 block. q K0s w down
nai K1 keta. S_1 K0s and
• P2 above a, and S_2

• Dead lock khatam karney kay liye hum values hae
 reverse kr deay hain. P_4 | S_4 | S_0 hgy. 913
 ko P_4 | S_0 | S_4 kr deay hain. P_4 block hoga

• Ab S_3 kay full right fork na gya. khaana
 khaen aur phir fork ko release kr kary 1
 kr abse ab aise ko use kr
 skey hain philosopher.

• Now, P_4 ka sequence change in code also.
 phy right then left.

— Monitors. (solution to critical section)
 ↓
 class. → Avoid problems of managing multiple
 semaphores.



- 1 process enters monitor at a time
- When enter, then there is a lock.
- P_1 can not access shared variables directly,
 it will get it from procedures.
- P_1 bhr aays. P_2 enter.

Monitor
 { Condition variables; → Operate on 2 operations
 Variables; → await()
 Procedure 1 → signal().
 { }
 Procedure 2
 { }