Chapter no 1s- (1.1, 1.2, 1.4, 1.11) What operating Systems do? Operating System > manages computers hardware.
Q: What operating Systems do?
- Operating System - manages computers hardwere.
enche de la
OS (and application - program)
· Application (software programs)
Programs -
· Usev
Embeded Systems > with minimal or no direct user
interface. e.g., TV, Washing machine
Resource manager - Os can be seen as resource manager.
Control program - OS can be control program.
·- Mouve's Daw -> number of transistors on an integrated
eixcuit would double every 18 months
co-founder of has held true.
and the second of the contract
Components of Os > Kernal > core part of os that
Buston - After Kernal (windows NT kernal) managing system resources.
daemon starts. System - associated with of but not the part of the
hamles enter leathands Programs Keynal.
program program but run on the
System.
Sophistication -> In 1988, us department of justice
more knowledge case against microsoft for building to much functionality with its as.
too much functionality with its os;
Web Browser was integral past

b/w computers Liquit / output operations: - Transfer of data and external devices or stirege LSR (Interrupt. Service yestine) - special function in computer. important needs immediate Kernal- + middlewave includes ·- Mobile OS frameworks, by hardware or Software. Computer - System organization device driver sismals that indicate Interrupts > I/o opera program in device controller performs I/O operation Ila operations controller Continue No Data transfer Data to Docal asynchronous Complete? operation buffer Conging > CPU Stop > CPU device controller vece ive Jenerates an Chryent tention toterupt execution Chachia. interiorpt) ISR CPU update device + Jump to ISR drive driver processes the resumes Chiver updatel interupt. Preview WITH I/O operation. execution Program Ve Wets Interrupt > When an interrupt occurs,
Yequer task and transfer control Interrupt handling > request (continued on next to ISR. Page) After ISR executes. CPV previous tasks. resumes its Table of potators (Interrupt - Intervept Management - Table of Post nters vector table that for manasing Intervopt & called Vaviu number it used to inches me Interrets efficiently this proposed 40617 appropriate CPU Sovel convent State. before & restoring servicing the interript, and vestore it afterward to esure the previous CHA te computation somoothly. Location of I'm adverses - Table pointers located in ;] you memory

Interupt Cycle > process of vaising, catching and
Savicing an interrupt.
· Cou but this
- Interrupt request line Wire that con senses after executing every instruction.
every instruction.
epu detects > vends - uses it as interrupt > interrupt > interrupt = a index into venuelt number interrupt venuelt number
Veguest number interript Table of addressed to find JCR vector.
Table of addresses / used to find ISR Yectiv.
Interrupt handling > · Deferming > interruption sog system
- Interrupt handling > o Deferming > interruption so, system Interrupt Can wait to handle Interrupt Can wait to handle Interrupt on the prior ontil Important job is done.
important sob is done.
o lispatching send to right handley,
Multilevel > which Enteropts are
interupts more orgent than others and handles important
firstly.
- o- CPU & Interupt · Controller → Manages interupt handling
CPU has 2 Interrept request Qines.
okable I take to Reserved for
- Non-maskable Interrupt (NMI) - Reserved for cirtical events like unrecoverable
memory errory.
o- Maskable Interrupt - Can be turned off by
CPUL & used by device
Controllers for general requests.
- Vanden into the state of the Parish.
·- Vector interupt mechanism > Instead of searching through
everything to find out what caused
interrupt, Computer oses shortcot (address)
that tells exactly where togo.
Interest oriovities > priorities besil.
- Interupt priorities > priorities basis.

Vetain & EEPON > (Exectrically evaluable Programmable memory)
power is DDR -> Double data vate / Type of DRam.
Yemwed. DRAM > Dynamic RAM.
V
- Main Memory (Random & ccoss memory - RAM)
· Yew table memory · implemented using DRAM.
- Bootstrap Program - First program executed on power on which loads operating system
e Ctore of in the party,
- Volatile Storage Non-Volative Storage
V STATE OF When
hoses content when power is turned on power is turned on
off: (RAM) (SSDI, HODI, EZ POM)
- Instruction Execution Cycle - memory and stores it in
instruction register.
· decades instructions & executes it.
Memory Hierarchy - Registers (Smallest & fastest)
Main memory (Ram) (harger & Slower than
· Secondary Storage (600, 400)
- Tentiary storage (Backup aystems (wb)
· - Storage devices - Mechanicas - Hoos (larger, less cost, slow)
NVM + Electrical > 080c (smaller, faster.
NVM + Electrical >> 980s (smaller, faster, storige
Cache memory . Installed to improve performance
by bridging speed gap blu fest
and slow storage components.

temporarily Pretending more physical memory Virtual memory moving data Beneficial for operation involving large amount of data, Dike Disk Ità. to hard Phone ki storage dvive. Converts to Yam. Direct Nemovy access Ilo Structure. + 2 Method Wed to transfer data 0 blu device and memory evithout of the CPU > one intervipt. is generted per block to tell device driver the task has completed Operating System Operation Multiprogramming - increase CPU utilization.
Keeps several processes memory simual tanously 3 Multitasking - logical extension of multiprogramaing CPU Scheduling - Process of determining which process or threed your on the cpu, at any maximize1 Siven time. CPU Utilization. - Actual hardware memory (RAM Physical memory tostalled in System. AS MO presented to memory - Address space Logical actual physical process gabltracting memory. jis Kay pass 4 aB memory by. Ya logical broken 27 memory. Computer thight have 228 actual physical Ram mode operations -> Kernal mode > 05 has full access to Cherice drivers · System starts this mode during · User-mode + Yestricted where user application (1) YUN. arrent mode. hard wave indicates the . Mode bit in for Kernal for

· GNU'S Not Unix (GNU) -> free software operating system.
· GNU'S Not Unix (GNU) - free software operation system. UNIX + OS System Calls + gate way for user programs to request services from os. etc.
services from os. etc.
Certain istructions (No control)
·- Privilege instructions > and be controlled only in
Keinal mode. If user executes than hardware traps action
and hands control to os.
· - Multimode Operations -> · More than 2 model.
· like an odditional mode for
Virtual machine manage (VMM)
Vei-
move privide ger than vier
but pers than Keinal, but pers than Keinal, Timer > ensures that CPU is not monopolized by a user program.
- Itmet -> Drogram.
a more distance descendent to them to the top of the formation of the first top of the firs
Control of the contro
SANTE CONTRACTOR OF CONTRACTO
(CAN) Comment with the first the property of
111 Free and Open Source Operation & Systems.
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III Free and Open Source Operating Systems. - provides access to source code, provider access to to source code, source Code but no cost we. does not guarantee same Otcensing Recedoms as free w. - Richard Stallman's initiative (1984) Developed a free, UNIX- compatible Os called freedom of use, not price. - Four essential Freedoms - Freely run program - study & change source code - give or sell copies - distribute copies. - Ganu Manifesto (1985) - Published by Stallman.
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3	- Formation of free Software fundation → use & development of free Software Established
2	
	- Copyleft → · Licencing concept invented by 8tallman.
4	· Grant four essential treedoms.
3	- Gieneral Public License (GPL) - Common copylett Dicence under which software is released.
3	Required that source code be distributed with binaries.
3	• All Copies must be released under same GPL Dicense
	- GINU/Linux > Origin > In 1991, hinus Torralds created UXIX-Like Kernal named as 'Dinux'
	using and Touls. and invited Conta; butus. hinux was not free, in 1992, lines release, it under apl, making it free and popp conrece.
-	- GNU/Linux Alefers to complete OS that Combines Dinux Kerned with GNU Tools.
7	BSD UNIX →. Free BSD → High performance + advance
7	(Beyekely Software Open BSD. >> Simplicity Open BSD. >> Security and code correctness
9	e Drasmoffur RSD - HAMMER TILL Buttern +
2	optimization for multi-core Processors.
4	· Darwin > core kernal of Macos based on
2	BSD UNIX and it open source. Commercial unix-based of of Sun microsystems.
<u>-</u>	in 1991 as its base. Oracle purched Sun.