### Page 2

LPC1769 Core

### Page 3

USB interface: Host or Device

## Page 4

100/10M Ethernet PHY

## Page 5

CAN and Serial expansion interface

### Page 6

RF interfaces

### Page 7

uSD interface

# Page 8

LPC1769 I/O and Peripherals

### Page 9

Power supply and UART-to-USB bridge

### Page 10

CAN node (LPC11C24 based)

UL = UnLoaded = normally not mounted component.

Default jumper settings are indicated in the schematic. However, always check jumper positions on actual boards since there is no guarantee that all jumpers are in default place.

#### Rev A

First release
Updated U5 to 24AA02E48T-I/OT (contains MAC address).



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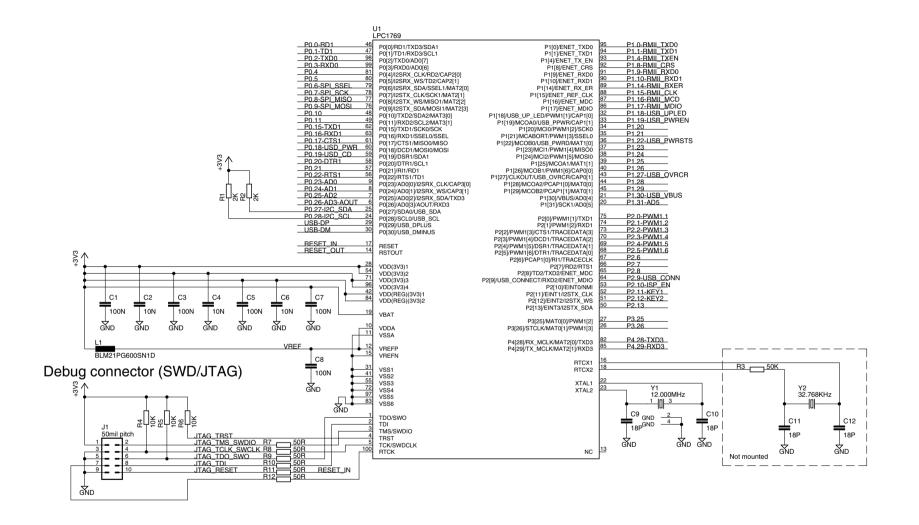
TITLE: AOA Application Board A

Document Number:

Date: 2013-04-12 15:52:05

Sheet: 1/10

#### LPC1769 core





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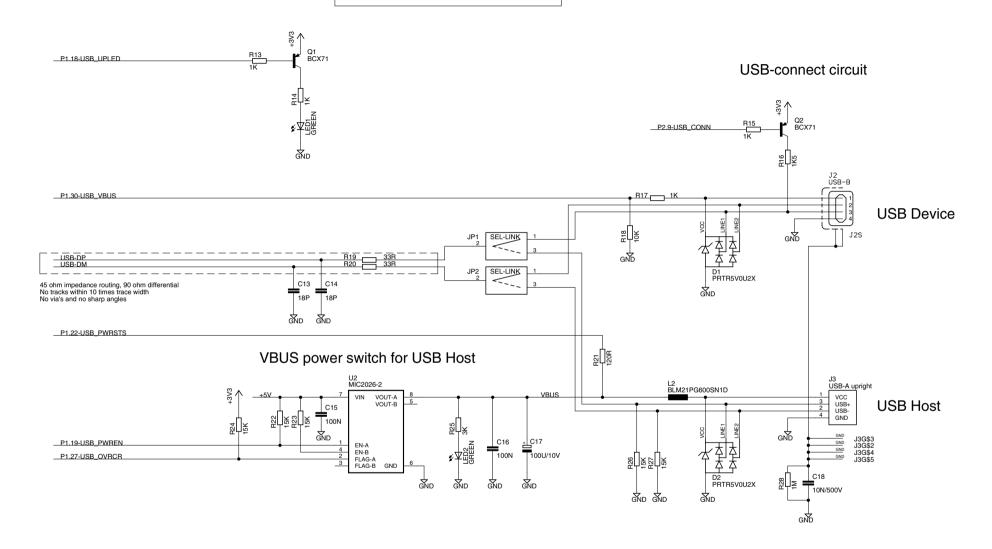
TITLE: AOA Application Board A

Document Number:

Date: 2013-04-12 15:52:05

Sheet: 2/10

#### USB interface: Host or Device



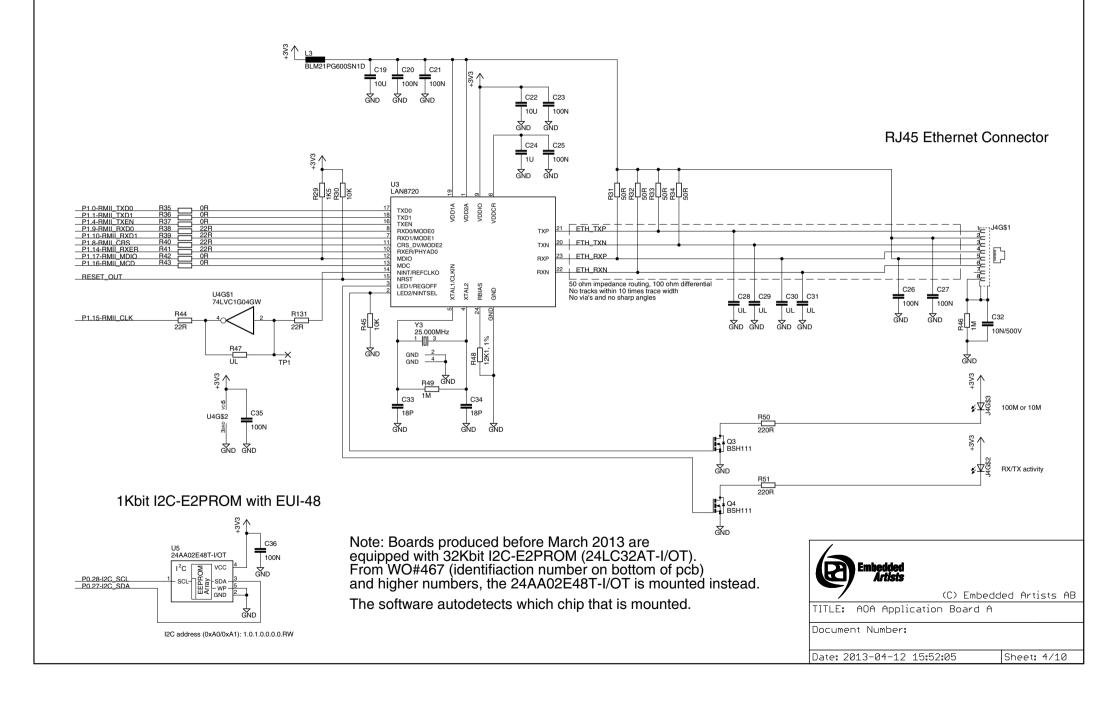


TITLE: AOA Application Board A

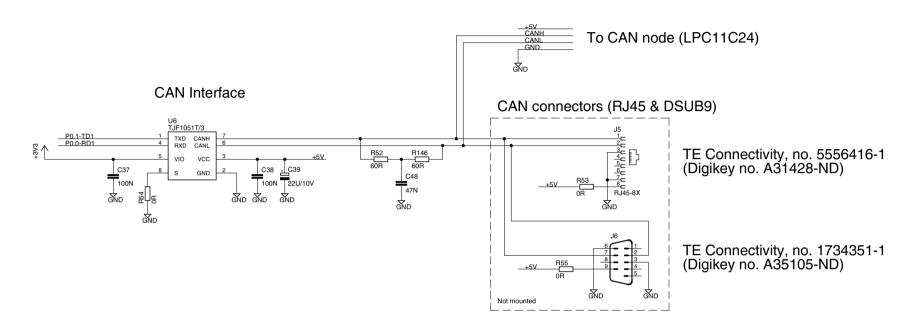
Document Number:

Date: 2013-04-12 15:52:05 Sheet: 3/10

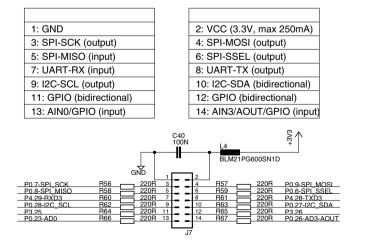
### 100/10M Ethernet PHY (via RMII interface)

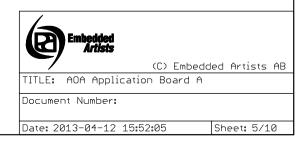


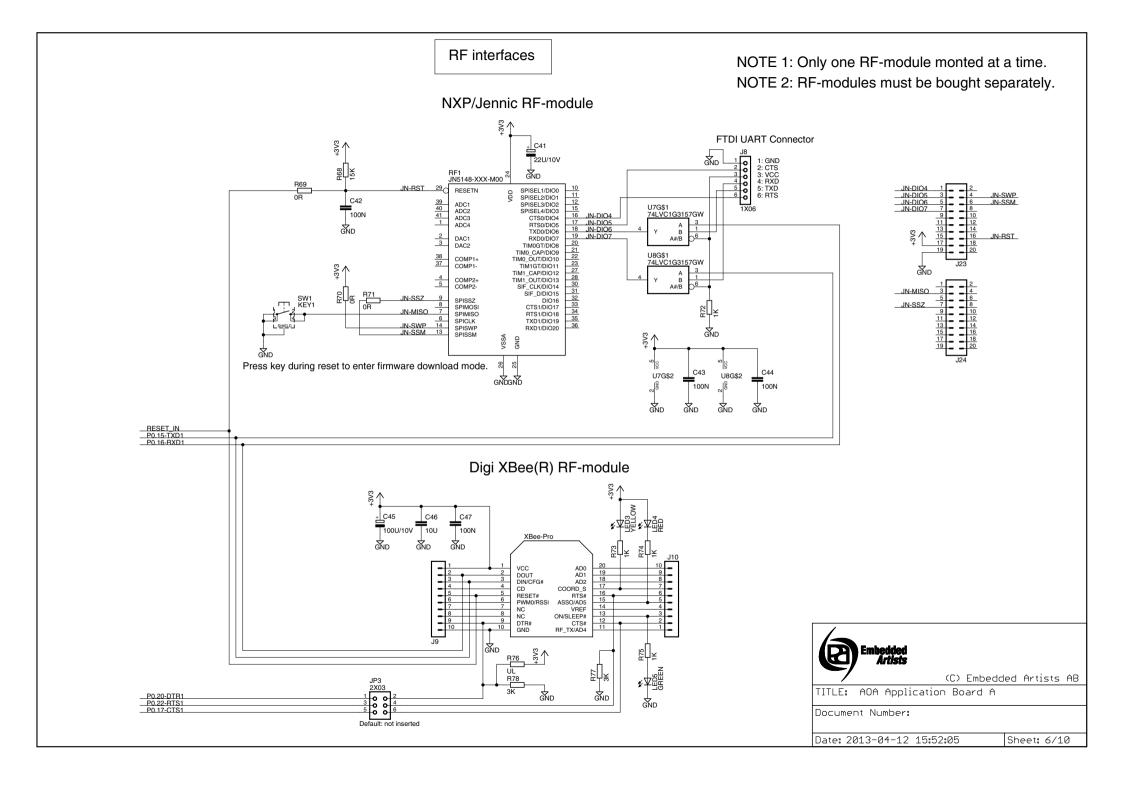
## CAN and Serial expansion interface



## Serial Expansion Connector

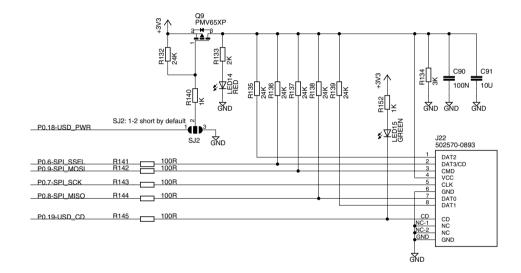






## uSD interface

# uSD Memory Card Interface (SPI interface mode)





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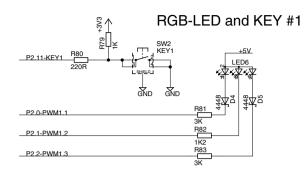
TITLE: AOA Application Board A

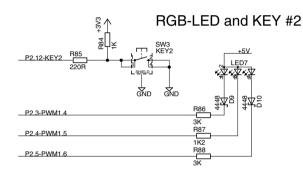
Document Number:

Date: 2013-04-12 15:52:05

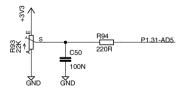
Sheet: 7/10

# LPC1769 I/O and Peripherals

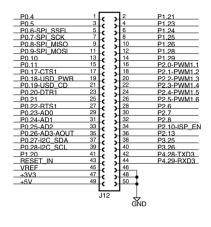




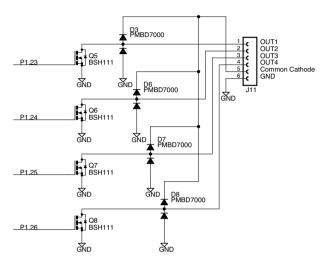
### **Analog Input**



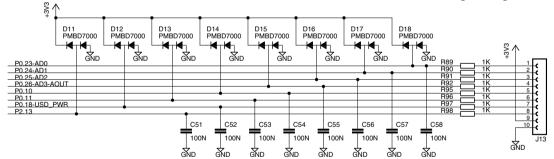
### **Expansion connector**



# 4 Open Drain Outputs



## 8 Protected Analogue/Digital I/O





Document Number:

Date: 2013-04-12 15:52:05 Sheet: 8/10

### Power supply and UART-to-USB bridge

