SEL-2404 Satellite-Synchronized Clock Instruction Manual

Features, Benefits, and Applications

The role of satellite-synchronized clocks has grown from basic sequence-of-event and fault recorder time referencing to mission-critical roles such as synchrophasor measurement and detailed event analysis. These new applications require that satellite clocks meet the same environmental standards and be as reliable as the protective relays and other high-reliability devices with which these clocks are used.

The rack-mount SEL-2404 Satellite-Synchronized Clock displays satellite-synchronized time in a large and easy-to-see form and is ideal for substations, industrial facilities, and control centers.

The SEL-2404 provides the following features:

- **Reliability**. Apply in harsh environments. Meets IEEE C37.90 and IEC 60255 protective relay surge and environmental standards.
- **Flexibility**. Displays time or date. Time displays in either 12- or 24-hour format modes.
- **Visibility**. Bright, easy-to-read, 3-in (76-mm) digits display time or date.
- **Accuracy**. Apply for synchrophasor, relay event correlation, and other high-accuracy timing needs. Demodulated IRIG outputs with accuracy of ±100 ns average (±500 ns peak) meet requirements for existing and future timing applications.
- **Low Cost, High Function**. Provides demodulated IRIG-B timecode with time-quality values specified by IEEE C37.118-2005 (Standards for Synchrophasors for Power Systems). Preset daylight-saving time for European Union (EU) and United States (USA), plus a custom **DST** command provides automatic advance and return for daylight (summer) time anywhere in the world.

Product Overview

Figure 1 and *Figure 2* provide front and rear views of the SEL-2404.

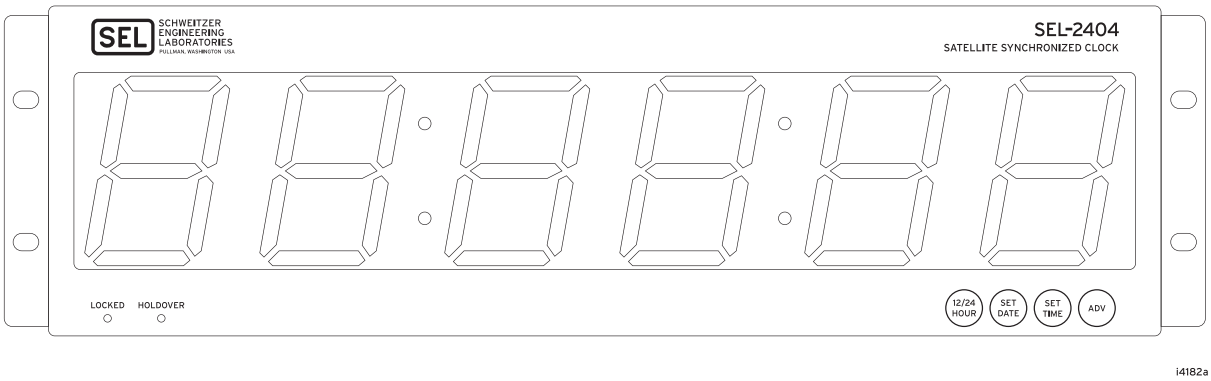


Figure 1 SEL-2404 Front View

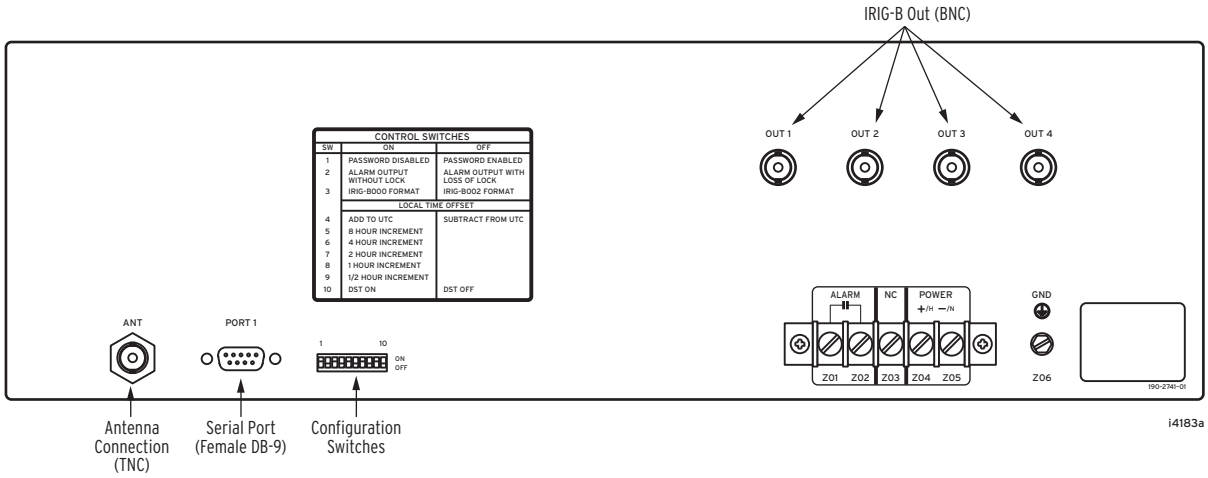


Figure 2 SEL-2404 Rear View

Accessories

Accessories enhance your SEL-2404 application. For a complete list of available accessories refer to the SEL Satellite-Synchronized Clocks Accessory Guide at selinc.com/SEL-2404/.

Installation and Maintenance

The SEL-2404 is available in a rack-mount version that bolts easily into a standard 19-inch rack (see *Figure 1*). From the front of the clock, insert four rack screws (two on each side) through the holes on the relay mounting flanges.

IRIG-B

IRIG-B is a serial data time format consisting of a 1-second frame that contains 100 pulses divided into fields. The time-synchronized device decodes the second, minute, hour, and day fields and sets the device internal time clock upon detecting valid time data in the IRIG time mode.

The SEL-2404 provides only an unmodulated IRIG-B output (according to the Range Commanders Council IRIG Standard 200-04). Unmodulated IRIG-B timecode is IRIG-B00*x*. The last digit, either 2 or 0, indicates the coded expression(s).

The timecode format IRIG-B002 is binary-coded decimal (BCD) timecode (HH,MM,SS,DDD). This format represents traditional or legacy IRIG-B.

The timecode format IRIG-B000 consists of BCD timecode (HH,MM,SS,DDD), plus straight binary seconds (SBS) of the day (0–86400 s), and also contains control function extensions that include data for: year, leap second, daylight time, UTC time offset, time quality, and parity (odd).

These control function extensions are described in IEEE 1344 Annex F, or in IEEE C37.118-2005.

Power Connections

The rack-mount SEL-2404 comes with a 125 Vdc or Vac power supply. Control power passes through these terminals to a fuse and to the switching power supply. The control power circuitry is isolated from the clock chassis ground. Refer to *Settings and Commands on page 7* for power supply ratings. The power supply rating is listed on the serial number sticker on the rear panel.

Compression Terminal Connectors

Terminate connections to the SEL-2404 compression terminal by stripping 8.0 mm (0.31 in) of the wire insulation. Tightening torque for the terminal connector screws is 0.79 Nm (7 lb-in).

Indicators

The SEL-2404 has two status indicator LEDs. *Table 1* describes these indicators that annunciate the status of the clock.

Table 1 Status Indicator LEDs

Label	Color	Description
HOLDOVER	Green	Clock lost satellite lock and is running on internal backup clock.
LOCKED	Green	Clock tracked at least four satellites and is tracking one or more satellites and time is locked in.

Antenna

The SEL-2404 GPS antenna must be installed in accordance with national electric codes. A clear view of the sky, 360 degrees, is preferred. The antenna is housed in waterproof packaging designed to withstand exposure to shock, excessive vibration, extreme temperatures, rain, snow, and sunlight. Position the antenna so that the radome points skyward.

The GPS antenna should be located low and close to the control house roof, above maximum snow accumulation and away from roof maintenance activities.

Lightning Protection

Mounting the antenna on an equipment building roof or cabinet is safest because the potential rise on the outside of either of these structures would be more or less equal to the potential on the inside. A lightning protector should be used to equalize the difference in potential that can occur between the center conductor and the shield of the coaxial cable between the antenna and the clock. The higher the GPS antenna is mounted on a support structure, the greater the probability of equipment damage resulting from a lightning strike.

In all surge-protector applications, you should mount the surge protector at the building or enclosure entrance and ground the surge-protector body at this entrance. Ground the clock to the same point as the surge-protector ground to avoid ground-rise-potential damage.

Antenna Cabling

The SEL-9524, the Gas Tube Coaxial Surge Protector, and the SEL-2404 are all equipped with TNC female connectors. The SEL-2404 supports a maximum cable length of 152 m (500 ft) for the LMR-400 (SEL-C961) antenna cable. Users can choose RG-8X (SEL-C965) cables if long distances are not required.

Disconnect Device

For installations requiring IEC compliance, place an external circuit breaker no more than 3.0 m (9.8 ft) from the equipment. The circuit breaker must comply with IEC 60947-1 and IEC 60947-3 or an equivalent approved disconnect device appropriate for the country of installation and be identified as the disconnect device for this equipment. The overcurrent (OC) device must be sized to the conductors suppling the equipment. If the conductors can handle 20 A then the OC device can be 20 A or lower.

Operational power is internally fused. This fuse is user-replaceable. If a failure occurs, use the procedure described in *Testing and Troubleshooting on page 20*.

Lock Sequence

The SEL-2404 front-panel LEDs show if the clock is running on internal time or is satellite locked.

Cold-Start Sequence

NOTE: Carrier-to-noise density (C/N_0) is a measurement of the signal quality comparing the signals the clock needs to perform its functions to the general noise that the receiver receives. This density is expressed in dB-Hz, and the larger the number the better the signal. 30 dB-Hz is a minimum accepted signal level.

When initially powered and before searching for a satellite signal, the SEL-2404 displays time from when it was previously powered and locked. Satellite lock occurs when the SEL-2404 detects and tracks at least four satellites each with a carrier-to-noise density (C/N_0) of 30 dB-Hz or better. To remain locked, the clock must track at least one satellite. The correct time, per the offsets programmed with Switches **4** through **9**, is reported when the **Locked** LED is illuminated.

Cleaning

Use care when cleaning the SEL-2404. Perform the following steps:

Step 1. Use a mild soap or detergent solution and a damp cloth to clean the enclosure.

Step 2. Be careful cleaning the front label because a permanent plastic sheet covers this panel.

Do not use abrasive materials, polishing compounds, or harsh chemical solvents (such as xylene or acetone) on any surface.

2/24 Hour

Pressing the **12/24 HOUR** pushbutton toggles the clock display between 12- and 24-hour time. For instance, if the time is 11:30:00 p.m., pressing the **12/24 HOUR** pushbutton toggles between 23:30:00 and 11:30:00.

Pressing the **12/24 HOUR** pushbutton for longer than 3 seconds displays the Firmware ID (FID) of the clock display. Release the pushbutton to return to normal operation. For details, see *Determining the Firmware Version on page 25*.

Set Date

Pressing the **SET DATE** pushbutton for less than 3 seconds displays the present date. When displaying the date, the clock does not light the colon separators.

NOTE: If the SEL-2404 is satellite locked, it automatically displays the date and overrides the battery-backed settings entered. The year is overridden if the Year-Source Selection jumper is removed (see *Year-Source Selection on page 6*).

Pressing the **SET DATE** pushbutton for more than 3 seconds enters the date-set mode. In the date-set mode, the most significant digit of the year flashes first, indicating that the value may be changed. The year, month, and day are set in that order. You can change the value of the flashing LED by pressing the **ADV** pushbutton. Pressing the **SET DATE** pushbutton again, sets the flashing LED and advances to the next digit. To advance past the flashing digit, press the **SET DATE** pushbutton. Continue this process until the correct date is set. When the last seven-segment LED is set, the SEL-2404 returns to normal operation and stores the date in the battery-backed clock.

Set Time

Pressing the **SET TIME** pushbutton for less than 3 seconds causes the clock to display the present time, and light the colons used to separate hours/minutes and minutes/seconds.

NOTE: If the SEL-2404 is satellite locked, it automatically displays the time and overrides all battery-backed settings entered.

Pressing the **SET TIME** pushbutton for more than 3 seconds enters the time-set mode. In time-set mode, the most significant digit hour LED begins to flash, indicating that the value may be changed. The hour, minutes, and seconds are set in that order. You can change the value of the flashing LED by pressing the **ADV** pushbutton. Pressing the **SET TIME** pushbutton again sets the flashing LED value and advances to the next digit. To advance past the flashing digit, press the **SET TIME** pushbutton. Continue this process until the correct time is set. When the last seven-segment LED is set, the SEL-2404 returns to normal operation and stores the time in the battery-backed clock.

Adv

Pressing the **ADV** pushbutton, while in time-set and date-set modes, increments the value indicated for the flashing digits. Pressing the **ADV** pushbutton for more than 3 seconds when not in a setting mode begins the LED self-test. See *Self-Test on page 20* for details.

Accessing Jumpers

To set specific clock functions, the SEL-2404 has two sets of jumpers (labeled **J2** and **J4**) inside the back of the clock display. Jumper **J2** consists of one row of eight pins displayed in *Table 2*. Jumper **J4** consists of two rows of four pins displayed in *Table 3*.

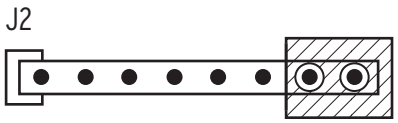
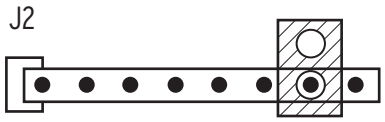
To access the jumpers on the SEL-2404, remove the top panel plate. The jumpers are located on the back of the clock display PCB board.

Year-Source Selection

Use the Year-Source Selection jumper (J2) to select the year source, either from a satellite source or the internal battery-backed clock (see *Table 2*).

The SEL-2404 is shipped from the factory with the Year-Source Selection jumper (J2) installed, which displays the year from the satellite timecode information.

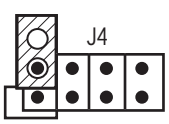
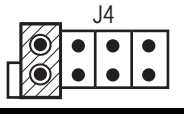
Table 2 Year-Source Selection Jumper (J2) Recommendations

Year-Source Selection	Jumper Position
Jumper Installed (Default) Displays the year from the satellite timecode information. Use with IRIG-B000 time source, which includes year information. IRIG-B output format is IRIG-B000 when no signal is present.	
Jumper Removed Displays the year stored in the internal battery-backed clock. Use with IRIG-B002 time source, which does not include year information. IRIG-B output format is IRIG-B002 when no input signal is present.	

Front-Panel LED Dimming

Use the dimming jumper to reduce the brightness of the front-panel display. The SEL-2404 is shipped from the factory with front-panel dimming disabled.

Table 3 Front-Panel Dimming Jumper (J4) Positioning

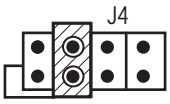
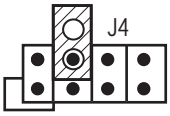
Dimming Front Panel	Jumper Position
Jumper Removed (Default) The clock displays brighter with no jumper across Pin 1 and Pin 2. This is the default for shipment.	
Jumper Installed Installing the dimmer jumper across Pin 1 and Pin 2 reduces the brightness of the front-panel display.	

IRIG-B Output

Use the IRIG-B jumper to control when the SEL-2404 outputs IRIG-B if it has a locked signal (see *Table 4*).

The SEL-2404 is shipped from the factory with the IRIG-B output jumper enabled. In this mode, the SEL-2404 will only output IRIG-B if the satellite clock is locked and healthy. When the SEL-2404 loses satellite lock and switches to the internal clock, it will disable the IRIG-B outputs. If the jumper is removed than the SEL-2404 will always output IRIG-B regardless if the clock is satellite locked.

Table 4 IRIG-B Output Jumper (J4) Positioning

IRIG-B Output	Jumper Position
Jumper Installed (Default) Installing a jumper across Pin 3 and Pin 4 disables all IRIG-B output signals if the SEL-2404 loses satellite lock and is running on the internal clock.	
Jumper Removed With no jumper on J4 across Pin 3 and Pin 4, the SEL-2404 will always output an IRIG-B signal, whether it has a locked time source or is running on its internal clock. In this mode, the IRIG-B output format will be based on the Year-Source Selection jumper.	

Settings and Commands

Settings

Most settings for the SEL-2404 are performed through configuration (DIP [dual in-line package]) switches located on the rear bottom of the device (shown in *Figure 2*).

The switches are labeled **SW1**–**SW10** starting from the left side. The switch settings are listed in *Table 5*.

Table 5 Configuration (DIP) Switches and Functions (Sheet 1 of 2)

Switch	Function		
1	ON = Password protection disabled (only use to reset password when lost) OFF = Password protection enabled		
2	ON = **ALARM** output does not include loss of satellite lock OFF = Adds loss of satellite lock to **ALARM** output		
3	IRIG-B output format ON = extended IRIG-B000 format (odd parity) OFF = standard IRIG-B002 format		
4-9	Local Time Offset:		
	4	ON	= add to UTC
		OFF	= subtract from UTC
	5	ON	= 8 hour increment
	6	ON	= 4 hour increment
	7	ON	= 2 hour increment

Table 5 Configuration (DIP) Switches and Functions (Sheet 2 of 2)

Switch	Function		
	8	ON	= 1 hour increment
	9	ON	= 1/2 hour increment
10	Daylight-Saving Time (DST) ON = daylight-saving time enabled OFF = daylight-saving time disabled		

Setting Local Time

NOTE: All time configuration switches have a delayed activation time (five seconds), which prevents false time outputs during setting changes while the clock is powered. The clock does not implement a new setting until five seconds after the last switch change.

Control the time format with Switches **SW4**–**SW9**. The clock output is UTC time when these switches are all in the **OFF** position. To set for local time, you need to set the appropriate offset from UTC time.

Always set the clock to the local region standard time (for daylight-saving time, see *Daylight-Saving Time (DST)*). Set Switches **SW4**–**SW9** incrementally. For example, to set the local time to EST (Eastern Standard Time) use the following settings:

SW4 = **OFF** (subtract from UTC time)
SW5 = **OFF**
SW6 = **ON** (4 hour)
SW7 = **OFF**
SW8 = **ON** (1 hour)
SW9 = **OFF**

These settings will set local time to five hours less than UTC time.

Daylight-Saving Time (DST)

The clock supports five modes of DST: Off, Manual, Custom, and two preset modes (EU, USA). Switch **SW10** determines whether the clock adjusts for DST, and it must be switched to **ON** for any of the modes other than Off to operate. The status page **SW10** setting reflects the current DST mode.

Preset DST

The clock has two preset DST configurations: European Union (EU) and North America (USA). When **SW10** is **ON** and a custom DST is not set, the clock determines from its GPS location whether one of these modes should be selected.

If the GPS location is within the area boundary defined by the latitude and longitude, then that preset DST rule is enabled. The status page under **SW10** will reflect either Auto (EU) or Auto (USA). *Table 6* defines the preset DST conditions and when the offsets will be active.

Table 6 Preset DST Conditions

DST Rule	Latitude	Longitude[a]	Start Month	Start Week	Start Day	Start Hour	End Month	End Week	End Day	End Hour
North America (USA)	15° N → 89°N	–170° → –60°	Mar	2nd	Sunday	2:00 a.m. Local	Nov	1st	Sunday	2:00 a.m. Local
European Union (EU)	35° N → 89°N	–15° → 40°	Mar	Last	Sunday	1:00 a.m. UTC	Oct	Last	Sunday	1:00 a.m. UTC

[a] All longitude ranges West to East.

Custom DST

In all areas you can use the **DST ASCII** command to set the date and time when daylight-saving time begins and ends (see *DST Command (Access Level 1 and Access Level 2) on page 13*). This custom DST setting method must be used in areas outside the European Union and North America for implementing automatic daylight-saving time changes. For areas in the preset regions, the **DST** command changes the preprogrammed daylight-saving time, which is useful where local rules differ from regional practice. **SW10** must be set to ON for the Custom DST to operate, and the status page under **SW10** will reflect Auto (CUSTOM).

Manual DST

When the clock is operating without a custom daylight-saving time and outside of the defined preset DST areas, the clock is operating in manual DST mode. This will be reflected on the status page under **SW10** as MANUAL. **SW10** advances the time manually by one hour when ON. When you set Manual DST, the time changes when the seconds output reaches 00 after crossing 25 seconds. For example, if the present time is 08:09:10 when the DST setting is turned to ON, the time will change at the end of the immediate minute from 08:09:59 to 09:10:00. However, if the current time is 03:45:28 when the DST setting is turned to ON, the time will change at the end of the following minute from 03:46:59 to 04:47:00. The clock uses this delay to transmit the correct IRIG-B time-change notification sequence to downstream relays and other equipment.

Serial Port Communications

The SEL-2404 has one EIA-232 serial port. The fixed serial port settings are shown in *Table 7*.

Table 7 Serial Port Configuration

Data Rate:	9600 bps
Data Bits:	8
Stop Bits:	1
Parity:	None
Port Time-Out:	15 minutes

Serial Port Connections

Pinout functions on the rear EIA-232 port are shown in *Table 8*. The SEL-2404 does not support hardware or software flow control.

Table 8 Pinout Functions (Sheet 1 of 2)

Pin	Function
Pin 1	N/C
Pin 2	RXD
Pin 3	TXD
Pin 4	N/C
Pin 5	GND
Pin 6	N/C

Table 8 Pinout Functions (Sheet 2 of 2)

Pin	Function
Pin 7	RTS
Pin 8	CTS
Pin 9	N/C

Communications Port Access Levels

Table 9 shows communications port commands available at each access level. There are two access levels (1 and 2); all commands available at Access Level 1 are also available at Access Level 2.

Table 9 Communications Port Command Summary

Serial Command	Access Level	Description
ACC	1	Enter access level
2AC	1	Enter second access level
BX	1	Set broadcast command on serial port
CON	1	Display time controlled pulses for **ALARM**
DST	1	Display custom daylight-time settings
LOC	1	Request current location of clock
STA	1	Request status of the GPS clock
TIM	1	Request local time
UTC[a]	1	Request UTC
BX	2	Set broadcast command on serial port
CLO	2	Close **ALARM** contact
CON	2	Time controlled pulses for **ALARM**
DST	2	Modify custom daylight-time setting
FTQ	2	Force time quality (expires after 18 hours)
L_D	2	Begin firmware upgrade process
OPE	2	Open **ALARM** contact
PAS	2	Change password
STA	2	Request clock status and satellite signal quality
TIM	2	Manually set clock time

[a] UTC = Universal Coordinated Time, also known as GMT, or Greenwich Mean Time.

NOTE: Unlike most SEL products, the SEL-2404 does not have an Access Level 0.

ACC (Access Level 1)

The default access level is Access Level 1. The => prompt indicates Level 1 access. There is no password protection at this level. Use the **ACC** command from Access Level 2 to return to Access Level 1.

2AC (Access Level 2)

Use the **2AC** command to enter Access Level 2. At this level, the clock issues a password prompt. Level 2 access is indicated with the =>> prompt. Note that all commands are available at this level.

SEL ASCII Commands

For all commands, use a terminal emulation program to enter the selected command string followed by a carriage return to issue the command to the clock. (A carriage return is not required for **BX** commands.)

BX Commands (Access Level 1 and Access Level 2)

The **BX** commands give the ability to control and format time broadcasts from the serial port from Access Level 1 and Access Level 2. These broadcast commands allow a way to synchronize a remote PC or RTU through a serial connection.

The broadcast commands have the format BX with X as the parameter.

X can have one of the following values:

- 0 = Deactivates broadcast mode
- 1 = Activates broadcast of ddd:hh:mm:ss
- 5 = Activates broadcast of l YY ddd:hh:mm:ss.000
- 6 = Activates broadcast of ddd:hh:mm:ss q
- 8 = Activates broadcast of YYYY:ddd:hh:mm:ss q
- L = Configures B1, B5, B6, and B8 broadcasts to send local time
- U = Configure B1, B5, B6, and B8 broadcasts to send UTC time

The following are definitions of the formats used above:

- YY = The last 2 digits of the year (00–99)
- YYYY = Date year (2000–9999)
- ddd = Day of the year (1–366)
- hh = Time hours (00–23)
- mm = Time minutes (00–59)
- ss = Time seconds (00–59)
- l = The satellite lock status, with the values:
  - <SP> = Locked
  - ? = Unlocked
- q = The time quality, with the values:
  - <SP> = Locked
  - . = <1 microsecond
  - * = <10 microsecond
  - # = <100 microsecond
  - ? = >100 microsecond

If the **B1** command is issued, then the SEL-2404 will enter time broadcast mode and respond with `ddd:hh:mm:ss` once per second. Note that any time the broadcast commands are issued they are not echoed to the screen.

```
=>B1 <Enter>
313:11:17:14
313:11:17:15
313:11:17:16
313:11:17:17
313:11:17:18
313:11:17:19
313:11:17:20
313:11:17:21
313:11:17:22
313:11:17:23
313:11:17:24
313:11:17:25
```

This broadcast will continue indefinitely until the cancel command **B0** is issued.

CLO Command (Access Level 2)

The **CLO** command forces the **ALARM** contact to the closed state and is reset when any key is pressed.

CON (Access Level 1 and Access Level 2)

The **CON** command forces the **ALARM** contact closed one or more times, beginning at a specified time, and with a specified duration. This command implements a precise alarm clock function with an accuracy of ±0.5 ms, and a pulse length that you set.

NOTE: Alarm contacts must be set to the open condition before the alarm clock function can be used. Enter the **CON ALARM** command without any other parameters to open the alarm contact. When the alarm clock function is no longer needed, enter **CON ALARM C** to restore normal contact operation.

The clock command is the following format:

CON ALARM [C] [xxxxx.xx] [[yyyy-mm-dd] [Thh:mm:ss.ff]] [P[dd] [Thh:mm:ss.ff]]

where:

- **C** cancels the command. When this parameter is present, the clock ignores all other parameters.
- **xxxxx.xx** is the pulse width in the range 0.01 to 64800.00 seconds. When this parameter is not present, the default pulse width is 1.00 seconds.
- **yyyy-mm-dd** is the pulse local start date. When this parameter is not present, the default start date is today.
- **Thh:mm:ss.ff** is the pulse local start time. When this parameter is not present, the default start time is the local time now.
- **PddThh:mm:ss.ff** is the pulse "period time" or repetition period in the range 0T00:00:00.05 to 99T23:59:59.99. When this parameter is not present, the clock outputs a single pulse (single shot). If the dd parameter is not present, then the clock assumes 0 days. If the Thh:mm:ss.ff parameter is not present, the clock assumes 00:00:00.00.

Once you have entered the **CON** command parameters to output a pulse, the clock displays a confirmation message similar to the following.

```
Alarm Clock Settings are:
Start Date    Start Time     Pulse Width    Repetition Period
yyyy-mm-dd    hh:mm:ss.ff    xxxxx.xx       hh:mm:ss.ff
or            or                            or
Today       Immediately                   Single Shot
Begin Precision Alarm Clock Operation on Alarm (Y,N)?
```

Answer **Y <Enter>** to begin Precision Alarm Clock operation. To return to Normal Alarm operation you must issue the **CON ALARM C** command.

Table 10 CON Command Examples

Command	Description
CON ALARM	Begin precision alarm clock operation
CON ALARM 5	Single shot 5-second pulse, starting immediately
CON ALARM T17:00:00	Single shot 1-second pulse, starting at 5:00 p.m. local time
CON ALARM 5.5 T8:29:24.5	Single shot 5.5-second pulse, starting at 8:29:24.50 a.m. local time
CON ALARM PT00:00:10	1-second pulse, repeating every 10 seconds, starting immediately
CON ALARM 0.05 PT00:00:00.25	0.05-second pulse, repeating every 0.25 seconds, starting immediately
CON ALARM T6:30:00 P1	1-second pulse, repeating daily, starting at 6:30:00 a.m. local time
CON ALARM 5 2006-9-25 T12:00:00 P7	5-second pulse, repeating weekly, starting at noon local time on September 25, 2006
CON ALARM 30 T8:00:00 PT1:00:00	30-second pulse, repeating hourly, starting at 8:00:00 a.m. local time
CON ALARM C	Cancel precision alarm clock operation

DST Command (Access Level 1 and Access Level 2)

At Access Level 1, the **DST** command shows the custom daylight-time setting that the clock uses to automatically advance and return local time by one hour.

```
=>DST <Enter>
Custom Daylight Saving Settings
Begin DST at 2:00 hours on the second Sunday of March
End DST at 2:00 hours on the third Sunday of October
=>
```

The SEL-2404 reports `Custom DST not set` and uses the values 99,9,9,99 when there is no valid custom daylight-time setting.

To change the custom daylight-time setting, you must enter Access Level 2 (see *2AC (Access Level 2) on page 11*). At Access Level 2, the **DST** command customizes the time when DST begins and ends. This custom method must be used in areas outside the European Union and North America for implementing automatic DST changes; for areas in the preset regions, the **DST** command changes the preprogrammed daylight time, which is useful where local rules differ from regional practice.

NOTE: The minimum separation for Begin DST and End DST settings is three hours.

For testing the beginning of daylight time, set the Begin DST time at the next hour change and the End DST time at least three hours in the future. For testing the end of daylight time, set the Begin DST time at least three hours in the past, and set the End DST time at the next hour change.

With these methods, you can examine whether time-consuming devices correctly apply the daylight-time changeover bits in the extended IRIG-B timecode (IRIG-B000).

The clock **DST** command uses the following format:

DST [C] [HH,N,D,MM] [hh,n,d,mm]

- **C** clears any previously set custom DST setting. When this parameter is present, the clock ignores all other parameters.
- **HH,N,D,MM** sets the DST begin rule
  - **HH** = local time hour to begin DST (range is 0–23)
  - **N** = week of the month to begin DST (range is 1–4, L [first, second, third, fourth, last])
  - **D** = day of week to begin DST (range: 1–7 [Sunday–Saturday])
  - **MM** = month to begin DST (range: 1–12 [January–December])

- **hh,n,d,mm** sets the DST end rule
  - **hh** = Local time hour to end DST (range: 0–23)
  - **n** = Week of the month to end DST (range: 1–4, L [first, second, third, fourth, last])
  - **d** = Day of week to end DST (range: 1–7 [Sunday–Saturday])
  - **mm** = Month to end DST (range: 1–12 ([January–December])

```
=>>DST <Enter>
Custom Daylight Saving Settings
Custom DST not set.
Setting ranges are:
hh = Hour, range 0-23 (local time hour)
n = Occurrence in month, range 1-3, L (first, second, third, last)
d = Day-of-week, range 1-7 (Sunday - Saturday)
mm = Month, range 1-12 (January - December)
Begin DST (hh,n,d,mm) 99,9,9,99 ? 02,2,1,03 <Enter>
End DST (hh,n,d,mm) 99,9,9,99 ? 02,1,1,11 <Enter>
Begin DST at 2:00 hours on the second Sunday of March
End DST at 2:00 hours on the first Sunday of November
Save changes (Y,N)? Y <Enter>
Settings Saved
=>>
```

Upon issuing the **DST** command, the clock reports the present setting and help information on how to correctly use the **DST** command. If you enter an incorrect range parameter, the clock replies with an invalid setting prompt. Once valid DST arguments are entered, the clock reports the new custom daylight-time changes and prompts you to save these new times. Answer **Y** to change to (new) custom daylight-time settings, or **N** to retain the previous settings.

Use the **DST C** command to clear both the Begin DST and End DST settings. This command returns the settings values to the default value, which enables manual DST operation outside of the European Union and North America regions.

Use the **DST C** command to clear both the Begin DST and End DST settings. This command returns the settings to the default value, which enables manual DST operation outside of the European Union and North America regions.

Use the custom DST setting for areas where time changes occurring at midnight must be set differently. For example, if DST is observed the second Saturday in October to the second Saturday in March, with the transitions at 24:00 local time, the DST setting would be set to the following:

DST 0,3,1,10 0,3,1,3

The local time hour change for daylight-saving time is at 24 but the setting only allows 0–23. In this case, the setting must be set one day ahead with an hour setting of 0. This change causes the day of the week to occur on Sunday which now is the third week of the month. The week of the month needs to be set to three instead of two because daylight-saving time occurs on Sunday at 0:00:00 local time.

FTQ Command (Access Level 2)

The **FTQ** command forces a selected value for the IEEE C37.118-2005 extensions time-quality output (useful for testing whether IEDs connected to the clock outputs are properly receiving and decoding these extended control function bits). To force a time-quality output, issue the command **FTQ *n***, where *n* represents a time-quality value shown in *Table 11*. To return to normal operation, issue the **FTQ C** (clear) command. The SEL-2404 forces and reports the time-quality values listed in *Table 11* during this testing mode. Only use the **FTQ** command when the clock is in Satellite Lock or manual time mode. The **FTQ** command will expire after 18 hours, and the SEL-2404 will then return to normal operation.

Table 11 Test Operation Forced Values for Time Quality

n	Time Quality	n	Time Quality
0	Locked	8	10 milliseconds
1	1 nanosecond	9	100 milliseconds
2	10 nanoseconds	10	1 second
3	100 nanoseconds	11	10 seconds
4	1 microsecond	12	100 seconds
5	10 microseconds	13	1000 seconds
6	100 microseconds	14	10000 seconds
7	1 millisecond	15	Fault

L_D Command (Access Level 2)

Use this command for upgrading firmware in the SEL-2404. See *Firmware Upgrade Instructions on page 28* for instructions on issuing this and other commands in the correct order to upgrade firmware.

LOC Command (Access Level 1 and Access Level 2)

At Access Level 1, the **LOC** command shows the current location of the GPS clock. If the clock is not satellite locked or does not have the previous location the latitude, longitude, and altitude will display all 0. The **LOC** command has two arguments to show two different formats of the GPS location. If the **LOC** command is entered either without any arguments or with the argument "D" the clock will display GPS coordinates as shown in *Figure 3*.

```
=>LOC <Enter>
46°44'33.069"N 117°11'25.794"W 00752.724m
=>
```

Figure 3 LOC Command Example Without Any Arguments

The **LOC** command is also set to send in a digital format that is easier for computers to read and parse. Issuing the **LOC** command with the argument "H" will display the clock's location in a digital format, as shown in *Figure 4*.

```
=>LOC H <Enter>
+464433.08–1171125.73+00755.623CRSWGS_84/
=>
```

Figure 4 LOC H Command Example For Digital Format

OPE Command (Access Level 2)

The **OPE** command forces the **ALARM** contact to the open state and is reset when any key is pressed.

PAS Command (Access Level 2)

The **PAS** command allows changing serial port access passwords.

The default password for Access Level 2 is TAIL. (Firmware versions earlier than R109 had a default password of ACCESS+2401.)

Passwords can include as many as 12 characters. The printable characters from the 7-bit ASCII set (i.e., values between 0x21 and 0x7e) are the only allowed password characters.

There is also a calibration access level used at the factory as a part of testing and validation procedures. There are no commands valuable to the user at this level. Some security processes require that there must be a means for security officers to change any password in the device. To change the CAL Level password, you must first move to the CAL Level. At Access Level 2, type CAL and, when prompted, the CAL Level password. The default Access Level CAL password is **CLARKE** (firmware versions before R109 had a default CAL access level password of **HAWKING+2401**). CAL Level access is indicated with the ==>> prompt.

R_S Command (Access Level 2)

Use the **R_S** command to restore factory default settings and passwords and reboot the system. This command is available only after a settings or critical RAM failure (after selected firmware upgrades).

STA Command (Access Level 1 and Access Level 2)

The **STA** command displays the clock status, firmware versions, and control (DIP) switch setting information.

The SEL-2404 provides additional firmware information in the Access Level 2 **STA** command.

```
=>>STA <Enter>

SEL-2401 Satellite Synchronized Clock     Local Date/Time = 2013-06-19  23:25:02
FID=SEL-2401-R200-V0-Z003002-D20130627    CID=60FF
RFID=SEL-2401-R103-V1-D20130531
RXFID=NAV-V00.00-D00000101 DSP-V00.00-D00000101 HW-0000-SN      0
SELF TEST RESULTS
RTL         FLASH       SDRAM       DISPLAY     ANTENNA     GPS_RX
OK          OK          OK          OK          OK          OK
SETTINGS
SW 1        SW 2        SW 3        SW 4-9        SW 10
PASSWORD    ALARM       IRIG FORMAT OFFSET        DST
Enabled    Enable      Extended    +0.0          Off

SATELLITES
   # Signal   # Signal   # Signal   # Signal   # Signal   # Signal   # Signal
  17  55.0   28  49.0   24  48.0    9  46.0    1  51.0    4  46.0   15  48.0
   # Signal   # Signal   # Signal   # Signal   # Signal   # Signal   # Signal
  26  50.0    8  42.0   12  46.0    3  20.0   11   0.0   20   0.0    -   -.-
STATUS
CLOCK       SIGNAL      ALMANAC
Enabled     Locked      Complete
=>>
```

Table 12 Status Report Elements (Sheet 1 of 2)

Report Element	Response
FID	FID is the firmware identifier string. It identifies the firmware revision.
CID	CID is the firmware checksum identifier.
RTL	Processor, OK or FAIL.
RFID	FPGA firmware identifier string.
RXFID	GPS receiver firmware ID.
FLASH, SDRAM	Memory status, OK or FAIL.
DISPLAY	Display panel status, OK or FAIL.

Table 12 Status Report Elements (Sheet 2 of 2)

Report Element	Response
ANTENNA	Antenna status, OK or FAIL.
GPS_RX	GPS receiver module status, OK or FAIL.
SW 1 PASSWORD	Required or Disabled.
SW 2 ALARM	Enable: ALARM output follows self-test result; Lock: adds satellite lock in ALARM output.
SW 3 IRIG FORMAT	Extended or Standard. (Extended = IEEE C37.118-2005 [1344] IRIG-B000 format; Standard = IRIG-B002 format.)
SW 4-9 OFFSET	Local Time Offset ±hh.h.
SW 10 DST	Daylight-Saving Time, OFF, AUTO (USA), AUTO (EU), AUTO (CUSTOM), or MANUAL.
# Signal	nn pp.p, where nn = GPS satellite number (0–32); pp.p = GPS signal power received in linear AMU.
CLOCK	Enabled or Disabled.
SIGNAL	GPS signal, Locked, Locking, Lock+UTC, Holdover, or None.
ALMANAC	Satellite tracking, Complete or Acquiring.

TIM Command (Access Level 1 and Access Level 2)

Use the **TIM** command to either view the current local time or to manually set the time if no satellite lock is available. This command is useful for either testing roll-over date and time values for downstream devices or for areas where a satellite lock is not available. At Access Level 1, the **TIM** command will respond with the local time in hours, minutes, seconds (hh:mm:ss). At Access Level 2, the **TIM** command allows you to manually set the date and time. Manually setting the date and time will set the clock display, enable IRIG-B outputs, and set the time quality to 4. The date can only be set between the years 2006 and 2100. Once the date and time are manually set the clock will not synchronize to the current date and time even if the clock picks up satellite lock. To clear the manual date and time either cycle power or issue the **TIM C** command. The **TIM** command has a time-out period of 18 hours. When the time-out expires, the clock returns to normal operation. The **TIM** command has the following parameters:

TIM [C] [YYYY-MM-DDThh:mm:s]

where:

- **C** cancels the user time setting.
- **YYYY-MM-DDThh:mm:ss** is the local date and time setting.
  - **YYYY** = 4 digit year
  - **MM** = Month
  - **DD** = Day of Month
  - **T** = Required separator between date and time
  - **hh** = Hour of day in 24-hour format
  - **mm** = Minutes
  - **ss** = Seconds

UTC Command (Access Level 1 and Access Level 2)

Upon receipt of the **UTC** command, the SEL-2404 responds with the UTC date, time, and local time offset in the following format (ISO 8601:2000):

YYYY-MM-DDThh:mm:ss±HH:NN

where:

- **YYYY** = UTC Date Year (2000–9999)
- **MM** = UTC Date Month (01–12)
- **DD** = UTC Day of the Month (01–31)
- **T** = Literal Time Separator
- **hh** = UTC Time Hours (00–23)
- **mm** = UTC Time Minutes (00–59)
- **ss** = UTC Time Seconds (00–60)
- **±HH** = Local Time Hours Offset including DST (–15 to +16)
- **NN** = Local Time Minutes Offset (00 or 30)

The **UTC *n*** command repeats the UTC time output for *n* times at the beginning of each second. A special case of this command is **UTC 0**, which outputs UTC time continually until the communications port receives any valid ASCII character (including **<Ctrl+X>**).

Applications

Timecode Distribution

The SEL-2404 has sufficient driving capacity to provide demodulated timecode signals to many products simultaneously.

Demodulated Timecode

Table 13 shows typical drive capabilities per demodulated BNC output for the SEL-2404 to other SEL equipment. The demodulated BNC outputs provide a standard IRIG-B00*x* dc level-shift (TTL) signal. The accuracy of this signal is ±100 ns, average. A series/parallel connection of SEL-100 and SEL-200 series products consists of two relays in series, with as many as 20 of these series pairs connected in parallel.

Table 13 Output Drive Capacity (Sheet 1 of 2)

Product	Connection	Input Impedance (Ohms)	Units Per SEL-2404 Output
SEL-100 Series	AUX INPUT (Conxall®)	56/82	2 parallel, 20 series/parallel[a]
SEL-200 Series	AUX INPUT	56/82	2 parallel, 20 series/parallel[a]
Legacy SEL-300 Series	(DEMODULATED) IRIG-B	333	10[b]
New SEL-300 Series	IRIG-B	1 K	10[b]
SEL-351R & SEL-351R Falcon	(DEMODULATED) IRIG-B	333	10[b]
SEL-400 Series	IRIG-B, serial port	2.5 K	20[c]

Table 13 Output Drive Capacity (Sheet 2 of 2)

Product	Connection	Input Impedance (Ohms)	Units Per SEL-2404 Output
New SEL-400 Series	IRIG-B, BNC	>1 K	20[d]
SEL-500 Series	(DEMODULATED) IRIG-B	333	10[b]
SEL-651R	IRIG-B	1.33 K	20[c]
SEL-700 Series	IRIG-B	4.5 K or 2.5 K[e]	20[c]
SEL-734	IRIG-B	2.5 K	20[c]
SEL-2032, SEL-2030, SEL-2020	IRIG-B (In) (BNC)	333	10[b]
SEL-2240	IRIG-B	2.5 K	20[c]
SEL-2411	IRIG-B	4.5 K or 2.5 K[e]	20[c]
SEL-2414	IRIG-B	4.5 K or 2.5 K[e]	20[c]
SEL-2431	IRIG-B	750	10[b]
SEL-2440	IRIG-B	2.5 K	20[c]
SEL-2523, SEL-2533	IRIG-B	2.5 K	20[c]
SEL-2810MT	IRIG-B	25 K	20[c]
SEL-2812MT	IRIG-B	2 K	20[c]
SEL-3031	IRIG-B	333	10[b]
SEL-3350 Series, SEL-3530, SEL-3610, SEL-3620, SEL-3622	IRIG-B	2.5 K	20[c]
SEL-3401 manufactured before Sept. 2011	IRIG-B (In)	332	10[b]
SEL-3401 manufactured Sept. 2011 or later	IRIG-B (In)	1.3 K	15[b]

[a] Do not add external terminating resistor.
[b] Install 50-ohm termination resistor on farthest device for 4 or fewer devices.
[c] Install 50-ohm termination resistor on farthest device.
[d] Set internal 50-ohm termination resistor on farthest device.
[e] 2.5 kilohm if no Ethernet or single copper Ethernet port; 4.5 kilohm if fiber-optic or dual Ethernet port(s).

The maximum cable length is 40 m (131.2 ft). Connect multiple devices as illustrated in *Figure 5*.

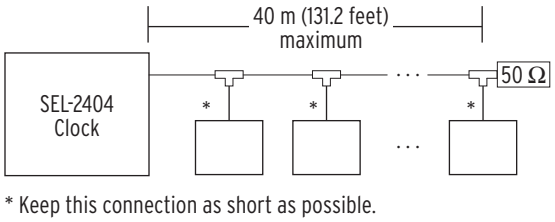


Figure 5 Multiple Device Connections

Testing and Troubleshooting

Troubleshooting Procedure

Troubleshooting procedures for common problems are listed in *Table 14*. The table lists each symptom, possible causes, and corresponding solutions.

Table 14 Troubleshooting Procedures

Symptom	Possible Cause/Solution
Digital clock will not light.	Check to ensure power is applied.
Digital clock loses time when power is cycled.	Check the 3 V coin cell battery, and replace if needed. See *Battery Replacement* procedure.
Year does not display correctly.	See *Year-Source Selection* for year-display discussion.

Self-Test

The SEL-2404 has a display self-test that is activated by pressing the **ADV** pushbutton for more than three seconds. When in display-test mode, each LED position counts from 0 to 9, illuminating the appropriate segments. After the last display test, the SEL-2404 returns to normal operation.

While in display-test mode, pressing the **ADV** pushbutton ends the test and restores normal operation.

Battery Replacement

A battery maintains the time if the external power is lost or removed. The battery is a 3 V Lithium-Carbon Monofluoride coin cell, IEC No. BR2335 or equivalent. At room temperature (25°C), the battery lasts for at least two years if there is no other power to the SEL-2404. The battery cannot be recharged.

⚠CAUTION
There is danger of explosion if the battery is incorrectly replaced. Replace only with Rayovac no. BR2335 or equivalent recommended by manufacturer. Dispose of used batteries according to the manufacturer's instructions.

To change the battery, perform the following steps:

Step 1. Remove the power (and the IRIG-B signal, if applied) from the SEL-2404.

Step 2. Remove the top-panel screws and top panel.

Step 3. Locate the battery in the middle of the circuit board.

Step 4. Remove the battery from beneath the clip and install a new one being careful not to deform the clip.

The positive side (**+**) of the battery faces up.

Step 5. Replace the rear panel and rear-panel screws and tighten securely.

Step 6. Apply power to the SEL-2404.

Fuse Replacement

If it appears that there is no power to the device (for example, all display indications are off), take the following steps:

Step 1. Verify that a supply voltage within the power supply range is present on terminals **4** and **5**.

Step 2. If the correct supply voltage is present, remove power and then remove the SEL-2404 from the equipment rack.

Step 3. Remove the top cover.

Step 4. Replace the fuse with a BUSS S505 2 A (ceramic), Schurter T 2 A 250 V, or equivalent.

Step 5. Replace the top cover.

Step 6. Reinstall the SEL-2404 and apply power.

The Clock Will Not Lock

When the clock is unable to achieve a lock, the cause of such failure typically is poor receive signal quality. For the clock to lock, the receiver must initially track at least four satellites (not all in the same region). To remain locked, the clock must be tracking at least one satellite.

To verify the signal, use the serial port **STA** command from Access Level 2.

Step 1. Verify that the antenna connection displays `OK` in the status window.

If `OK` is not displayed, check the connections and the cable for short or open circuits.

Step 2. Verify the signal quality of the tracked satellites.

There are 14 satellite-status fields listed near the bottom of the status screen. The first two-digit number is the GPS satellite designator for each tracked satellite. The next number is the carrier-to-noise density in dB-Hz.

To achieve lock, the clock must have a minimum reading of 30 dB-Hz on four satellites. If carrier-to-noise density does not meet these requirements, the signals are too weak. After lock is achieved, the clock must have the minimum reading on at least one satellite.

If signals are too weak to achieve lock, perform the following steps:

Step 1. Reposition the antenna for an unobstructed view of the sky.

Step 2. Verify that the antenna cable or external lightning protection is not damaged.

Mechanical Diagrams

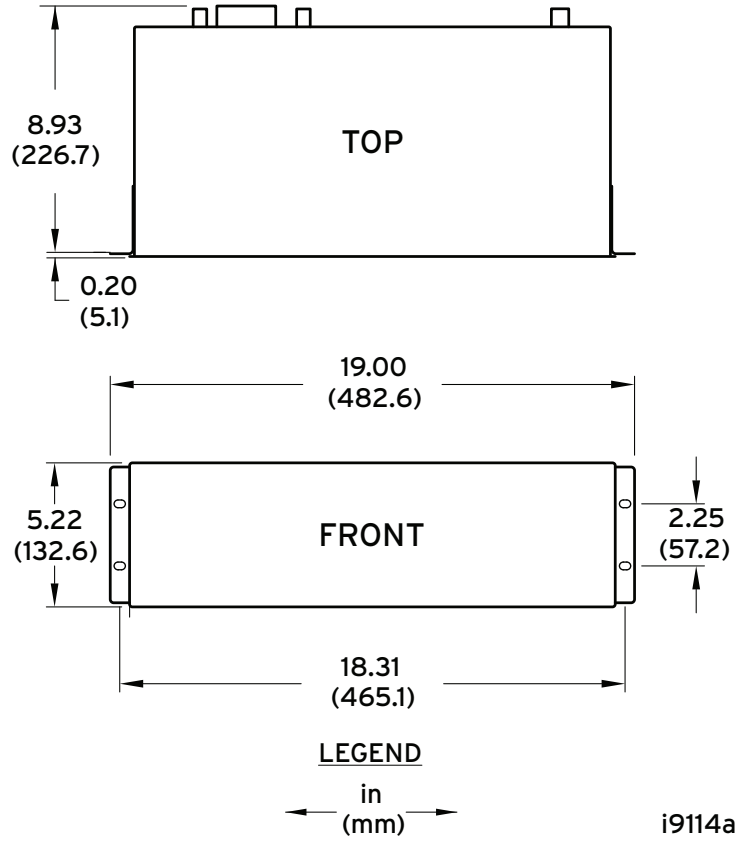


Figure 6 SEL-2404 Rack-Mount Dimension Drawing

Specifications

Compliance

ISO 9001: This product was designed and manufactured under an ISO 9001 certified quality management system.

FCC CFR 47 Part 15, Class A

Note: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area may be likely to cause harmful interference in which case the user will be required to correct the interference at his own expense. Any changes or modifications not expressly approved by the manufacturer can void the user's authority to operate the equipment.

Receiver

Satellite Tracking:	GPS L1, C/A Code (1575.42 MHz) 14-channel continuous tracking

Acquisition Times

Hot Start:	135 s
Cold Start:	135 s + UTC compensation time
Typical Cold Start:	330 s

Clock Accuracy (to UTC Time)

Demodulated IRIG-B:	±100 ns average, ±500 ns peak

Holdover Stability

±0.08 ppm for 20 minutes (from –40° to +80°C)

Antenna Requirements

5 V, <80 mA

25 dB preamp, min. LNA gain

IRIG-B Output Drive Level

Demodulated IRIG-B000 and IRIG-B002,
TTL: 5 V nominal, 4 V (min) into 50 Ω

ALARM Contact

Form A Carry:	100 mA
Rated Voltage:	48 Vdc

Note: The alarm contact is open during an alarm condition.

Power Supply

Rating:	125 Vdc or Vac
Range:	75–250 Vdc or Vac
Burden:	< 10 W

Serial Port

EIA-232 DB-9 Female

Fixed 9600 bps

8 data bits

No parity

1 stop bit

15-minute timeout, fixed

Operating Temperature Range

–40° to +80°C (–40° to +176°F)

Humidity

0% to 95% without condensation

Altitude

2000 m (6,600 ft.)

Unit Weight

2 kg (4 lb 6 oz)

Dimensions

132.6 mm H x 482.6 mm W x 226.7 mm D
(5.22 in. H x 19.00 in. W x 8.93 in. D)

Type Tests

Electromagnetic Compatibility

EN 50263:1999 [BS EN 50263:2000]

Electromagnetic Compatibility Emissions

IEC 60255-25:2000
[BS EN 60255-25:2000]
EN 55011:1998 + A1:1999 + A2:2002
FCC CFR 47 Part 15, Class A

Electromagnetic Compatibility Immunity

All tests were conducted using a 10 meter long serial port.

Conducted RF Immunity:	IEC 60255-22-6:2001 IEC 61000-4-6:2008 Severity Level: 10 Vrms
Electrostatic Discharge Immunity:	IEC 60255-22-2:2008 IEC 61000-4-2:2008 Severity Level: 2, 4, 6, 8 kV contact; 2, 4, 8, 15 kV air IEEE C37.90.3-2001 Severity Level: 2, 4, 8 kV contact; 4, 8, 15 kV air
Fast Transient/Burst Immunity:	IEC 60255-22-4:2008 IEC 61000-4-4:2011 Severity Level: 4 kV @ 2.5 kHz and 5 kHz on EIA-232 Port, antenna input, and IRIG output
Radiated Radio Frequency Immunity:	IEC 60255-22-3:2007 IEC 61000-4-3:2010 Severity Level: 10 V/m IEEE C37.90.2-2004 Severity Level: 35 V/m
Surge Immunity:	IEC 60255-22-5:2008 IEC 61000-4-5:1995 + A1:2005 Severity Level: 0.5 kV, 1.0 kV line-to-earth on EIA-232 Port, antenna input, and IRIG output
Surge Withstand Capability Immunity:	IEC 60255-22-1:2007 Severity Level: 1.0 kV peak common mode on EIA-232 Port, antenna input, and IRIG output IEEE C37.90.1-2002 Severity Level: 2.5 kV oscillatory; 4 kV fast transient waveform on EIA-232 Port, antenna input, and IRIG output

Environmental

Cold:	IEC 60068-2-1:2007 Severity Level: 16 hours @ –40°C
Dry Heat:	IEC 60068-2-2:2007 [BS EN 60068-2-2:1993 + A1:1995] Severity Level: 16 hours @ +80°C

Damp Heat Cyclic:	IEC 60068-2-30:2005 Severity Level: 25° to 55°C, 6 cycles, Relative Humidity: 95%
Object Penetration and Dust Ingress:	IEC 60529:2001 + CRGD:2003 Severity Level: IP30
Vibration:	IEC 60255-21-1:1988 Severity Level: Class 1 (Endurance), Class 2 (Response) IEC 60255-21-2:1988 Severity Level: Class 1 (Shock Withstand, Bump); Class 2 (Shock Response) IEC 60255-21-3:1993 Severity Level: Class 2 (Quake Response)

Safety

Dielectric Strength:	IEC 60255-5:2000 IEEE C37.90-2005 Severity Level: 2500 Vac on contact inputs, contact outputs, and analog inputs. Type tested for 1 minute.

Firmware and Manual Versions

Determining the Firmware Version

To determine the firmware version, view the status report by using the serial port **STATUS** command or the front-panel **STATUS** pushbutton. The status report displays the Firmware Identification (FID) number.The firmware version number is after the R, and the release date is after the D. For example, the following is firmware version number R106, release date April 11, 2007.

FID=SEL-2401-R106-V0-Z002002-D20070411

NOTE: The SEL-2404 uses SEL-2401 firmware, so "SEL-2401" is used throughout the manual when discussing firmware.

The firmware version will be either a standard release or a point release. A standard release adds new functionality to the firmware beyond the specifications of the existing version. A point release is reserved for modifying firmware functionality to conform to the specifications of the existing version.

A standard release is identified by a change in the R-number of the device FID number.

Existing firmware:

FID=SEL-2401-**R100**-V0-Z002002-Dxxxxxxxx

Standard release firmware:

FID=SEL-2401-**R101**-V0-Z00 1001-Dxxxxxxxx

A point release is identified by a change in the V-number of the device FID number.

Existing firmware:

FID=SEL-2401-R100-**V0**-Z001001-Dxxxxxxxx

Point release firmware:

FID=SEL-2401-R100-**V1**-Z001001-Dxxxxxxxx

The release date is after the D. For example, the following is firmware version number R100, release date December 10, 2003.

FID=SEL-2401-R100-V0-Z001001-**D20031210**

Table 15 300-Series Firmware Revision History

Firmware Identification (FID) Number	Summary of Revisions	Manual Date Code
SEL-2401-R300-V0-Z003002-D20190725 **Note:** R300-series firmware uses new hardware and is not compatible with R200-series or R100-series firmware.	➤ Introduced new clock hardware on R300 firmware. Not backwards compatible.	20191119

Table 16 200-Series Firmware Revision History

Firmware Identification (FID) Number	Summary of Revisions	Manual Date Code
SEL-2401-R200-V0-Z003002-D20130627 **Note:** R200-series firmware uses new hardware and is not compatible with R100-series firmware.	➤ Introduced new clock hardware on R200 firmware. Not backwards compatible with R100-series firmware. ➤ Increased antenna receiver sensitivity. ➤ Updated satellite locking mechanism so that the clock can remain locked with only one satellite in view after initial satellite lock.	20130627

Table 17 100-Series Firmware Revision History

Firmware Identification (FID) Number	Summary of Revisions	Manual Date Code
SEL-2401-R112-V1-Z003002-D20190604	➤ Updated firmware to correctly set the date if installed following the recent GPS rollover event experienced on April 6, 2019.	20190604
SEL-2401-R112-V0-Z003002-D20130828	➤ Corrected leap second time adjustment where time jumped by one minute and one second for a duration of one second, then returned to the correct time. ➤ Corrected leap second time adjustment where SBS had incorrect seconds during the leap second change. ➤ Modified leap second time adjustment so that the Leap Second Pending bit in IRIG-B now triggers one minute before the time change at zero seconds. ➤ Corrected issue where, if a leap second occurred on a Saturday, the clock would increment/decrement the time by one second one week early. ➤ Corrected the status command signal indication when the clock loses lock. ➤ Corrected error that would have occurred on September 15, 2024 where the clock would have rolled back to January 30, 2005 because of an internal week rollover. ➤ Corrected certain custom **DST** command rules that would not increment or decrement the hour properly. ➤ Revised the **TIM** command to require complete date and time as arguments when setting manual time. ➤ Revised the **STA** command to show satellite signals in dB-Hz. ➤ Revised custom DST settings to be a command interface and no longer a settings prompt. ➤ Corrected signal-level mask change incorrectly introduced in R111.	20130828
SEL-2401-R111-V0-Z002002-D20130315	➤ Corrected condition where clock can lock exactly one second off. ➤ Added **LOC** command for obtaining clock's GPS coordinates.	20130315
SEL-2401-R110-V0-Z002002-D20120309	➤ Corrected issue where the clock could send out the incorrect seconds for as long as 5 seconds before correcting itself. ➤ Corrected issue where the clock may not obtain satellite lock on cold start. Cycling power would previously fix this problem. ➤ Corrected issue where the clock may not send out time for a few seconds.	20120309
SEL-2401-R109-V0-Z002002-D20110408	➤ Added ability to update GPS chip firmware via SEL firmware upgrade to correct rare failure to reacquire satellite lock condition. ➤ Changed 2AC and CAL default passwords.	20110408
SEL-2401-R108-V0-Z002002-D20100504	➤ Corrected very rare condition of the clock getting exactly one second slow for a brief time before automatically correcting itself. ➤ Corrected condition where new leap second would update one day late.	20100504
SEL-2404-R107-V0-Z002002-D20080515	➤ Corrected UTC hour display for GPS clocks located in the positive local time zones.	20080515
SEL-2401-R106-V0-Z002002-D20070411	➤ Made the alarm contact initialize correctly when Control (DIP) Switch 2 is on.	20070411
SEL-2404-R105-V0-Z002002-D20070209	➤ Made the alarm contact work properly when set to change state from loss of satellite lock. ➤ Corrected upgrading firmware where the status Flash would show a FAIL until the satellite obtained initial satellite lock.	20070209
SEL-2404-R104-V0-Z002002-D20061220	➤ Added the following ASCII commands: **BX** broadcast, **CON** time controlled pulses, and **TIM** manually set date and time.	20061220
SEL-2404-R103-V0-Z002001-D20061002	➤ Added the **CON** command.	20061002
SEL-2404-R102-V0-Z001001-D20060608	➤ Added the custom **DST** command. ➤ Added the **UTC *n*** command. ➤ Added CE compliance.	20060608
SEL-2404-R101-V0-Z000000-D20060206	➤ Initial version.	20060206

Determining the Firmware Version of the Digital Display

To find the firmware revision number in your digital display, press and hold the **12/24 HOUR** pushbutton for longer than three seconds. The SEL-2404 displays the Firmware ID (FID) on the six, 7-segment LEDs until the pushbutton is released.

The firmware release date is after the "R." The following example shows the firmware release date as the 18th day of 2007.

R07018

Table 18 lists the display firmware versions, a description of modifications, and the instruction manual date code that corresponds to firmware versions. The most recent firmware version is listed first.

Table 18 Firmware Revision History

Firmware Identification (FID) Number	Summary of Revisions	Manual Date Code
R07018	➤ Manual update only. See *Table 17*.	20110805
R07018	➤ Manual update only. See *Table 17*.	20090508
R07018	➤ Initial version.	20071101

Determining the Manual Version

The date code at the bottom of each page of this manual reflects the creation or revision date.

Table 19 lists the product manual release dates and a description of modifications. The most recent product manual revisions are listed at the top.

Table 19 Manual Revision History (Sheet 1 of 2)

Revision Date	Summary of Revisions
20191119	➤ Added *Antenna Cabling*. ➤ Updated *Cold-Start Sequence*. ➤ Updated *IRIG-B Output Drive Level* in *Specifications*. ➤ Updated for firmware version R300-V0.
20190604	➤ Updated for firmware version R112-V1.
20180215	➤ Updated *Table 2: Control (DIP) Switches and Functions.* ➤ Updated *Settings*. ➤ Added FTQ command to *Table 9: Communications Port Command Summary.* ➤ Updated *Daylight-Saving Time (DST).* ➤ Added *FTQ Command (Access Level 2).*
20141103	➤ Removed CE Mark certification from *Specifications*.
20130828	➤ Updated for firmware version R112.
20130627	➤ Updated *Accessories*. ➤ In *Table 1: Status Indicator LEDs*, *Cold Start Sequence*, and *Testing and Troubleshooting*, changed number of satellites needed to stay locked from three to one. ➤ Updated *DST Command (Access Level 1 and Access Level 2).* ➤ Updated example screen capture in *STA Command (Access Level 1 and Access Level 2).* ➤ Updated *Table 11: Output Drive Capacity.* ➤ Updated *Specifications*. ➤ Updated for firmware version R200.

Table 19 Manual Revision History (Sheet 2 of 2)

Revision Date	Summary of Revisions
20130315	➤ Added LOC serial command in *Table 9: Communications Port Command Summary.* ➤ Added *LOC Command (Access Level 1 and Access Level 2)*. ➤ Updated for firmware version R111.
20120309	➤ Updated for firmware version R110.
20110805	➤ Updated description for **STA** serial command in *Table 11: Communications Port Command Summary.* ➤ Added detail about **STA** command display in *STA Command (Access Level 1 and Access Level 2)*. ➤ Updated *Table 3*: *Output Drive Capacity* with additional information about SEL-3401 manufacture dates.
20110408	➤ Updated antenna bracket number in *Table 2: GPS Signal-Acquisition Accessories*. ➤ Added new devices SEL-2240 and SEL-2533 in *Table 3: Output Drive Capacity.* ➤ In *Table 4: Status Indicator LEDs*, *Cold Start Sequence*, and *Testing and Troubleshooting*, clarified that four satellites are needed to achieve lock, and three are needed to stay locked. ➤ Changed default **2AC** password and added instruction to change the **CAL** password in *PAS Command*. ➤ Removed restriction on when **TIM** command could be used in *TIM Command*. ➤ Deleted obsolete safety standard reference in *Specifications*. ➤ Updated for firmware version R109. ➤ Add GPS firmware upgrade description in *Firmware Upgrade Instructions*.
20100504	➤ Updated for firmware version R108.
20091120	➤ Updated *Table 3: Output Drive Capacity.* ➤ Updated *Specifications*.
20090508	➤ Updated/added time accessory products.
20071101	➤ Initial version.

Firmware Upgrade Instructions

Overview

SEL may occasionally offer firmware upgrades to improve the performance of your clock. The clock stores firmware in Flash memory; therefore, changing physical components is not necessary. A firmware loader program called SELBOOT resides in the clock. These instructions give a step-by-step procedure to upgrade the clock firmware by uploading a file from a personal computer to the clock via direct connection to the serial port.

Required Equipment

You will need the following to perform a firmware upgrade.

- ➤ Personal computer
- ➤ Terminal emulation software that supports the Xmodem/CRC protocol (these instructions use HyperTerminal® from a Microsoft Windows operating system)
- ➤ Serial communications cable (SEL-C234 or equivalent)
- ➤ The firmware upgrade file (Rxxx24*nn*.hex), where *nn* indicates the last two digits in the product number

Firmware Upgrade Procedure

Step 3. Connect the PC to the serial port of the clock, and enter Access Level 2.

a. Type **2AC <Enter>**.

b. Enter the Access Level 2 password.

Step 4. Record DST settings.

a. If the clock has custom daylight-saving time settings, issue the **DST** command.

b. Record the beginning and ending daylight-saving time settings.

Step 5. Start the upgrade process.

a. Issue the **L_D** command to the clock.

b. Type **Y <Enter>** at the following prompt:

```
Disable device to receive firmware (Y,N) ?
```

c. Type **Y <Enter>** at the following prompt:

```
Are you sure (Y, N)?
```

The clock will respond with the following message and send the !> prompt.

```
Device disabled
```

Step 6. Issue the **BAU 115200** command. On your terminal or computer terminal program, change the terminal data rate to 115200 bps.

Step 7. Begin the transfer of the new firmware to the clock by issuing the **REC** command.

Step 8. Type **Y** to erase the existing firmware, or press **<Enter>** to abort.

Step 9. Start the file transfer.

a. Select the send file option in your communications software.

b. Use the Xmodem or 1K Xmodem protocol and send the file that contains the new firmware.

After the file transfer, the clock reboots.

Step 10. If the GPS firmware in the SEL firmware is newer than the firmware in the GPS chip, the clock will upgrade the GPS firmware. While the firmware upgrade is progressing, several progress messages will be sent to the serial port.

On completion of the GPS firmware upgrade, or if there is no GPS firmware upgrade, the **ENABLED** LED will illuminate and the clock will return to Access Level 1. Change the terminal data rate back to 9600 bps to access the clock serial port.

Step 11. Press any key (e.g., **<Enter>**) when the clock sends a prompt.

```
=>>L_D <Enter>
Disable device to receive firmware (Y,N) ? <Enter>
Are you sure (Y/N) ? Y <Enter>
Device disabled

!>REC <Enter>
Caution! This command erases the firmware.
If you erase the firmware then new firmware
must be loaded before returning the IED to service.

Are you sure, you want to erase the existing firmware (Y/N)?
Y <Enter>
Erasing firmware.
Erase successful.
Press any key to begin transfer and then start transfer at the terminal.
```

Step 12. Issue the **STA** command, and examine the status report for FAIL messages.

If the status report shows a FAIL message, perform the following steps.

a. Type **2AC <Enter>**.

b. Enter the Access Level 2 password.

c. Type **R_S <Enter>**.

At this time you must reenter passwords and any custom daylight-saving time settings.

NOTE: The Access Level 2 password for an SEL-2401 Clock is **TAIL**.

d. Type **2AC <Enter>**.

e. Enter the default Access Level 2 password **TAIL**.

f. Use the **PAS** command to change the default password to your secure password (see *PAS Command (Access Level 2)* in the *SEL-2404 Instruction Manual*).

g. If necessary, use the **DST** command to set the beginning and ending times for daylight-saving (summer) time (see *DST Command (Access Level 1 and Access Level 2)* in the *SEL-2404 Instruction Manual*).

This completes the firmware upgrade instructions. If you have a field-programmable gate array (FPGA) configuration upgrade, continue with the following procedure.

FPGA Configuration Upgrade Procedure

Step 1. Connect the PC to the serial port of the clock, and enter Access Level 2.

Step 2. Start upgrading of firmware.

a. Issue the **L_D** command to the clock.

b. Type **Y <Enter>** at the following prompt:

```
Disable device to receive firmware (Y,N) ?
```

c. Type **Y <Enter>** at the following prompt:

```
Are you sure (Y, N)?
```

The clock will respond with the following message and send the !> prompt.

```
Device disabled
```

Step 3. Issue the **BAU 115200** command, and change the terminal baud rate to 115200 bps.

Step 4. Begin the transfer of the new FPGA configuration file (rtl_Rxxx) to the clock by issuing the **CFG** command.

Step 5. Type **Y** to erase the existing FPGA configuration or press **<Enter>** to abort.

Step 6. Press any key (e.g., **<Enter>**) when the clock sends a prompt.

Step 7. Start the file transfer.

a. Select the send file option in your communications software.

b. Use the Xmodem protocol and send the file that contains the new firmware.

After the file transfer, the clock will reboot and return to Access Level 1. Change the terminal data rate back to 9600 bps to access the clock serial port.

```
=>>L_D <Enter>
Disable device to receive firmware (Y,N) ? Y <Enter>
Are you sure (Y/N) ? Y <Enter>
Device disabled

!>CFG <Enter>
Caution! This command overwrites the FPGA configuration and SELboot.

Are you sure, you want to overwrite
the existing FGPA configuration and SELboot (Y/N)? Y <Enter>

Press any key to begin transfer and then start transfer at the terminal.
```

The satellite-synchronized clock is now ready for commissioning.

Technical Support

We appreciate your interest in SEL products and services. If you have questions or comments, please contact us at:

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Notes

⚠CAUTION This terminal is for use with a regulated +5 Vdc source only.	**⚠ATTENTION** Cette borne doit être raccordé uniquement une source de tension régulée de +5 VCC.
⚠WARNING Operator safety may be impaired if the device is used in a manner not specified by SEL.	**⚠AVERTISSEMENT** La sécurité de l'opérateur peut être compromise si l'appareil est utilisé d'une façon non indiquée par SEL.

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