

Malware Message Classification by Dynamic Analysis

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Abstract. The fact that new malware appear every day demands a strong response from anti-malware forces. For that sake, an analysis of new samples must be performed. Usually, one tries to replay the behavior of malware in a safe environment. However, some samples activate a malicious function only if they receive some particular inputs from its command and control server. The problem is then to get some grasp on the interactions between the malware and its environment. For that sake, we propose to work in four steps. First, we enumerate all possible execution path following the reception of a message. Second, we describe for all execution path the set of corresponding messages. Third, we build an automaton that discriminate types of runs given an arbitrary word. Finally, we unify some equivalent run, and simplify the underlying automaton.

1 Introduction

The essential problem of an anti-virus software is to discriminate malware from safe programs. There are broadly two ways to perform such a task: by a syntactic analysis of the code or by a behavioral one. The first one is the key of standard anti-virus software. Indeed, the principle takes benefit from optimized pattern matching algorithms and may be tuned to avoid false positives. On the other hand, they are very sensitive to malware mutations, and thus may be easily fooled.

Behavioral analyses try to discriminate good usages from bad ones. Usually, this is done by recording interactions of the program with its operating system: file modifications, access to some drive, memory rights alteration and so on. Good/Bad behaviors are modeled by state automata or by temporal logical formulae [18, 22]. Though attracting in theory, such systems suffer from an heavy slow down of the monitored machines. Furthermore, it is not that easy to distinguish good from bad behaviors leading to high false negative/positive rates. And finally, it is not that complicate to make behaviors mutate.

In this contribution, we propose an alternative form of behavioral analysis which focuses on the interactions between a program with its environment via messages. It results more technically in an analysis of the structure of input messages as read by programs. This is called *message format extraction*.

Message format extraction can be considered also in the context of retro-engineering as a first and inevitable step to understand the communication protocols used in the malicious code. In the pioneer work [5], the authors described a method to extract formats of received messages by a pattern analysis. Using similar techniques, this work was pursued later on for respectively sent messages [10], encrypted ones [14] or hierarchically-structured ones [8, 11], and finally for communication protocols [11].

In this line of work, messages are modeled as fields sequences. The main issue is then to detect the fields boundaries in a message. The problem is even more complicated due to fields (qualified as *directed*) which may contain informations about other fields (via length measure for instance). The authors developed algorithms that detect direction fields and *separators*. However, to do that, they make a strong assumption on the parsing algorithm of the target program itself. If one replaces the naive string matching algorithm with a more sophisticated one (e.g. Knuth-Morris-Pratt), the assumptions (described in these interpretations) are no more valid and thus the results.

Our approach relies on a different paradigm. We state that the structure of input messages can be described (or at least approximated) by finite state automata. Suppose we are given a program P which reads some input m at run time, then we provide a finite state automaton which will categorize input messages according to their corresponding execution. Actually, we will show experimentally that one retrieves the shape of messages from the automaton. Second difference, we do not make any hypothesis on the parsing algorithm of the program P .

However, from the idea to its concrete realization, there are some preliminary steps that must be crossed. Suppose we want to study the communications of a malware P . First, one must identify which instructions open a communication. This is not computable in general. But, for self-modifying program—that is almost all malware—standard heuristics will fail. This is a first reason to work dynamically. Second reason, suppose P opens a communication with its Command and Control (C&C). In that case, the C&C behaves like an oracle from our perspective and there is no way to get a simple sample of a message. Letting the communication open (and the program run) gives us access to a (hopefully informative) message from the other side.

But, in the scenario above, there are two issues. First, the C&C server may be closed. Malware managers move very quickly servers to escape traffic surveillance. Second, launching P may be observed by the C&C, thus breaking the stealth of the analysis. In the experiment we report here, we opened the communication *only once*. This can be seen as a kind of trade-off between stealth and information. However, the method we develop cope with the all scenarios, fully open or fully closed communications and all in-between ones.

So, let us suppose we run a program P which at some point receives some data. As said above, we will build an automaton which is fed by some byte sequence w (in other words, a message) will output a state witnessing the execution

path in P of w . Actually, states of the automaton describe the analysis of the message by the program P , and consequently reveal the structure of messages.

To build the automaton, we proceed in four steps. First, we enumerate all execution paths from the reception instant that are reachable by message modification. This is known as a coverage issue in the context of software testing or in the context of malware retro-engineering. In recent times [6, 7, 21], the binary code is translated to some intermediate representation, and then some *concolic testing* [3] is performed with help of SMT solvers. Our approach is known as tainting driven fuzzing analysis which gave reasonably good coverage level for the experiments we made. But we already think for the near future to use techniques mentioned above, or some coming from disassembly such as the ones presented by Reps et al. [9] who developed CodeSurfer, or by Kinder and Veith [13], the authors of Jakstab, or the more recent Bardin et al. [17] which combines advantages of both latter methods.

For the second step, we proceed for each execution branch to a tainting analysis. For each conditional instructions (that are junctions in the execution tree), we describe a condition for each successor. Again, symbolic execution techniques could be used here, such as [16]. From these constraints, we build in a third step a dual automaton which serves as a basis for the fourth one. In the last step, we simplify the automaton by state sharing. Those are meant to fold execution loops.

We have implemented the constructions described all along, and we tested it on several programs, but notably on the well-known malware called **Zeus**. In the final Section, we present the result of the approach that we obtained on the malware.

Limitations In this contribution, we do not consider protocols. We work only on one message, not on the dialog between the program and its C&C. Thus, a malware could easily escape our analysis, simply by splitting messages. However, we think that a careful analysis of one message is a first step toward protocol analysis. Second limitation, we consider that the message analysis is done by some non-self-modifying code, globally, the code may be self-modifying, but not the parsing procedure itself. This limitation is not too harsh: during packer's decryption (that is when a malware is essentially self-modifying), there are few interactions with the environment. In his thesis, Calvet [20] showed that less than 2% of malware interact with the system during decryption. But, a code may interpret some commands sent by a C&C, and for those ones, our method is not operant. We let that issue for further researches.

Due to lack of space, we did not put the proofs of statements which essentially follow from the definitions, some interesting experiments are also omitted. We refer the readers to the technical report at www.loria.fr/~tayhanhd/techreport2.pdf for full justifications and more experiments.

Related work Beside the previously cited works [5, 8, 10, 11, 14], the automata approximation can be thought of as a *learning procedure*. This approach has

natural relations with researches in *machine learning* [1, 2] and may take benefit from results in this domain for the further development.

2 Background

In this contribution, we model the memory as a flat sequence of 8-bit bytes. The set of bytes is noted BYTES. The set of addresses (the address space) is noted DWORDS. Typically, the address space is the range $0..2^{32} - 1$, that is 4 BYTES. The memory is represented as a finite mapping $\mu : \text{DWORDS} \rightarrow \text{BYTES}$. Aside from the memory, there is a finite set of *registers*, next denoted REGISTERS, which contains **eip**, the *instruction pointer*. The state of registers is represented by a finite function $\nu : \text{REGISTERS} \rightarrow \text{DWORDS}$.

An *execution environment* (μ, ν) represents the state of the system. At each step of the computation, an *opcode* **c** pointed by **eip** is fetched from memory, and depending on it, the execution environment is updated. The couple $(\nu(\mathbf{eip}), \mathbf{c})$ is the *underlying instruction* of (μ, ν) next denoted $\mathcal{I}(\mu, \nu)$.

Accordingly, the execution from an initial state (μ_1, ν_1) is the sequence $(\mu_1, \nu_1) \rightarrow (\mu_2, \nu_2) \rightarrow \dots$. From this execution, we extract the *sequence of underlying instructions* called the *trace* $T(\mu, \nu) = \mathbf{i}_1, \mathbf{i}_2, \dots$ where $\mathbf{i}_j = \mathcal{I}(\mu_j, \nu_j)$ for all $j \geq 1$ (see Example 1).

A running program P may ask for some inputs. To do that, it gives the control back to the operating system which will itself (a) push in memory a sequence of bytes m and (b) resume P . We call the execution environment at instant (b) an *input environment*. Input environments are characterized to be the "return" state of reading interruptions, or to follow input functions calls (e.g. **WSARecv** or **recv** on the Windows® OS). The instruction corresponding to an input environment is said to be an *input instruction*.

Let $N = (\mu, \nu)$ be an input environment. The *input message* m is stored in memory in an interval of addresses $A = [\mathbf{b}, \mathbf{b}+1, \dots, \mathbf{b}+\ell-1]$ with ℓ the length of the message m and \mathbf{b} some (more or less arbitrary) address. Given some message $m' = b_0 \dots b_{\ell'-1} \in \text{BYTES}^*$, let $N[m'] = (\mu', \nu')$ be the same environment as N but the one which received m' instead of m . More technically, suppose ℓ is stored in **eax** then $\mu'(\mathbf{b} + i) = b_i$ for all $i < \ell'$, otherwise $\mu'(\mathbf{a}) = \mu(\mathbf{a})$ and $\nu'(r) = \nu(r)$ for all registers except **eax** for which $\nu'(\mathbf{eax}) = \ell'$. For the remaining, we define $m[i] = b_i$ for all $i < \ell'$.

Given some input environment N , consider an instruction \mathbf{i}_j in a trace $T(N) = \mathbf{i}_1, \dots, \mathbf{i}_j, \mathbf{i}_{j+1}, \dots$, we say that \mathbf{i}_j is *deterministic* if for any message m such that $T(N[m]) = \mathbf{i}_1, \dots, \mathbf{i}_j, \mathbf{i}', \dots$, then $\mathbf{i}' = \mathbf{i}_{j+1}$. In other words, the successor of a deterministic instruction does not depend on messages. Typically, sequential instructions (e.g. **mov ebp, esp**), calls and unconditional jumps to some fixed address (e.g. **call 0xf1b542** or **jmp 0xf1ae33**) enter this category.

Non deterministic instructions are called *message-triggered*. In this category, one finds calls or jumps to some register (e.g. **call edx** or **jmp eax**), but the most representative are the conditional jumps (e.g. **jnz 0xf1a9ce**).

2.1 Execution tree

Let us consider a program P asking for some input m , its behavior will then depend on m . In a first step, we compute all possible run depending on m seen as a parameter. In a second step, for each execution branch, we describe the language of messages corresponding to that branch. But, for the moment, let us define the execution tree of an input environment N .

Consider an input environment N , define i_0^N to be the index of the first message triggered instruction in the trace $T(N) = i_1, \dots, i_{i_0^N}, \dots$. Actually, the prefix $i_1, \dots, i_{i_0^N}$ is shared by all traces $T(N[m])$ with m a message. Indeed, by definition, $i_1, \dots, i_{i_0^N}$ are deterministic but the last one, thus the sequence is uniquely determined by N . In the sequel, we shall drop the superscript N when the context is clear.

The trace $T_N(m) = i_1, \dots, i_n$ is the sub-sequence of $T(N[m])$ cut after the first input instruction i_n following i_1 (if such an i_n exists, otherwise $T_N(m) = T(N[m])$). In other words, we forget anything which would follow the analysis of a second message coming from the environment. The *trigger-trace* of m is the sequence $B_N(m) = [(a_1, i_{i_1}), \dots, (a_k, i_{i_k})]$ where:

- $i_{i_0}, i_{i_1}, \dots, i_{i_k}$ is the subsequence of $T_N(m)$ containing only the *message-triggered instructions* and (if relevant) the input instruction at the end of $T_N(m)$,
- $a_{\ell+1}$ is the address pointed by `eip` at step $i_{i_{\ell+1}}$ for all $\ell \geq 0$, that is the address of the instruction following $i_{i_{\ell}}$.

$$T_N(m) = \mathcal{I}(N) \rightarrow \dots \rightarrow i_{i_0} \rightarrow i_{a_1} \rightarrow \dots \rightarrow i_{i_1} \rightarrow i_{a_2} \rightarrow \dots$$

One may observe that the trace $T_N(m)$ is uniquely determined by $B_N(m)$. Indeed, with the notation above, for all $\ell \geq 0$, instructions $i_{i_{\ell+1}}, \dots, i_{i_{i_{\ell+1}-1}}$ are deterministic, thus uniquely determined by $a_{\ell+1}$.

Example 1. Let us run `wget` on some URL, the program receives a first message from the server via the operating system. It then verifies that the first four bytes of this message are "HTTP". Figure 1 displays the corresponding piece of trace:

address	trace	trigger-trace
0x404f89	<code>cmpsb [esi],[edi]</code>	<code>(0x403a20,cmpsb [esi],[edi])</code>
0x404f89	<code>cmpsb [esi],[edi]</code>	<code>(0x404f89,cmpsb [esi],[edi])</code>
0x404f89	<code>cmpsb [esi],[edi]</code>	<code>(0x404f89,cmpsb [esi],[edi])</code>
0x404f89	<code>cmpsb [esi],[edi]</code>	
0x404f8b	<code>pop edi</code>	
0x404f8c	<code>pop esi</code>	
0x404f8d	<code>jz 0x404f94</code>	<code>(0x404f89,jz 0x404f94)</code>
0x404f94	<code>test edx, edx</code>	...

Fig. 1: Trace and its corresponding triggered-trace

The notion of message-triggered instruction is relative to traces. Indeed, the instruction `cmpsb [esi],[edi]` at address `0x404f89` is message triggered the first three times, not the fourth one (that is because of `cmpsb` stops either when `[esi] ≠ [edi]` or if `ecx = 0`, and `ecx`'s value is 0 after the third time).

Definition 1 (Trigger Execution Tree (TET)). Given an input environment N , its trigger execution tree \mathcal{E}_N is a tree with labeled vertices and labeled edges, that is defined inductively as follows:

- the root is labeled $\mathbf{i}_{i_0^N}$, it is associated with the empty sequence $[]$.
- suppose $B_N(m) = [(\mathbf{a}_1, \mathbf{i}_{i_1}), \dots, (\mathbf{a}_k, \mathbf{i}_{i_k})]$ is a trigger-trace, suppose that the vertex n is associated with $[(\mathbf{a}_1, \mathbf{i}_{i_1}), \dots, (\mathbf{a}_j, \mathbf{i}_{i_j})]$ for some $j < k$, then \mathcal{E}_N contains a transition $n \xrightarrow{\mathbf{a}_{j+1}} n'$ with n' a (fresh) vertex labeled $\mathbf{i}_{i_{j+1}}$ and associated to $[(\mathbf{a}_1, \mathbf{i}_{i_1}), \dots, (\mathbf{a}_{j+1}, \mathbf{i}_{i_{j+1}})]$.

The trigger execution tree is the standard execution tree once removed intermediate deterministic instructions. Each path from the root to some leaf of \mathcal{E}_N corresponds to a trigger trace, thus to a trace in the original program.

Since traces are infinite in general, so may be the execution tree. Given $c > 0$, let $T_c(N)$ be the prefix of length c of the trace $T(N)$, then we define $\mathcal{E}_{N,c}$ to be the sub-tree of \mathcal{E}_N cut to traces of length at most c .

Example 2. Figure 3a shows the TET of `wget` with traces limited to 50 instructions. The vertices \perp_i 's mark the ends of traces due to length limit.

Remark 1. From now on, the trigger execution trees are understood as to be cut at some length limit c . We then omit c in $\mathcal{E}_{N,c}$ when it is clear from the context.

3 Input messages analysis

3.1 Dynamic tainting analysis

Dynamic tainting is a well known technique which tracks the dependency of data within programs. The below is a very brief introduction, we refer the reader to [15] for details.

The semantics of an instruction $\mathbf{i} = \mathcal{I}(N)$ of some execution environment N depends on a set of registers¹ R and memory content at a set of addresses A , and modify a set register² R' and memory content at a set of addresses A' . For such an \mathbf{i} , one derives a *hyper-edge* $(R \cup A, \mathbf{i}, R' \cup A')$ where source nodes are "read" memory addresses or registers, targets are "written" ones. Source and target nodes are considered separate. We say that \mathbf{i} depends on source nodes.

Let us consider a prefix $\mathbf{i}_1, \dots, \mathbf{i}_j$ of some trace $T(N) = \mathbf{i}_1, \mathbf{i}_2, \dots$. The *hyper-graph* $\mathcal{H}(\mathbf{i}_1, \dots, \mathbf{i}_j)$ is the chain of edges obtained by *glueing* corresponding target and sources (cf. Example 3). The sources of $\mathcal{H}(\mathbf{i}_1, \dots, \mathbf{i}_j)$, denoted by $\mathcal{S}(\mathbf{i}_1, \dots, \mathbf{i}_j)$, consists of source nodes that are not targets of any hyper-edge (that is source nodes which are not glued). The set $\mathcal{S}(\mathbf{i}_1, \dots, \mathbf{i}_j)$ describes the dependency of $\mathbf{i}_1, \dots, \mathbf{i}_j$ on N . The local sources $\mathcal{S}_\ell(\mathbf{i}_1, \dots, \mathbf{i}_j)$ are source nodes in $\mathcal{S}(\mathbf{i}_1, \dots, \mathbf{i}_j)$ related by a path to the targets of \mathbf{i}_j . They describe the dependency of \mathbf{i}_j on N (supposedly it is executed).

¹but `eip` which is treated apart.

²Idem.

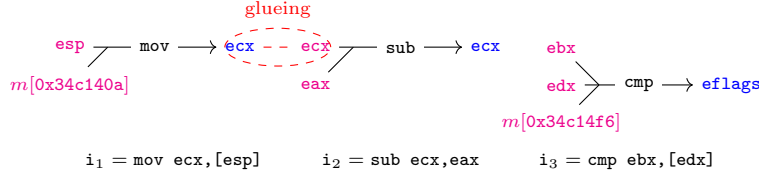


Fig. 2: Hyper-edge glueing

Example 3. Given that **esp** and **edx** have respectively the value 0x34c140a and 0x34c14f6 at the beginning. The hyper-graph $\mathcal{H}(i_1, i_2, i_3)$ in Figure 2 has sources $\mathcal{S}(i_1, i_2, i_3) = \{\text{esp}, m[0x34c140a], \text{eax}, \text{ebx}, \text{edx}, m[0x34c14f6]\}$ and local sources $\mathcal{S}_\ell(i_1, i_2, i_3) = \{\text{ebx}, \text{edx}, m[0x34c14f6]\}$.

Proposition 1. *The tainting analysis leads to two main facts.*

1. Any instruction in a finite trace depends only on finitely many input bytes from memory. It is a direct consequence from the fact that assembly instructions read and modify only locally the memory.
2. Given some execution environment N and some instruction i_j occurring in its trace $T(N) = i_1, \dots, i_j, \dots$, let N' be an environment coinciding with N on the sources of $\mathcal{H}(i_1, \dots, i_j)$, then, $T(N') = i_1, \dots, i_j, \dots$ coincide with the trace of N at least up to the j -th instruction.

3.2 Trace formula construction

In this section, and the following ones, N denotes an input environment and $A = [\mathbf{b}, \dots, \mathbf{b} + \ell - 1]$ is the relative interval of addresses. A *local condition* is a subset $\gamma \subseteq \text{BYTES}^S$ for some (finite) set S , called the support of γ .

Definition 2 (Trace language). *Given a program P and some input environment N , its execution tree (possibly cut at some length c) is \mathcal{E}_N . Let p be a finite path in \mathcal{E}_N from the root to some leaf, its language is the set of messages*

$$M_N(p) = \{m \in \mathcal{M} \mid B_N(m) = p\}.$$

A path (from the root to some leaf) p in \mathcal{E}_N corresponds to a trigger-trace $B_N(m)$ of some $m \in M_N(p)$, and thus to a sub-trace $T_N(m) = i_1, \dots, i_j$. Let \mathcal{S}_p be addresses of A that are sources of $T_N(m)$, namely $\mathcal{S}_p = A \cap \mathcal{S}(i_1, \dots, i_j)$.

Proposition 2. *For any message $m \in M_N(p)$, for any message m' , if $m' =_{|\mathcal{S}_p} m$ then $m' \in M_N(p)$.*

Proposition 2 can be restated as follows: the set $M_N(p)$ is isomorphic to $\gamma_p \times \text{BYTES}^{A \setminus \mathcal{S}_p}$ for some set $\gamma_p \subseteq \text{BYTES}^{\mathcal{S}_p}$. Abstracting A , in other words, $M_N(p)$ is defined by a couple $(\mathcal{S}_p, \gamma_p)$ of a local condition and its support. In terms of languages, it means that $M_N(p)$ is a regular one.

Example 4. In Figure 3a, writing $[\hat{X}]$ for $(\text{BYTES} \setminus \{X\})$, we have $M_N(\perp_1) = [\hat{H}] \cdot \text{BYTES}^*$, $M_N(\perp_2) = H \cdot [\hat{T}] \cdot \text{BYTES}^*$, $M_N(\perp_3) = H \cdot T \cdot [\hat{T}] \cdot \text{BYTES}^*$, etc.

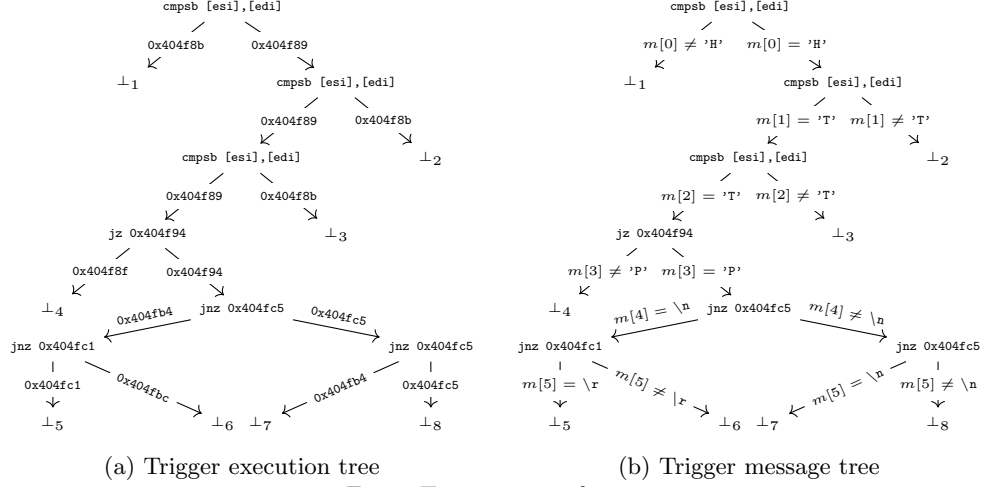


Fig. 3: Trigger trees of **wget**

3.3 Message execution tree

A trigger execution tree can be seen as a decision tree, which gives a set of messages given a trace. Here, we consider the dual view that gives a trace given a message.

Definition 3. A *Message Execution Tree (MET)* is a tree whose transitions are labeled by finite sets of bytes and leaves are labeled by a trigger path; for each vertex n and letter $b \in \text{BYTES}$, there is a unique vertex n' such that $n \xrightarrow{S \ni b} n'$.

Given a MET G and a word m , let $G(m)$ denote the trigger path used as the label of the vertex reached following the path (within G) described by m in G . Then G is *compatible* with some TET \mathcal{E}_N if $G(m)$ is also the trigger path containing m in \mathcal{E}_N , namely $m \in M_N(G(m))$ for all $m \in \text{BYTES}^*$.

We can always build a compatible MET for any finite¹ TET \mathcal{E}_N . Indeed, let $L = \max(\cup_{p \in \mathcal{E}_N}(\mathcal{S}_p)) - \mathbf{b}$ as the largest offset appearing in the sources of a path $p \in \mathcal{E}_N$, and let G be the full 256-ary tree of depth L . Each leaf of G corresponds to some word in $m \in \text{BYTES}^L$, we associate to that leaf the path p such that $m \in M_N(p)$, then G is compatible with \mathcal{E}_N .

However, one may notice that the full tree G describe above is very large in general, its size is of the order 256^L , thus hardly computable. We develop here below some heuristics to get a much more compact representation.

3.4 Message decomposition

Given a path $p = [(a_1, i_{i_1}), \dots, (a_k, i_{i_k})]$ in a TET \mathcal{E}_N , it corresponds to a trace $T_N(m) = i_1 \rightarrow \dots \rightarrow i_{i_0} \rightarrow i_{a_1} \rightarrow \dots \rightarrow i_{i_1} \rightarrow i_{a_2} \rightarrow \dots$. The tainting analysis shows that any $i \in T_N(m)$ depends on the environment N with respect

¹Possibly due to a length limit.

to a local sources $\mathcal{S}_l(i_1, i_2, \dots, i)$. Hence the set $\mathcal{S}^A(i) = \mathcal{S}_l(i_1, i_2, \dots, i) \cup A$ describes the *local dependency* of i on the input.

In the TET \mathcal{E}_N , we replace the label a_{j+1} of each edge $i_{i_j} \xrightarrow{a_{j+1}} i_{i_{j+1}}$ by its *local condition*, that is the set $\gamma_{a_{j+1}} = \{m_{|\mathcal{S}^A(i)} \mid m \in \text{BYTES}^* \wedge T_N(m) = i_1, \dots, i_{a_{j+1}}, \dots\}$. The tree thus obtained from \mathcal{E}_N is called the *trigger message tree* relative to \mathcal{E}_N , next denoted by \mathcal{F}_N .

Example 5. The fig. 3b shows the trigger message tree relative to the TET in Figure 3a.

Let $\mathcal{S}_{\mathcal{F}_N}$ be the union $\bigcup_{i \in \mathcal{F}_N} \mathcal{S}^A(i)$ of local dependencies in \mathcal{F}_N . The relation $R_{\mathcal{F}_N} \subseteq \mathcal{S}_{\mathcal{F}_N} \times \mathcal{S}_{\mathcal{F}_N}$ is defined by $(a, a') \in R_{\mathcal{F}_N}$ if $a, a' \in \mathcal{S}^A(i)$ for some $i \in \mathcal{F}_N$.

Definition 4 (Address closure). *The reflexive-transitive closure of $R_{\mathcal{F}_N}$ is called the address closure of \mathcal{F}_N , denoted by $\alpha_{\mathcal{F}_N}$.*

The address closure $\alpha_{\mathcal{F}_N}$ induces a partition $\mathcal{S}_{\mathcal{F}_N} = A_1 \cup \dots \cup A_r \subseteq A$ of equivalence classes. Let p be some path in \mathcal{F}_N , from the root to some leaf, each $A_i \subseteq A$ gives a *local decomposition* of $M_N(p)$ defined by $\gamma_{p, A_i} = \{m_{|A_i} \mid m \in M_N(p)\}$. Then the path language $M_N(p)$ can be decomposed into:

Proposition 3. $M_N(p) = \gamma_{p, A_1} \times \gamma_{p, A_2} \times \dots \times \gamma_{p, A_r} \times \text{BYTES}^{A \setminus \mathcal{S}_{\mathcal{F}_N}}$

local condition	path
$m[0] = \text{'H'}$	cmpsb [esi], [edi] i1
$m[1] = \text{'T'}$	cmpsb [esi], [edi] i2
$m[2] = \text{'T'}$	cmpsb [esi], [edi] i3
$m[3] = \text{'P'}$	jz 0x404f94 i4
$m[4] = \backslash n$	jnz 0x404fc5 i5
$m[5] \neq \backslash n$	jnz 0x404fc1 i6
$m[5] = \backslash r$	jnz 0x404fc5 i7

Fig. 4: Path of `wget` and message decomposition

Example 6. The path p in Figure 4 is extracted from the execution tree of `wget` cut at length 60, it is of the form

$$i_1 \xrightarrow{m[0] = \text{'H'}} i_2 \xrightarrow{m[1] = \text{'T'}} \dots \xrightarrow{m[4] = \backslash n} i_6 \xrightarrow{m[5] \neq \backslash n} i_7 \xrightarrow{m[5] = \backslash r} \perp$$

Both instructions **i6** and **i7** depend on the 5-th byte of the input, the local decomposition groups their local conditions into a single set $M_N(p)_{|\{5\}} = \{m[5] \neq \backslash n\} \cap \{m[5] = \backslash r\} = \{m[5] = \backslash r\}$, thus $M_N(p)$ decomposes:

$$M_N(p) = \{\text{'H'}\} \times \{\text{'T'}\} \times \{\text{'T'}\} \times \{\text{'P'}\} \times \{\backslash n\} \times \{\backslash r\} \times \text{BYTES}^{A \setminus \{0,1,2,3,4,5\}}$$

From the partition $\mathcal{S}_{\mathcal{F}_N} = A_1 \cup \dots \cup A_r$ induced from the closure $\alpha_{\mathcal{F}_N}$ of \mathcal{F}_N , we derive a tree $\overline{\mathcal{F}_N}$ whose branches have the form where γ_i is a local condition on A_i . Moreover, each path in \mathcal{E}_N corresponds uniquely to a leaf in $\overline{\mathcal{F}_N}$. Algorithm 1 constructs the tree $\overline{\mathcal{F}_N}$ when the following hypothesis holds.

Hypothesis 1 For any path $i_{i_0} \xrightarrow{\gamma_1} i_{i_1} \xrightarrow{\gamma_2} \dots$ in \mathcal{F}_N , let $A_{d(k)}$ denote the equivalence class containing the support of γ_k . Then, for all $k \leq l$, $d(k) \leq d(l)$.

Proposition 4. Let s_0 be the root of the trigger message tree $\overline{\mathcal{F}_N}$. For any path p in \mathcal{E}_N , there is a unique path $\bar{p} = s_0 \xrightarrow{\gamma_1} \dots \xrightarrow{\gamma_r} s_r$ in $\overline{\mathcal{F}_N}$ such that $M_N(p) = \gamma_1 \times \dots \times \gamma_r \times \text{BYTES}^{\mathbb{G}_{A_1 \cup \dots \cup A_r}}$.

Input: $\mathcal{P} = \{M_N(p) = \gamma_{p,A_1} \times \dots \times \gamma_{p,A_r} \times \gamma_{p,\mathbb{G}} \mid p \text{ is a path on } \mathcal{F}_N\}$.
Output: the tree $\overline{\mathcal{F}_N}$.
begin
 $\overline{\mathcal{F}_N} \leftarrow \{\text{initial state } s\};$
foreach $M_N(p) \in \mathcal{P}$ **do**
 $i \leftarrow 1, s_1 \leftarrow s;$ **while** edge $e = s_i \xrightarrow{\gamma_{p,A_i}} s'_i$ for some s'_i exists on $\overline{\mathcal{F}_N}$ **do**
 $i \leftarrow i + 1; s_1 \leftarrow \text{source of } e; s_2 \leftarrow \text{target of } e;$
 end
 while $i \neq j$ **do**
 $s_1 \leftarrow s_2;$
 if $i + 1 \leq n$ **then**
 $s_2 \leftarrow \text{new state};$
 else
 $s_2 \leftarrow \text{new state labeled } \perp;$
 end
 add new edge $s_1 \xrightarrow{\gamma_{p,A_i}} s_2$ into $\overline{\mathcal{F}_N}; i \leftarrow i + 1;$
 end
end
end

Algorithm 1: Tree construction

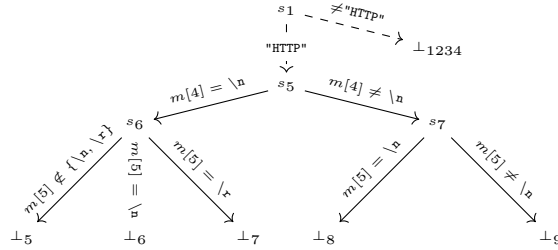


Fig. 5: Decomposition tree

Finally, local conditions in the tree $\overline{\mathcal{F}_N}$ can be reordered to follow the progression of a MET. We end with a message execution tree, next denoted \mathcal{A}_N .

Example 7. The fig. 5 shows the tree $\overline{\mathcal{F}_N}$ corresponding to the \mathcal{E}_N discussed in Example 6. To save space, we grouped transitions of the first four characters, that is from s_1 to s_4 . One can observe that $\overline{\mathcal{F}_N}$ in this case is indeed \mathcal{A}_N .

4 Message classification

In Section 3, we showed how to relate messages to traces of execution using path languages and their dual representation by message execution trees. With this analysis, some messages m and m' that are not treated in the same manner are not in the same path language, even if they are clearly related. For example, consider a program which looks for the first newline character by examining each byte, then the execution traces of the parsing of "aaa\n" and the one of "aaaa\n" fall into different categories. In this section, we propose a method to avoid such irrelevant details. The principle is to gather equivalent path languages with respect to the remaining execution of the program.

4.1 Message automaton

Definition 5 (Message automaton (MA)). A message automaton \mathcal{A} is a quadruple $\langle Q, \iota, E, P \rangle$ where

- Q is a set of states, $\iota \in Q$ being the one qualified to be initial,
- $E \subseteq Q \times \text{BYTES} \rightarrow Q$ is a transition function which is totally determined at some states, and totally undetermined at other states.
- $P : Q \rightarrow 2^{\mathcal{B}}$ maps each state to a set of trigger paths (in general, the trigger paths of \mathcal{B} are distinguished from the paths within \mathcal{A}).

In other words, it is a pair made of a (deterministic) automaton and the path function P . Given some word m , $E(\iota, m)$ and $Q(\iota, m)$ denote respectively the state and the path (within the automaton) following the word m along the transition function E from the initial state ι .

Proposition 5. The message execution tree \mathcal{A}_N has form of a message automaton $\langle Q, \iota, E, P \rangle$ where Q consists of all vertices, ι is the root vertex, E consists of $s \times \gamma \rightarrow s'$ whenever $s \xrightarrow{\gamma} s'$ is an edge of \mathcal{A}_N , and $P(q) = \{\text{the path from root to } q\}$ for any leaf q , otherwise $P(q) = \emptyset$.

Given some trigger path equivalence $\approx \subseteq \mathcal{B} \times \mathcal{B}$, a morphism $h : \mathcal{A} \rightarrow \mathcal{A}'$ where $\mathcal{A} = \langle Q, \iota, E, P \rangle$ and $\mathcal{A}' = \langle Q', \iota', E', P' \rangle$ is a function $h : Q \rightarrow Q'$ satisfying: 1. $h(\iota) = \iota'$, 2. $E'(h(q), b) = h(q')$ for all $q \in Q$ and $b \in \text{BYTES}$, 3. $P'(h(q)) \approx P(q)$ ¹ for any $q \in Q$ satisfying $P(q) \neq \emptyset$.

Definition 6 (Observational approximation). Given a MA $\mathcal{A} = \langle Q, \iota, E, P \rangle$, a MA $\mathcal{A}' = \langle Q', \iota', E', P' \rangle$ is said to approximate observationally \mathcal{A} if $E(\iota, m)$ exists² then $E'(\iota', m)$ exists, moreover if $P(E(\iota, m)) \neq \emptyset$ then $P'(E'(\iota', m)) \approx P(E(\iota, m))$ and if $Q(\iota, m_1) \neq Q(\iota, m_2)$ then $Q'(\iota', m_1) \neq Q'(\iota', m_2)$, for all $m, m_1, m_2 \in \text{BYTES}^*$.

Intuitively, the message automaton \mathcal{A}' recognizes and distinguishes at least the paths of \mathcal{A} . Morphisms justify the observational approximation:

¹This is the set equality up to the equivalence \approx .

²Because E is a partial function, $E(\iota, m)$ does not always exist for any $m \in \text{BYTES}^*$.

Proposition 6. *If there is a morphism $h: \mathcal{A} \rightarrow \mathcal{A}'$, then \mathcal{A}' approximates observationally \mathcal{A} .*

Example 8. The automaton \mathcal{A} in Figure 6 is constructed from \mathcal{A}_N in Example 7 using the morphism that maps $s_i \mapsto S_i$ and $\perp_i \mapsto \perp$ for all $1 \leq i \leq 7$. On \mathcal{A} , set the path function to be $\perp \mapsto \cup_{i \leq 7} \perp_i$ (with the identification of \perp_i with its corresponding path in \mathcal{A}_N) and $S_i \mapsto \emptyset$ otherwise. One can observe that \mathcal{A} approximates observationally \mathcal{A}_N .

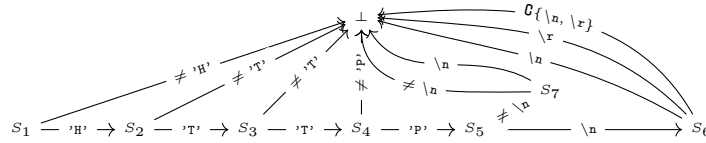


Fig. 6: Message automaton constructed from a morphism

Definition 7 (State equivalence). *Given a MA $\mathcal{A} = \langle Q, \iota, E, P \rangle$, a state equivalence relative to \approx is an equivalence $\sim \subseteq Q \times Q$ satisfying: if $q_1 \sim q_2$ then*

- *if there is a transition $q_1 \xrightarrow{t} q'_1$*
 - *then either q_2 has no outgoing transitions,*
 - *or there is a state q'_2 with transition $q_2 \xrightarrow{t} q'_2$ and $q'_1 \sim q'_2$.*
- *if $P(q_1), P(q_2) \neq \emptyset$ then $P(q_1) \approx P(q_2)$.*

The co-recursive definition of the state equivalence is constructive. Indeed, to look for a state equivalence we can start the verification from any pair (q_1, q_2) : first, add it as the first element of the relation whenever q_1 or q_2 has no transition, or both of them have similar transitions, then continue verifying pairs of corresponding states (q'_1, q'_2) or some pair obtained from the transitive closure of the current constructed relation. At any verification step, if states of the verified pair invalidate the definition, then the relation cannot be a state equivalence. In this case, we will restart verifying with another pair until an equivalence is completely constructed. This construction is given in the technical report.

Example 9. The fig. 7a shows the MET of **wget** with traces cut to length 70. Given the path equivalence $\perp_5 \approx \perp_9$, $\perp_6 \approx \perp_{10}$, $\perp_7 \approx \perp_{11}$, we have a state equivalence $s_5 \sim_1 s_7 \sim_1 \perp_8$, $s_6 \sim_1 s_{12}$, $\perp_5 \sim_1 \perp_9$, $\perp_6 \sim_1 \perp_{10}$ and $\perp_7 \sim_1 \perp_{11}$.

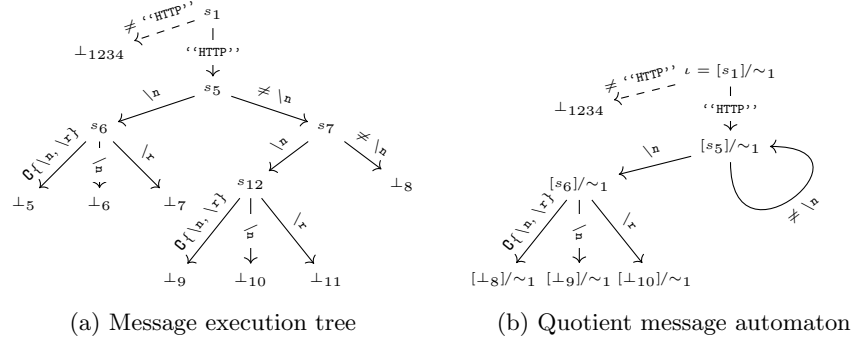
Given a MA $\mathcal{A} = \langle Q, \iota, E, P \rangle$, let \sim be a state equivalence relative to some path equivalence \approx , then the MA $\mathcal{A}/\sim = \langle Q', \iota', E', P' \rangle$ constructed as follows

- $Q' = \{[q]/\sim \mid q \in Q\}$, $\iota' = [\iota]/\sim$,
- $E' = \{[q_1]/\sim \xrightarrow{t} [q_2]/\sim \mid q_1 \xrightarrow{t} q_2 \in E\}$
- $P' : [q]/\sim \mapsto \bigcup_{q' \in [q]/\sim} P(q')$.

is called the *quotient of \mathcal{A} by \sim* . This construction leads to:

Proposition 7. *The function $\llbracket \cdot \rrbracket / \sim : Q \rightarrow Q'$ defines a morphism $\mathcal{A} \rightarrow \mathcal{A}/\sim$, then \mathcal{A}/\sim approximates observationally \mathcal{A} .*

Example 10. The MA in Figure 7b is constructed from the state equivalence \sim_1 in Example 9, it approximates observationally the MET in Figure 7a.



4.2 Minimal message automaton

Let \mathcal{A} be a message automaton, let \sim be a nontrivial¹ equivalence on \mathcal{A} then the quotient \mathcal{A}/\sim has fewer state than \mathcal{A} . A message automaton \mathcal{A} is said to be irreducible if there is no nontrivial equivalence on \mathcal{A} .

Proposition 8. *The composition $f \circ g$ of two morphisms is a morphism.*

Consider a sequence of morphisms $\mathcal{A}_N \xrightarrow{\text{id}} \mathcal{A}_1 \xrightarrow{h_1} \mathcal{A}_2 \xrightarrow{h_2} \dots$ where the h_i 's are induced from some nontrivial state equivalence on \mathcal{A}_i , then this sequence is always finite (the number of states is strictly decreasing). It ends on an irreducible machine automaton \mathcal{A}_{n+1} . By Proposition 7 and Proposition 8, $h = h_n \circ h_{n-1} \circ \dots \circ h_1$ is a morphism. By Proposition 6, \mathcal{A}_{n+1} approximates observationally \mathcal{A}_N . Moreover, since \mathcal{A}_{n+1} is not reducible, it is minimal:

Proposition 9. *There is no message automaton that approximates observationally \mathcal{A}_N but has less states than \mathcal{A}_{n+1} .*

Example 11. The MA in Figure 8 is reduced from the MA in Figure 7b using the morphism constructed from the state equivalence $\perp_1 \sim_2 \perp_2 \sim_2 \perp_3 \sim_2 \perp_4 \sim_2 [s_8]/\sim_1 \sim_2 [s_9]/\sim_1 \sim_2 [s_{10}]/\sim_1$, it approximates observationally the MET in Figure 7a. We let the reader verify that it is minimal.

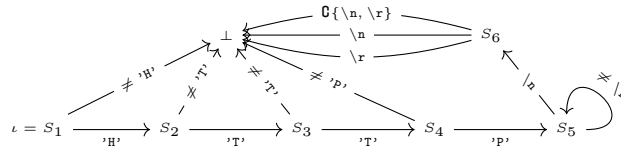


Fig. 8: Minimal message automaton

5 Implementation and experiments

To verify the relevance of the approach, we have implemented a *binary code coverage module* that builds the execution tree by a fuzzing techniques optimized

¹Namely \sim is not the equality.

$$\begin{aligned}
M_4 &= 'P'. 'W'. 'D'. \text{BYTES}^* \\
M_5 &= 'C'. 'W'. 'D'. \text{BYTES}^* \\
M_6 &= 'T'. 'Y'. 'P'. 'E'. \{'A'\}. \text{BYTES}^* \\
M_7 &= 'F'. 'E'. 'A'. 'T'. \{'A'\}. \text{BYTES}^* \\
M_8 &= 'S'. 'T'. 'A'. 'T'. \{'A'\}. \text{BYTES}^* \\
M_9 &= 'L'. 'I'. 'S'. 'T'. \{'A'\}. \text{BYTES}^*
\end{aligned}$$

The observed words USER, PASS, PASV, PWD, CWD, TYPE, FEAT, STAT, LIST are commands defined in the `ftp` protocol. That may be interpreted as an insight that Zeus steals information concerned with `ftp` communications. Moreover, besides these words there is no more commands of `ftp` occurring in the automaton, so we may think that Zeus is interested only in this subset of `ftp` commands. In the following languages that occur also within the automaton:

$$\begin{aligned}
M_{10} &= 'U'. 'S'. 'E'. 'R'. \{32\}. \text{BYTES}^* \\
M_{11} &= 'U'. 'S'. 'E'. 'R'. \{32\}. \{32, 33, \dots, 255\}^k. \backslash r. \text{BYTES}^* \\
M_{11} &= 'U'. 'S'. 'E'. 'R'. \{32\}. \{32, 33, \dots, 255\}^k. \backslash n. \text{BYTES}^* \\
M_{12} &= 'U'. 'S'. 'E'. 'R'. \{32\}. \{32, 33, \dots, 255\}^k. (\{1, 2, \dots, 31\} \setminus \{\backslash r, \backslash n\}). \text{BYTES}^* \\
M_{13} &= 'P'. 'A'. 'S'. 'S'. \{32\}. \text{BYTES}^* \\
M_{14} &= 'P'. 'A'. 'S'. 'S'. \{32\}. \{32, 33, \dots, 255\}^k. \backslash r. \text{BYTES}^* \\
M_{15} &= 'P'. 'A'. 'S'. 'S'. \{32\}. \{32, 33, \dots, 255\}^k. \backslash n. \text{BYTES}^* \\
M_{16} &= 'P'. 'A'. 'S'. 'S'. \{32\}. \{32, 33, \dots, 255\}^k. (\{1, 2, \dots, 31\} \setminus \{\backslash r, \backslash n\}). \text{BYTES}^*
\end{aligned}$$

we notice that 32 is the ASCII code of the space character, and the set $\{1, 2, \dots, 31\}$ contain all control characters. So these languages show clearly how Zeus parses a USER or PASS command: it checks whether the next character is space or not, if yes then it collects the bytes until a control character is detected. In other words, we retrieved the delimiter-oriented analysis of Caballero et al. [5].

6 Conclusion

The experiments we made so far showed that the hypothesis we made are generally speaking fulfilled. The parsing methods look more or less transparent. However, the shape of the automaton depends largely on the choice on the length of traces. At the same time, the computation time is more or less exponential in the size of the execution tree, thus, usually exponential in the length of traces. There are good reasons to optimize the computation of the execution tree and all derived ones, we think of symbolic execution, SMT-solvers and advanced tainting techniques. Finally, there is a parameter that we did not really use, namely the path equivalence \approx . Again, some first tries showed that this must be deepened.

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