

						Single Effective Address Operation Word Format															Brief Extension Word Format															Full Extension Word Format																	
Mnemonic	Page	\$W	\$X	\$Y	\$Z	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ANDI	4 - 18	0	0			0	0	0	0	0	0	0	0	Size	EA Mode	EA Register	16-Bit Word Data										8-Bit Byte Data					32-Bit Long Data																					
BCHG	4 - 27	0				0	0	0	0	Register			1	0	1	EA Mode	EA Register																																				
BCHG	4 - 27	0	8			0	0	0	0	1	0	0	0	0	1	EA Mode	EA Register	0	0	0	0	0	0	0	0	Bit Number																											
CMPI	4 - 79	0	C			0	0	0	0	1	1	0	0	Size	EA Mode	EA Register	16-Bit Word Data										8-Bit Byte Data					32-Bit Long Data																					
EORI	4 - 102	0	A			0	0	0	0	1	0	1	0	Size	EA Mode	EA Register	16-Bit Word Data										8-Bit Byte Data					32-Bit Long Data																					
SUBI	4 - 179	0	4			0	0	0	0	0	1	0	0	Size	EA Mode	EA Register	16-Bit Word Data										8-Bit Byte Data					32-Bit Long Data																					
MOVE.B	4 - 116	1				0	0	0	1	Dest Register			Dest Mode		Src Mode		Src Register																																				
MOVE.L	4 - 116	2				0	0	1	0	Dest Register			Dest Mode		Src Mode		Src Register																																				
MOVE.W	4 - 116	3				0	0	1	1	Dest Register			Dest Mode		Src Mode		Src Register																																				
CLR	4 - 73	4	2			0	1	0	0	0	0	1	0	Size	EA Mode	EA Register																																					
DIVS.L	4 - 92	4	C			0	1	0	0	1	1	0	0	0	1	EA Mode	EA Register	0	Register Dq			1	Size	0	0	0	0	0	0	0	0	0	0	0	Register Dr																		
JSR	4 - 109	4	E			0	1	0	0	1	1	1	0	1	0	EA Mode	EA Register																																				
LEA	4 - 110	4				0	1	0	0	Register			1	1	1	EA Mode	EA Register																																				
MOVEM	4 - 128	4				0	1	0	0	1	dr	0	0	1	size	EA Mode	EA Register	Register List Mask																																			
MULS.L	4 - 135	4	C			0	1	0	0	1	1	0	0	0	0	EA Mode	EA Register	0	Register Di			1	Size	0	0	0	0	0	0	0	0	0	0	0	Register Dh																		
NOP	4 - 147	4	E	7	1	0	1	0	0	1	1	1	0	0	1	1	1	0	0	0	1																																
RTS	4 - 169	4	E	7	5	0	1	0	0	1	1	1	0	0	1	1	1	0	1	0	1																																
ADDQ	4 - 11	5				0	1	0	1	Data			0	Size	EA Mode	EA Register																																					
BCC	4 - 26	6				0	1	1	0	Condition			8-Bit Displacement					16-Bit Displacement if 8-Bit Displacement = \$00										32-Bit Displacement if 8-Bit Displacement = \$FF																									
MOVEQ	4 - 134	7				0	1	1	1	Register			0	Data																																							
DIVS.W	4 - 92	8				1	0	0	0	Register			1	1	1	EA Mode	EA Register																																				
SUB	4 - 174	9				1	0	0	1	Register			Opmode		EA Mode		EA Register																																				
SUBA	4 - 177	9				1	0	0	1	Register			Opmode		EA Mode		EA Register																																				
CMP	4 - 75	B				1	0	1	1	Register			Opmode		EA Mode		EA Register																																				
CMPA	4 - 77	B				1	0	1	1	Register			Opmode		EA Mode		EA Register																																				
EOR	4 - 100	B				1	0	1	1	Register			Opmode		EA Mode		EA Register																																				
AND	4 - 15	C				1	1	0	0	Register			Opmode		EA Mode		EA Register																																				
MULS.W	4 - 135	C				1	1	0	0	Register			1	1	1	EA Mode	EA Register																																				
ADD	4 - 4	D				1	1	0	1	Register			Opmode		EA Mode		EA Register																																				
ADDA	4 - 7	D				1	1	0	1	Register			Opmode		EA Mode		EA Register																																				
ASL	4 - 21	E				1	1	1	0	Count/ Reg			1	Size	i/r	0	0	Register																																			
ASR	4 - 21	E				1	1	1	0	Count/ Reg			0	Size	i/r	0	0	Register																																			
LSL	4 - 113	E				1	1	1	0	Count/ Reg			1	Size	i/r	0	1	Register																																			
LSR	4 - 113	E				1	1	1	0	Count/ Reg			0	Size	i/r	0	1	Register																																			
ROL	4 - 161	E				1	1	1	0	Count/ Reg			1	Size	i/r	1	1	Register																																			
ROR	4 - 161	E				1	1	1	0	Count/ Reg			0	Size	i/r	1	1	Register																																			