Sprint 5

* Documentation - done td 3/13
  + Program description - 1 to 2 pages about the program.
  + Specification - 1 to 2 pages about what the program does.
  + Test Plan - 1 to 2 pages about how the program was tested.
  + Exception report - Describe problems encountered that couldn’t fix, or not fixed.
  + Team report - How the team was organized, and what tasks were accomplished by whom.
* I/O
  + Notes: Fold in requirements not met found during specifications audit
  + Status:
    - Correct input handler to request ending location. Currently only takes input for starting location. Done td 3/7
    - Correct INVALID\_OP to display 0000addr DATA $WYXZ for invalid machine code. Finished rh 3/7
    - Correct input handler to ask the user to disassemble another memory image, or prompt the user to quit. Done td 3/7
    - Verify that the disassembler will not crash on non-required opcodes or EAs for any input branch or mode. Done td 3/7
    - Added input validation for checking odd bytes, the ending address is not before the starting address, and the starting address is not lower than $0400. td 3/9
    - Added dynamic tab length so all columns line up. td 3/9
    - Made SR\_PrintHex more user friendly. Now can easily specify how many characters to print on D6. td 3/9
* EA
  + Notes: Do in depth testing of all EA modes to verify decoding properly.
* Opcodes
  + Complete the following opcode instructions:
    - MOVEQ - Finished. td 3/10
    - MOVEM - Finished rh 3/13
    - SUB - Finished rh 3/5
    - MULS - Finished rh 3/9
    - DIVU - Finished rh 3/9
    - LEA - Finished rh 3/7
    - CLR - Finished rh 3/7
    - CMP - Finished rh 3/7
    - BCC - Finished td 3/9
    - BGT - Finished td 3/9
    - BLE - Finished td 3/9
    - JSR - Finished rh 3/7
    - ADDQ - added optional opcode td 3/10
    - CMPI - added optional opcode td 3/10
    - DIVS - added optional opcode td 3/10
    - MOVEA - added optional opcode td 3/10
    - MULU - added optional opcode td 3/10
    - SUBA - added optional opcode td 3/10
    - SUBI - added optional opcode td 3/10
    - SUBQ - added optional opcode td 3/10
* Bug Triage
  + Assignment
    - Pri 1: Check that Easy68K logging function works properly. Failed td 3/7. All output needs to use proper CR,LF line endings to prevent bad logging. Fixed and tested td 3/13
  + Opcodes
    - So many bugfixes. Not itemized. td 3/10

Sprint 4

* Project General
  + Notes: Full audit of project specifications and deliverables.
  + Status:
    - Completely refactored code into separate files, standardized commenting, standardized naming. td 2/29
    - Added several evalbits and print subroutines for opcodes. td 3/2
    - EA decoding completed, needs integration. rh 3/5
* I/O
  + Notes: Input branching complete. Need to test on non-required opcodes.
  + Status:
    - Found and corrected decode errors for some opcodes. Input branching tested and complete for assigned and chosen optional opcodes. td 2/29
    - Updated root branch diagram. td 3/5
    - Updated branch 0 diagram. td 3/5
    - Updated branch 4 diagram. td 3/5
    - Created branch 5 diagram. td 3/5
    - Updated branch 8 diagram. td 3/5
* EA
  + Notes: Finish immediate addressing this week
  + Status:
    - Formatting and standardization coding pass for EA and subroutines. td 2/29
    - Completed Immediate address decoding -rh 3/5
* Opcodes
  + Notes: Finish minimum 10 this week
  + Blockers: Need Immediate Data EA mode to complete testing. td 3/2
  + Status:
    - ADD opcode complete, fails extended test 19. td 3/1
    - ADDA opcode complete. td 3/2
    - ADDI opcode complete. td 3/2
    - AND, ANDI, ASL, ASR, LSL, LSR, ROL, ROR opcodes complete. All SHIFTROT opcodes fail memory shift tests. td 3/2
    - Updated MOVE.B, MOVE.W, and MOVE.L, needs testing. rh 3/5

Sprint 3

* Full audit of project specifications and deliverables
  + Delayed until sprint 4. td 2/28
* Need to build full project timeline
  + Removing from project list due to lack of time. td 2/28
* Built interactive client interface
  + Status:
    - Imported work from HW5 into project to decode ASCII and take user input to define location of imported tests. td 2/24
    - Added ability to pause output when screen becomes full. td 2/28
* Implement minimum 5 new opcodes.
  + Status:
    - Delayed until sprint 4. td 2/28
    - Did full pass standardizing register use and merged prototype into mainline code instead. td 2/27
* Opcode processing
  + Status:
    - Opcode detection and routing is almost complete. td 2/28
    - A testing module with all assigned opcodes can be loaded and processed to evaluate basic decoding. td 2/28
* Finish effective address decoding subroutine
  + completed EA mode 000 rh 2/28
  + completed EA mode 001 rh 2/28
  + completed EA mode 010 rh 2/28
  + completed EA mode 011 rh 2/28
  + completed EA mode 100 rh 2/28

Sprint 2

* Need full project timeline
  + Status: Pushed back to Sprint 3
* Transfer completed layouts to project source code
  + Notes: Due 2/19
  + Assigned: Thomas 2/16
  + Status:
    - Layouts from Spring 1 are transferred in. Framework of project code is now in place. td 2/16
* Begin formal coding for project
  + Notes: Build disassembler based off of input test code modules after framework is created.
  + Blockers: Requires transfer of existing layouts to be complete. Done. td 2/16
  + Status:
    - Wrote TestMOVE.X86 which tests the entirety of the MOVE opcode. All EA modes are tested for source and destination. Implementing disassembly of this test module will create most of the structure for the entire project. td 2/16
    - Need stuff from Ross.
      * Will add next time. td 2/20
* Project Team Report 2
  + Notes: Due Saturday 2/20
  + Assigned: Thomas 2/19
  + Status:
    - Updated changes to Branch List table. td 2/19
    - Draft complete. td 2/20
* Continue creating branch, opcode, and subroutine logic layouts
  + Notes: Develop until basic idea of program flow is attained.
  + Assigned: Thomas 2/13
  + Status:
    - Removed Branch5 from Root Branch. td 2/19
    - Restructured Branch0. All paths complete. td 2/19
    - Restructured Branch4. All paths complete. td 2/19
    - Updating naming for Branch4E7. All paths complete. td 2/19
    - Completed all paths for Branch6. td 2/19
    - Completed all paths for Branch8. td 2/19
    - Completed all paths for Branch9. td 2/19
    - Completed all paths for BranchB. td 2/19
    - Completed all paths for BranchC. td 2/19
    - Completed all paths for BranchD. td 2/19
    - Completed all paths for BranchE. All Branch layouts complete, and all opcodes can be uniquely identified. td 2/19.
    - NOP opcode flowchart done. td 2/19
    - CLR opcode flowchart done. td 2/19
    - Drafted flowcharts for decoding size, registers, and effective addresses. td 2/19

Sprint 1

* Finish complete decode for assigned mnemonics and ea modes in Branch List spreadsheet.
  + Notes: Found a definite structure in the operational codes. This will reveal branches necessary in the program structure.
  + Assigned: Thomas 2/11
  + Status:
    - Finished organization of all assigned opcodes. td 2/12
    - Finished organization of all assigned ea modes. td 2/12
    - Done. td 2/12
* Create prototype disassembler client hardcoded to convert TestNOP.S68 machine code back into assembler.
  + Notes: Must decode NOP and SIMHALT machine code from address $7000
  + Blockers: Having difficulty printing hex addresses character by character. Using a character table to print each hex character, still trying to figure out how to set up the offsets for the table. rh 2/13 (resolved) rh 2/13
  + Assigned: Ross 2/12
  + Status:
    - Testcode uploaded. td 2/12
    - General program layout established. rh 2/13
    - Hardcoded versions of NOP and SIMHALT routines coded. rh 2/13
    - subroutine for printing hex addresses completed. rh 2/13
* Create flowchart for progress report detailing expected program structure and flow.
  + Notes: Due Saturday at 4:00pm.
  + Blockers: Requires prototype to be in progress so we better understand how this will work.
  + Status:
    - Creating branch structure for program layout. td 2/12
    - Root branch layout finished. td 2/13
    - 0 branch layout finished. td 2/13
    - 4 branch layout finished. td 2/13
    - 4E branch layout finished. td 2/13
    - 437 branch layout finished. td 2/13
    - MOVE.B opmode layout finished. td 2/13
    - NOP opmode layout finished. td 2/13
    - DECODE\_MODE subroutine layout finished. td 2/13