Milestone 6:

Sunday, February 11th, 2024
Individual Work(2hr)-
I worked on the Stage 3 and 4 implementation per the integration plan. I have been having some problems running testbenches in modelsim, but I worked today to get it working properly. I've been trying to be understanding when discussing work with Thomas, however I feel he has been a bit harsh toward Jada when she has been working according to our timeline. I am also a bit frustrated as I have been trying to get the team to follow the integration plan I wrote, but Thomas and Elenaor implemented the whole datapath instead causing us to have to backtrack.

Individual Work(3hr)-

I worked on debugging problems with the 3rd integration stage. There was a problem with bit sizes for the Compcode. I went ahead and fixed how that was addressed in the IR register and have updated our tb's accordingly. I am a bit worried about how meetings are working with our team. We have pretty much only been discussing the project over teams, while Thomas and Elenaor will meet in-person without discussing whole team meetings with Jada and I, causing discrepancies in expectations. I realize that I have been busy the past two milestones, but I would like to be kept in the loop regarding decisions made.

Monday, February 12th, 2024

Group work (1hr)-
Discussed expectations for the rest of the project and how we were going to finish the work required, especially since Thomas is leaving Wednesday afternoon and will not be available to work on the remainder of the project.

Group work (4.5 hrs)-
Worked on finishing stages and debugging for our integration with our final datapath. This involved a lot of debugging with bit-widths and checking sign-extension between stages and components. We also discussed how communication has been going with our team and some of the team conflicts that have happened more recently.

Tuesday, February 13th, 2024
Individual work (2hr)-

Worked with Thomas to update the assembler, and check its accuracy. I manually converted some instructions to machine code for Thomas to cross reference with the assembler's output. In doing this, I found an issue with BMEM where it required 18 bits instead of 16, so we made it ra-relative so that the immediate only took up 2 bytes.

Milestone 5:

This week I had tech week for theater and performances during the weekend. For Milestone 6, all we have to do is show that our final processor works and collect data about how it works?

Sunday, February 4th, 2024 (1hr)-
I looked over the design document for any discrepancies and errors.

Monday, February 5th, 2024 (1hr)-
I looked over the design document for any discrepancies and errors.

Milestone 4:

Friday, January 26th, 2024 (30 minutes)-

- Met with the team to discuss what we were going to do this milestone

Saturday, January 27th, 2024 (1hr)

- Worked on the SR and Compcode registers and their testbenches

Monday, January 29th, 2024 (3hr)

- Worked on the beginning of implementation plan

Its Tech week for theater this week so I've worked a bit less than usual.

Milestone 3:

Saturday January 20th, 2024- (20 minutes)

- Started work on unit tests

Sunday, January 21st, 2024- (2 hours)

- Continued designing unit tests for the components
- Noted small general competent discrepancies

Monday, January 22nd, 2024- (5 hours)

- Finished work on Unit tests
- Started work on integration plan and designed tests accordingly

Tuesday, January 23nd, 2024- (2 hours)

- Finished integration plan and reviewed other changes made by teammates

Milestone 2:

Friday January 12th, 2024- (150 minutes)

- Met with team to discuss progress and goals
- Worked with Eleanor to refine RTL chart and started work on generic components table
- Decided the initial operations for the ALU and their inputs/outputs and control signals.

Sunday, January 14, 2024- (20 minutes)

- I was unable to meet with the team due to a family emergency, but I looked over the design changes when I could.

Monday, January 15, 2024- (60 minutes)

- Updated the General Components Table for consistency and refined the ALU operations table.

Milestone 1:

Saturday, December 23, 2023 (90 minutes)

- Met with team to discuss instructions types and made the green sheet in google docs
- We also decided how we were going to break down the bits of the instruction and how to handle branching with a status flag

Thursday January 11th, 2024: (60 minutes)

- Added syntax descriptions for our instruction set
- Decided how the SR register and comp_code was formatted