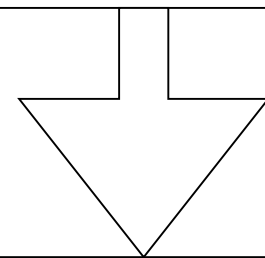
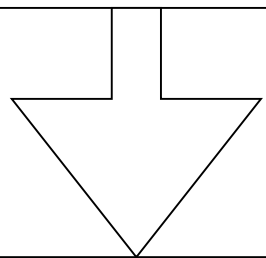


Logic data structure



Simulation

