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## ENEE350

## HW<sub>2</sub>

- 1.
- a. Direct Memory Access
- b. Two fatal bugs in the code:
  - The student seems to have implemented a polling interface for IO transfer, however the student *does not* check if there was, in fact, a character has been input on the computer. This can be fixed by adding a conditional branch after checking the keybd\_status register.
  - The student is working with byte-sized memory entries (i.e. 'lb,' 'sb'). This is fine for when the student loads keybd\_status into register \$t0, because only the last two bits (MSB) are important. However, when the student calls 'lb \$v0, keybd\_status' this will not load the entire keybd\_data memory word, and will result in reading something unexpected.

Fixed code: