

# Mr. Li Yang

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Research Interests: Multimodal learning, Machine Learning, LLM

## EDUCATIONAL BACKGROUND

### SOUTHWEST JIAOTONG UNIVERSITY(SWJTU) – LEEDS JOINT SCHOOL

Chengdu

B.Sc. Electronic and Electrical Engineering

Sept 2021-Present

- GPA: 89.2/100

### UNIVERSITY OF LEEDS

Leeds, West Yorkshire

B.Sc. Electronic and Electrical Engineering

Sept 2021- Present

- GPA: 3.64/4.00

## PUBLICATIONS

H Luo, Y Zeng, **L Yang**, K Chen, Z Shen, F Lv (2024). “**Vlai: Exploration and Exploitation Based on Visual-Language Aligned Information for Robotic Object Goal Navigation**” *Image and Vision Computing*, 2024: 105259.

**L, Yang**, M Xiong, F Lv, Tianrui Li, F Teng, A Kang, X, Qiu. 2024. **Helpfulness Prediction of Review Based on Multi-perspective Sub-label Enhancement**. China Patent 2024106622191, filed June 1, 2024, and issued September 5, 2024.

## RESEARCH PROJECTS

### E-commerce Platform Review Quality Analysis

Chengdu

Leader of School Student Research Project (Excellent Project Completion)

May 2023 - May 2024

- Analyzing and ranking the quality of e-commerce platform reviews using convolutional neural networks and attention mechanisms, with performance enhancement through sub-label supervision generated by CLIP.

### Candidate Label Pruning in Partial Label Learning via Machine Learning Method

Chengdu

Personal Research Project

Sep 2023 - Apr 2024

- Under the cross-entropy loss function, reducing false ground-truth labels in the candidate label set can significantly improve the efficiency of Partial Label learning.
- Using CLLP to find the cosine similarity between images and labels, and applying the KNN algorithm with this cosine similarity to replace low-similarity false ground-truth labels.

### Enhanced Word Embeddings to Mitigate the Impact of Spelling Errors on Chinese Large Models

Chengdu

Personal Research Project

Jun 2024 - Present

- Leveraging the characteristic that Chinese characters with similar pictographic features tend to share semantic and phonetic similarities, we designed word embeddings tailored for Chinese large language models that can effectively counteract the interference of spelling errors.

## INTERNSHIP EXPERIENCES

### North Central Network Chip Limited Company

Chengdu

Chip Verification Engineer

Jul 2022 – Aug 2022

- Gained in-depth knowledge of Verilog and System Verilog languages, along with familiarity with UVM verification platforms, report any error in chip designing, whilst collaborate to solve it in the team.
- Collaborated with a dedicated team to identify and resolve errors in the design of chips.

## AWARDS & HONOURS

**Service Outsourcing Competition**, National Third Prize, Ministry of Education of the PRC

Jul 2024

**17th China Undergraduate Computer Design Competition**, Province Second Prize

May 2024

**Championship** in the 2024 "Leeds Cup" Football Competition

Apr 2024

**Mingcheng Prize**

Oct 2023

**SWJTU-Leeds Joint School Third Class Scholarship**

Oct 2023

Won the **second place** in the 2022 "Leeds Cup" Football Competition

Apr 2022

Won **Third place** in the competition of the 2021 SWJTU "ICBC Cup" Freshman Football Tournament

Oct 2021

Won the **second place** in the 2021 SWJTU "Yunda Cup" Sports Festival Football Competition

Dec 2021

## EXTRACURRICULAR ACTIVITIES

**SWJTU - LEEDS JOINT SCHOOL Football Team Captain**

May 2022 - May 2024

## ADDITIONAL INFORMATION

**Skills:** C, C++, Python, LaTeX, Verilog, System Verilog

**Languages:** Mandarin (Native), English (IELTS – 6.5).

**Hobbies:** Football (Youth Training in Chengdu Football Association), Tennis (Youth Training), Guitar (Level 7 Certification), etc.