

## High Performance Schottky Rectifier, 3.0 A



**SMC (DO-214AB)** 

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	3.0 A			
V <sub>R</sub>	40 V			
V <sub>F</sub> at I <sub>F</sub>	0.46 V			
I <sub>RM</sub>	30 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
E <sub>AS</sub>	6.0 mJ			
Package	SMC (DO-214AB)			
Circuit configuration	Single			

#### **FEATURES**





Guard ring for enhanced ruggedness and long term reliability

ROHS COMPLIANT HALOGEN FREE

• Small foot print, surface mountable

- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-30BQ040HM3 surface-mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I <sub>F(AV)</sub>	Rectangular waveform	3.0	А	
V <sub>RRM</sub>		40	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1600	A	
V <sub>F</sub>	3.0 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.46	V	
TJ	Range	-55 to +150	°C	

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-30BQ040HM3	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	40	V
Maximum working peak reverse voltage	$V_{RWM}$	40	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDIT	TEST CONDITIONS		UNITS
Maximum average forward current	Marian management and a second		50 % duty cycle at T <sub>L</sub> = 115 °C, rectangular waveform		
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>L</sub> = 104 °C, rectangular waveform		4.0	
Maximum peak one cycle	la	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	1600	Α
non-repetitive surge current		10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	90	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.0 A, L = 12 mH		6.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1.0	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		3 A	T <sub>J</sub> = 25 °C	0.57	V
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	6 A		0.76	
Maximum forward voltage drop	V <sub>FM</sub> (')	3 A	T <sub>J</sub> = 125 °C	0.46	
		6 A		0.64	
Maximum reverse leakage current	1	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.5	mA
Maximum reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 125 °C	VR = nated VR	30	IIIA
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		230	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		3.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $^{(1)}\,$  Pulse width = 300  $\mu s,$  duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> <sup>(2)</sup>	DC appretion	12	· °C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	46	
Approximate weight			0.24	g
Approximate weight			0.008	oz.
Marking device		Case style SMC (DO-214AB)	3	F

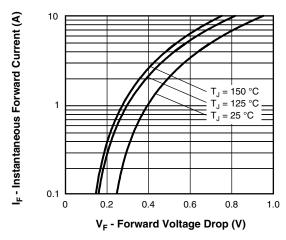
#### Notes

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB









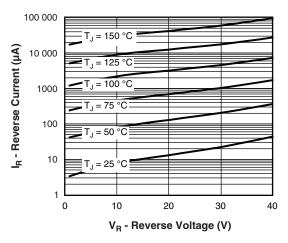


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

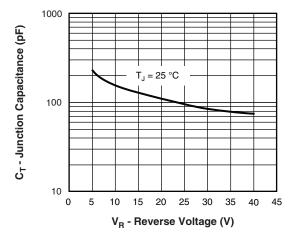


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

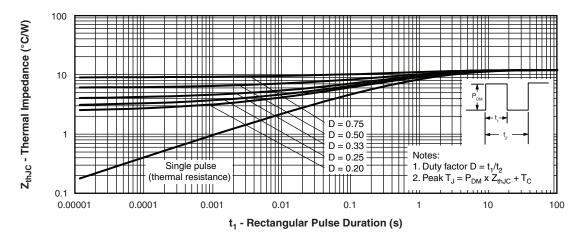


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)



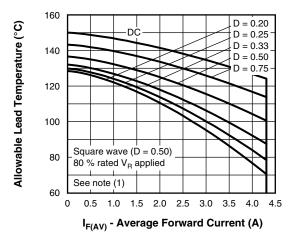


Fig. 5 - Maximum Average Forward Current vs.
Allowable Lead Temperature

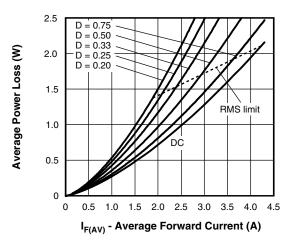


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

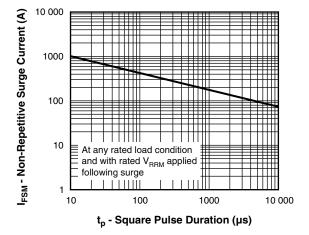


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

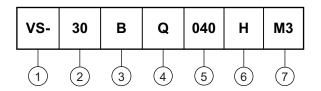
#### Note

(1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 



#### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

2 - Current rating

3 - B = SMC

4 - Q = Schottky "Q" series

5 - Voltage rating (040 = 40 V)

6 - H = AEC-Q101 qualified

7 - Environmental digit:

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-30BQ040HM3/9AT	9AT	3500	13" diameter plastic tape and reel	

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95402</u>				
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			
SPICE model	www.vishay.com/doc?96601			



## **SMC**

### **DIMENSIONS** in inches (millimeters)

#### **DO-214AB (SMC)**



#### **Mounting Pad Layout**





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Vishay

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