



1. Description

1.1. Project

Project Name	ICM42688_SPI
Board Name	NUCLEO-G474RE
Generated with:	STM32CubeMX 6.12.0
Date	09/13/2024

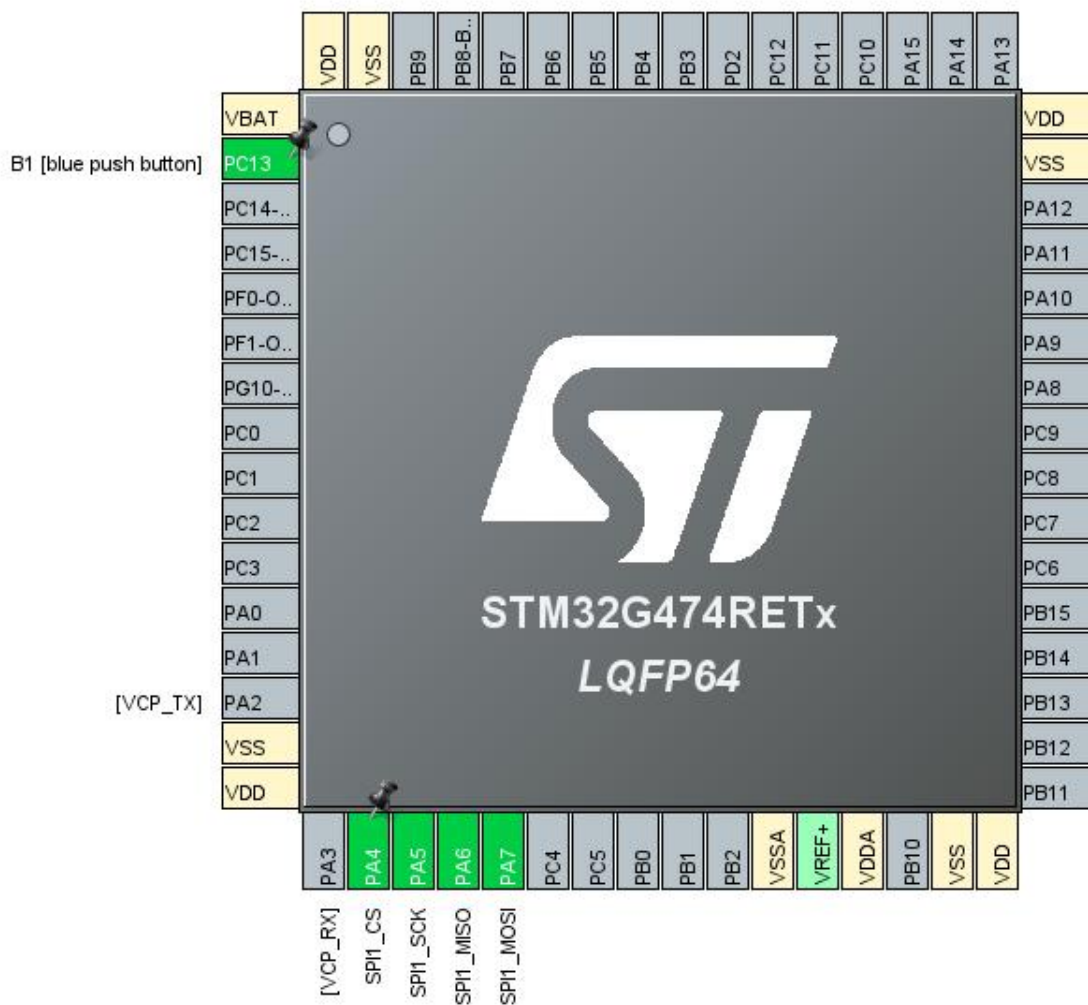
1.2. MCU

MCU Series	STM32G4
MCU Line	STM32G4x4
MCU name	STM32G474RETx
MCU Package	LQFP64
MCU Pin number	64

1.3. Core(s) information

Core(s)	ARM Cortex-M4
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2. Pinout Configuration



3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
2	PC13	I/O	GPIO_EXTI13	B1 [blue push button]
15	VSS	Power		
16	VDD	Power		
18	PA4 *	I/O	GPIO_Output	SPI1_CS
19	PA5	I/O	SPI1_SCK	
20	PA6	I/O	SPI1_MISO	
21	PA7	I/O	SPI1_MOSI	
27	VSSA	Power		
29	VDDA	Power		
31	VSS	Power		
32	VDD	Power		
47	VSS	Power		
48	VDD	Power		
63	VSS	Power		
64	VDD	Power		

* The pin is affected with an I/O function



1. Power Consumption Calculator report

1.1. Microcontroller Selection

Series	STM32G4
Line	STM32G4x4
MCU	STM32G474RETx
Datasheet	DS12288_Rev0

1.2. Parameter Selection

Temperature	25
Vdd	3.0

1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

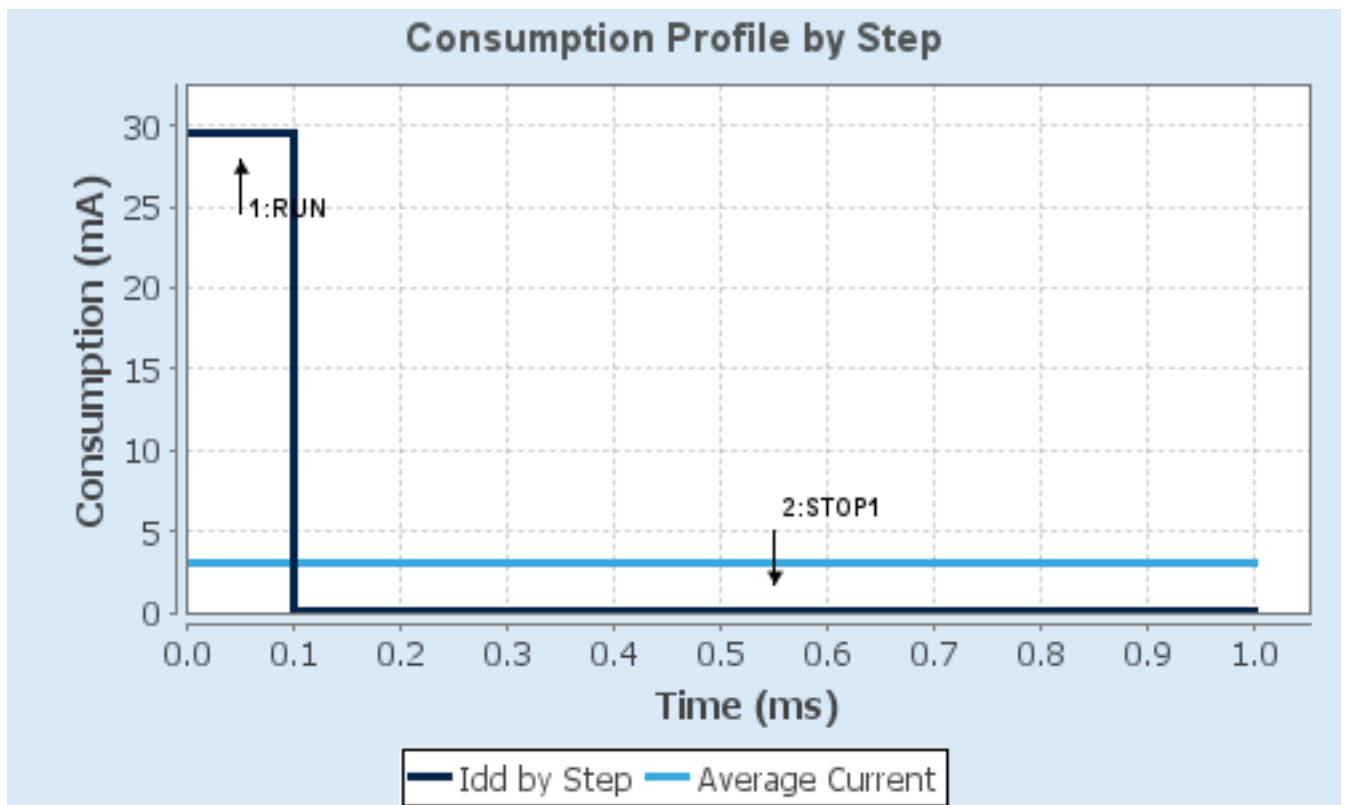
1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP1
Vdd	3.0	3.0
Voltage Source	Battery	Battery
Range	Range1-Boost	NoRange
Fetch Type	FLASH/DualBank/ART	NA
CPU Frequency	170 MHz	0 Hz
Clock Configuration	HSE BYP PLL	ALL CLOCKS OFF
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	29.5 mA	80.5 μ A
Duration	0.1 ms	0.9 ms
DMIPS	213.0	0.0
Ta Max	124.25	129.98
Category	In DS Table	In DS Table

1.5. Results

Sequence Time	1 ms	Average Current	3.02 mA
Battery Life	1 month, 16 days, 9 hours	Average DMIPS	212.5 DMIPS

1.6. Chart



2. Software Project

2.1. Project Settings

Name	Value
Project Name	ICM42688_SPI
Project Folder	C:\Users\physik\STM32CubeIDE\workspace_1.16.0\ICM42688_SPI
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_G4 V1.6.0
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_SPI1_Init	SPI1

3. Peripherals and Middlewares Configuration

3.1. NUCLEO-G474RE

mode: Human Machine Interface

3.1.1. Human Machine Interface:

Led:

USER LED GREEN (LD2) false

Button:

USER B1 Disable

VCOM:

Virtual Com Port **true ***

Demonstration code:

Generate demonstration code Disabled

3.2. NUCLEO-G474RE

mode: Human Machine Interface

3.2.1. Human Machine Interface:

Led:

USER LED GREEN (LD2) false

Button:

USER B1 Disable

VCOM:

Virtual Com Port **true ***

Demonstration code:

Generate demonstration code Disabled

3.3. RCC

3.3.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3

Instruction Cache Enabled

Prefetch Buffer Disabled

Data Cache Enabled

Flash Latency(WS)	4 WS (5 CPU cycle)
RCC Parameters:	
HSI Calibration Value	64
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000
Power Parameters:	
Power Regulator Voltage Scale	Power Regulator Voltage Scale 1 boost
Peripherals Clock Configuration:	
Generate the peripherals clock configuration	TRUE

3.4. SPI1

Mode: Full-Duplex Master

3.4.1. Parameter Settings:

Basic Parameters:	
Frame Format	Motorola
Data Size	8 Bits *
First Bit	MSB First
Clock Parameters:	
Prescaler (for Baud Rate)	32 *
Baud Rate	5.3125 MBits/s *
Clock Polarity (CPOL)	High *
Clock Phase (CPHA)	2 Edge *
Advanced Parameters:	
CRC Calculation	Disabled
NSS Signal Type	Software

3.5. SYS

Timebase Source: SysTick

mode: save power of non-active UCPD - deactive Dead Battery pull-up

* User modified value

4. System Configuration

4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PA6	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PA7	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Low	
GPIO	PC13	GPIO_EXTI13	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	B1 [blue push button]
	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	High *	SPI1_CS

4.2. DMA configuration

nothing configured in DMA service

4.3. NVIC configuration

4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
EXTI line[15:10] interrupts	true	0	0
PVD/PVM1/PVM2/PVM3/PVM4 interrupts through EXTI lines 16/38/39/40/41	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
SPI1 global interrupt	unused		
FPU global interrupt	unused		

4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
EXTI line[15:10] interrupts	false	true	true

* User modified value

5. System Views

5.1. Category view

5.1.1. Current

Middleware

System Core

Analog

Timers

Connectivity

Multimedia

Security

Computing

Utilities

Bsp

DMA

SPI1

NUCLEO-G474RE

GPIO

NVIC

RCC

SYS

6. Docs & Resources

Type	Link
BSDL files	https://www.st.com/resource/en/bsdl_model/stm32g4_bsd.zip
IBIS models	https://www.st.com/resource/en/ibis_model/stm32g4_ibis.zip
System View Description	https://www.st.com/resource/en/svd/stm32g4_svd.zip
Presentations	https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf
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